

PR3001 - PR3007 FAST RECOVERY RECTIFIER DIODES

VOLTAGE RANGE: 50 - 1000V CURRENT: 3.0 A

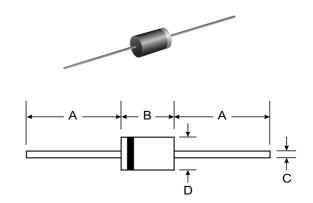
Features

- Glass Passivated Die Construction
- Diffused Junction
- Fast Switching for High Efficiency
- High Current Capability and Low Forward Voltage Drop
- Low Reverse Leakage Current
- Plastic Material: UL Flammability Classification Rating 94V-0

Mechanical Data

- Case:DO-201AD, Molded Plastic
- Terminals: Plated Leads Solderable per MIL-STD-202, Method 208
- Polarity: Cathode Band
- Marking: Type Number
- Weight: 1.12 grams (approx.)





DO-201AD							
Dim	Min	Max					
Α	25.40	_					
В	7.20	9.50					
С	1.20	1.30					
D	4.80	5.30					
All Dimensions in mm							

Maximum Ratings and Electrical Characteristics @ TA = 25°C unless otherwise specified

Single phase, half wave, 60Hz, resistive or inductive load. For capacitive load, derate current by 20%.

Characteristic		Symbol	PR 3001	PR 3002	PR 3003	PR 3004	PR 3005	PR 3006	PR 3007	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage		V _{RRM} V _{RWM} V _R	50	100	200	400	600	800	1000	V
RMS Reverse Voltage		V _{R(RMS)}	35	70	140	280	420	560	700	٧
Average Rectified Output Current (Note 1)	@ T _A = 55°C	Io	3.0					Α		
Non-Repetitive Peak Forward Surge Current 8.3ms Single half sine-wave Superimposed on Rated Load (JEDEC Method)		I _{FSM}	125							Α
Forward Voltage	@ I _F = 3.0A	V _{FM}	1.3						٧	
Peak Reverse Current at Rated DC Blocking Voltage	@ T _A = 25°C @ T _A = 125°C	I _{RM}	5.0 100						μА	
Reverse Recovery Time (Note 3)		t _{rr}	150		250		500		ns	
Typical Junction Capacitance (Note 2)		Cj	50							pF
Typical Thermal Resistance Junction to Ambient		$R_{\theta JA}$	32						K/W	
Operating and Storage Temperature Range		T _{j,} T _{STG}	-65 to +150						°C	

Notes:

- 1. Valid provided that leads are maintained at ambient temperature at a distance of 9.5mm from the case.
- 2. Measured at 1.0MHz and applied reverse voltage of 4.0V DC.
- 3. Measured with I_F = 0.5A, I_R = 1A, I_{rr} = 0.25A. See figure 5.



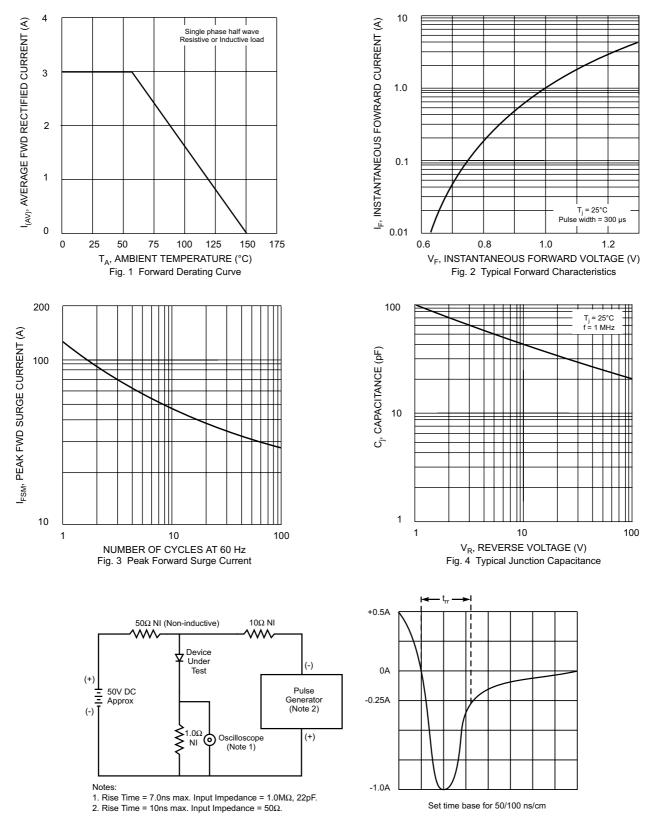


Fig. 5 Reverse Recovery Time Characteristic and Test Circuit