

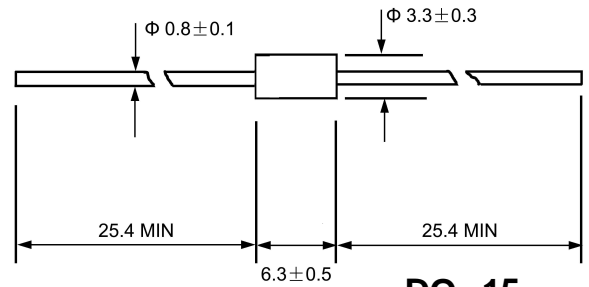
General Description

A sidac is a silicon bilateral voltage triggered switch, with greater power-handling capabilities than standard diacs. Upon application of a voltage exceeding the Sidac breakover voltage point, the Sidac switches on, through a negative resistance region, to a low on-state voltage. Conduction will continue until the current is interrupted or drops below the minimum holding current of the device.

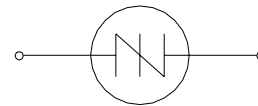
Switching voltages in the range of 95 V to 330 V.

Sidacs feature glass-passivated junctions that ensure long term reliability and stable characteristics by creating a rugged, reliable barrier against junction contamination.

Variations of devices covered in this data sheet are available for custom design applications. Please consult the factory for more information.


DO - 15

Dimensions in millimeters



Type	$I_{T(RMS)}$	V_{DRM}	V_{BO}		I_{DRM}	I_{BO}	I_H		V_{TM}	I_{TSM}		R_S	dv/dt	di/dt
			(1)				(3) (4)							
	A	V	V		μA	μA	mA		V	A		k	V/ μ Sec	A/ μ Sec
	MAX	MIN	MIN	MAX	MAX	MAX	TYP	MAX	Max	60Hz	50Hz	MIN	MIN	TYP
K1050G	1	± 90	95	113	5	10	60	150	1.5	20	16.7	0.1	1500	150
K1100G	1	± 90	104	118	5	10	60	150	1.5	20	16.7	0.1	1500	150
K1200G	1	± 90	110	125	5	10	60	150	1.5	20	16.7	0.1	1500	150
K1300G	1	± 90	120	138	5	10	60	150	1.5	20	16.7	0.1	1500	150
K1400G	1	± 90	130	146	5	10	60	150	1.5	20	16.7	0.1	1500	150
K1500G	1	± 90	140	170	5	10	60	150	1.5	20	16.7	0.1	1500	150
K2000G	1	± 180	190	215	5	10	60	150	1.5	20	16.7	0.1	1500	150
K2200G	1	± 180	205	230	5	10	60	150	1.5	20	16.7	0.1	1500	150
K2400G	1	± 190	220	250	5	10	60	150	1.5	20	16.7	0.1	1500	150
K2500G	1	± 200	240	280	5	10	60	150	1.5	20	16.7	0.1	1500	150
K2501G	1(10)	± 200	240	280	5	75	60	150	6	20	16.7	0.1	1500	150



CASE/LEAD TEMPERATURE

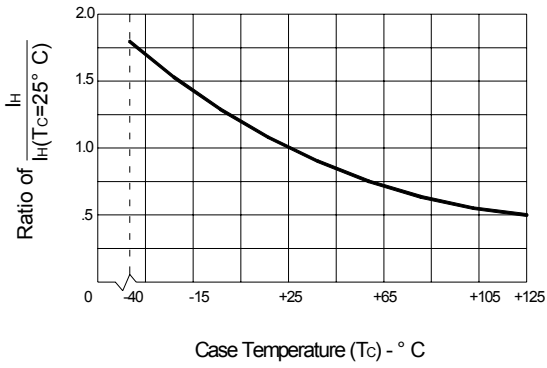


FIG. 4 -- MAXIMUM ALLOWABLE AMBIENT TEMPERATURE vs ON-STATE CURRENT

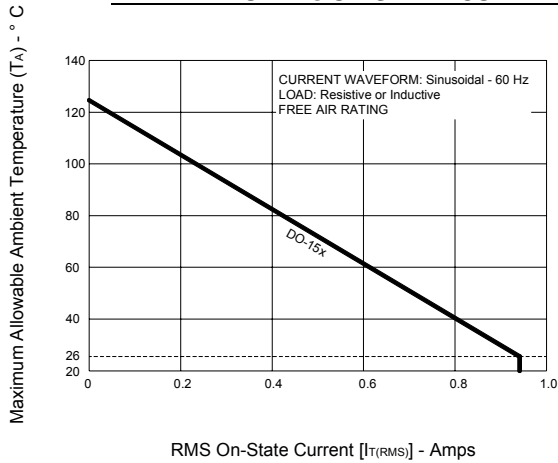


FIG. 6 -- NORMALIZED REPETITIVE PEAK BREAKOVER CURRENT vs JUNCTION TEMPERATURE

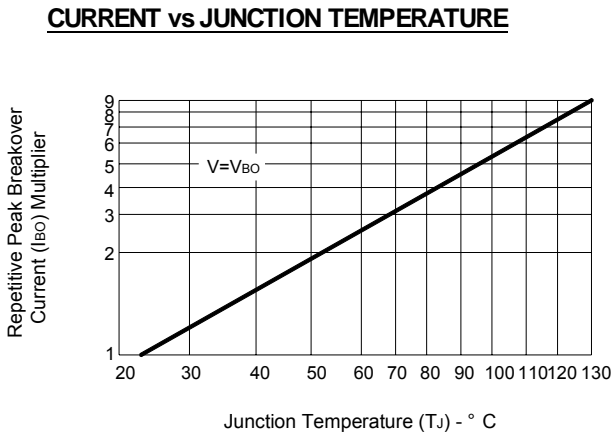


FIG. 3-- REPETITIVE PEAK ON-STATE CURRENT (I_{TRM}) vs PULSE WIDTH at VARIOUS FREQUENCIES

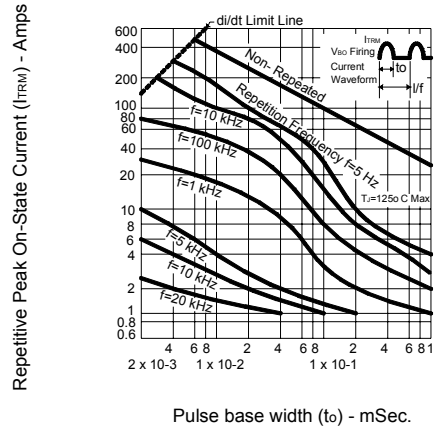


FIG. 5 -- NORMALIZED V_{BO} CHANGE vs JUNCTION TEMPERATURE

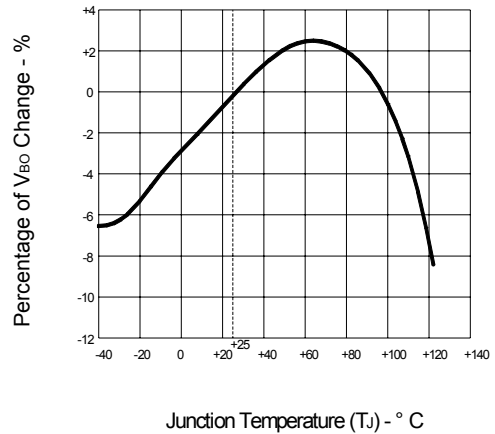


FIG. 7 -- ON-STATE CURRENT vs ON-STATE VOLTAGE (TYPICAL)

