

Parameters Subject to Change Without Notice

DESCRIPTION

JW7707C is a synchronous rectifier for Flyback converters. It integrates a 60V power MOSFET that can replace Schottky diode for high efficiency. It turns on the internal MOSFET if the $V_{SW} < -500mV$ and turns it off before the current from GND to SW is lower than zero.

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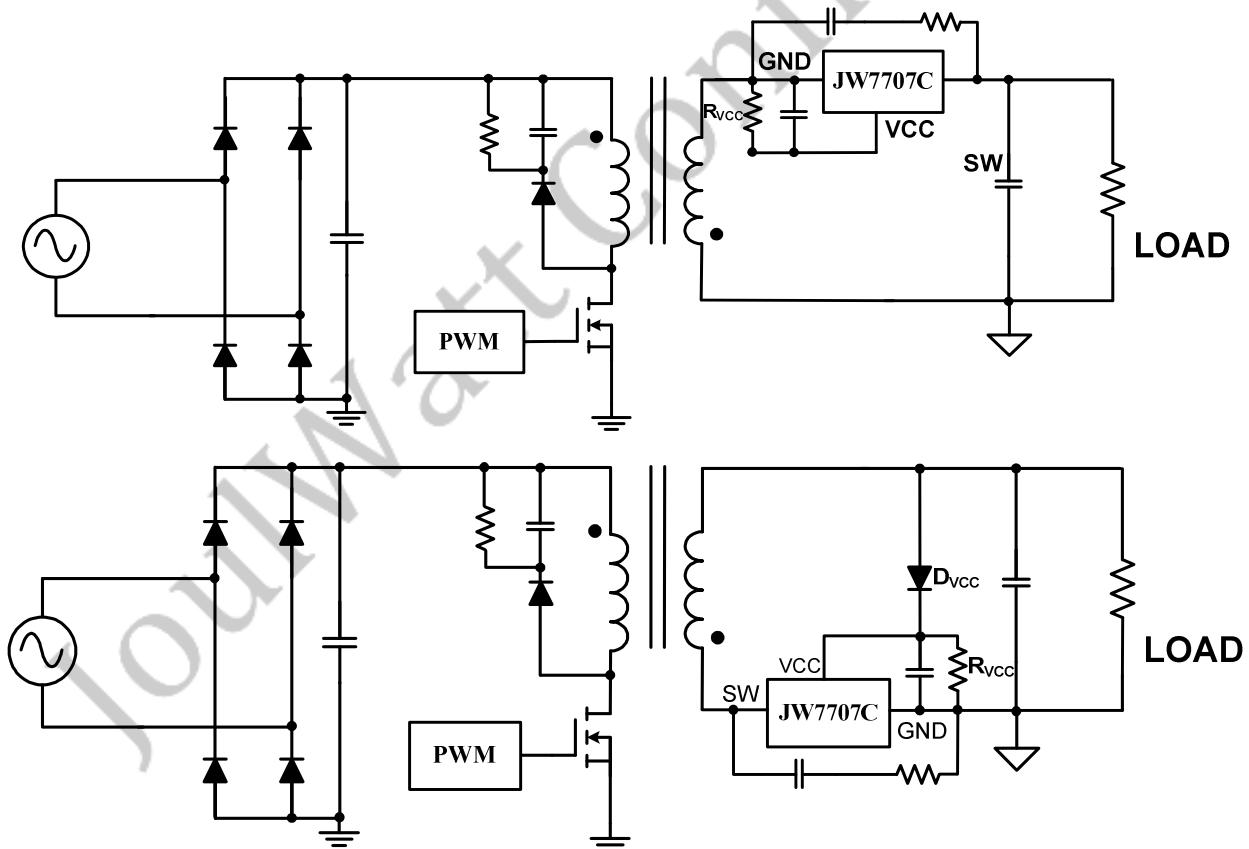
FEATURES

- Supports DCM and Quasi-Resonant Flyback converter
- Integrated 13mΩ 60V Power MOSFET
- Supports High-side and Low-side Rectification
- No need external power supply

APPLICATIONS

- Flyback converters
- Adaptors

TYPICAL APPLICATION



Note 1: R_{VCC} is recommended in case IC is damaged in CCM.

Note 2: D_{VCC} is recommended if VCC voltage is too low in light load.

ORDER INFORMATION

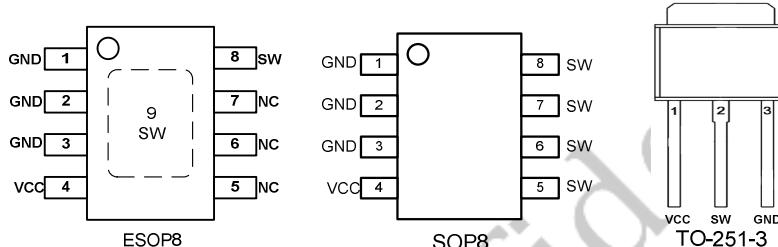
LEAD FREE FINISH	TAPE AND REEL	PACKAGE	TOP MARKING
JW7707CESOP#PBF	JW7707CESOP#TRPBF	ESOP8	JW7707C
JW7707CSOPB#PBF	JW7707CSOPB#TRPBF	SOP8	JW7707C
JW7707CTOH#PBF	JW7707CTOH#TRPBF	TO251-3	JW7707C

Note:

JWXXXXPPPF#TRPBF
 └── Part Number
 └── Pb Free
 └── Tape and Reel (If "TR" is not shown, it means Tube)
 └── Package Code

PIN CONFIGURATION

TOP VIEW



ABSOLUTE MAXIMUM RATING¹⁾

SW PIN	60V
VCC PIN	24V
Maximum Power Dissipation ²⁾	2.5W
Junction Temperature ³⁾	150°C
Lead Temperature	260°C
Storage Temperature.....	-65°C to 150°C

RECOMMENDED OPERATING CONDITIONS

SW Pin.....	20V to 55V
VCC PIN.....	.7V to 9V
Operation Junction Temp.	-40°C to 125°C

THERMAL PERFORMANCE⁴⁾

	θ_{JA}	θ_{Jc}
ESOP8	50	...10°C/W
SOP8	96	...45°C/W
TO251-3.....	62	...1.45°C/W

Note:

- 1) Exceeding these ratings may damage the device.
- 2) TA=25°C. The maximum allowable power dissipation is a function of the maximum junction temperature T_J(MAX), the junction-to-ambient thermal resistance θ_{JA} , and the ambient temperature T_A. The maximum allowable continuous power dissipation at any ambient temperature is calculated by P_D(MAX)=(T_J(MAX)-T_A)/ θ_{JA} . Exceeding the maximum allowable power dissipation will cause excessive die temperature, and the regulator will go into thermal shutdown. Internal thermal shutdown circuitry protects the device from permanent damage.
- 3) Measured on JESD51-7, 4-layer PCB.

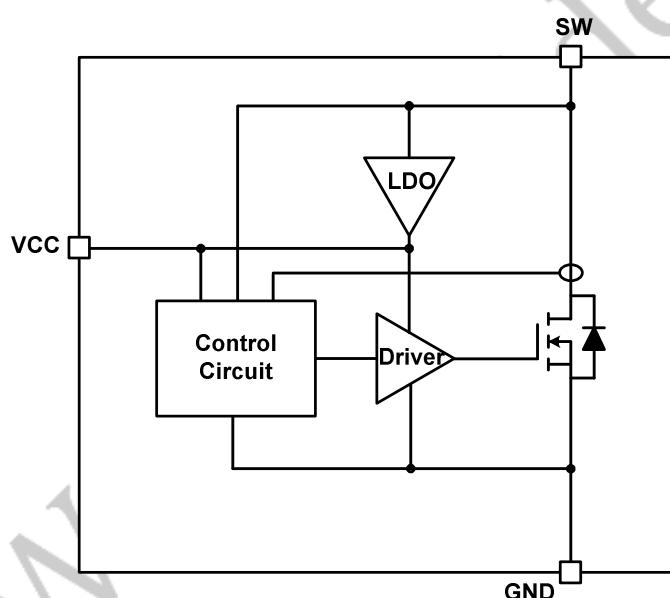
ELECTRICAL CHARACTERISTICS

<i>TA = 25°C, unless otherwise stated.</i>						
Item	Symbol	Condition	Min.	Typ.	Max.	Units
VCC Section						
VCC Operation Voltage	V_{CC}	SW=40V, VCC=0.1uF	7.4	7.75	8.1	V
VCC Start up Voltage	$V_{CC_STAR\ UP}$		3.6	3.8	4.0	V
VCC UVLO	V_{CC_UVLO}		3.5	3.65	3.8	V
Quiescent Current	I_Q	$V_{CC}=6.5V, C_{VCC}=0.1uF$	70	85	100	uA
Internal MOS Section						
Internal MOS R_{dson}	R_{dson}	$V_{CC}=8.5V, I_{sw}=8A$		13	20	mΩ
Maximum Peak Current	I_{peak}	$T_J=25^\circ C$		58		A
Drain Current-continuous	I_D	$T_J=25^\circ C$		20		A
Internal MOS turn on delay	T_{DON}			24		nS
Internal MOS turn off delay	T_{DOFF}			10		nS
Internal MOS turn on minimum time	T_{ON_MIN}			1		uS
Internal MOS turn off minimum time	T_{OFF_MIN}		2.5	3.5		uS
SW Section						
Internal MOS turn on Threshold	V_{MOS_ON}			-0.5		V
SW to GND Breakdown Voltage	$V_{(BR)DSS}$	$V_{CC}=9V, I_{sw}=250uA$	60			V

PIN DESCRIPTION

Pin No. ESOP8	Name	Description
1、2、3	GND	Ground
4	VCC	Power supply, Bypass a capacitor between VCC and GND.
5、6、7	NC	Not Connected.
8、9	SW	Internal Power MOSFET Drain.

BLOCK DIAGRAM



FUNCTIONAL DESCRIPTION

Operation

JW7707C is a synchronous rectifier, it can replace the Schottky to improve the efficiency in Flyback converters. It supports operation in DCM and Quasi-Resonant Flyback converters. It can power itself through the internal LDO during the turn-off period, a 0.1uF capacitor is needed between VCC and GND.

Turn-on Blanking Time

The control circuitry contains a blanking function. When the internal MOSFET is turned on, it at least last for some time, the turn on blanking time is about 1uS. During the turn on blanking period, the turn off threshold is not totally blanked, but changes the threshold current. This assures that the internal MOSFET can always be turned off even during the blanking period.

Under-Voltage Lockout (UVLO)

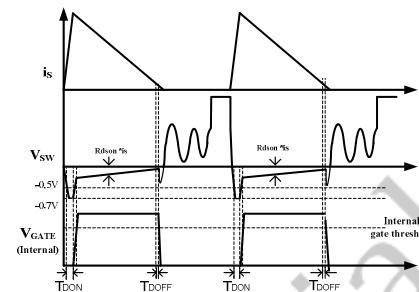
When the VCC is below UVLO threshold, the internal MOSFET is turned off and never turned on before the VCC exceeds the startup voltage.

Turn On Phase

The switch current first flows through the body diode of integrate MOSFET, which generates a negative V_{SW} . When the V_{SW} is higher than 0.7V and then V_{SW} is lower than V_{MOS_ON} , it turns on the integrate MOSFET after 24ns delay.

Turn Off Phase

The JW7707C senses the current of the internal MOSFET I_{SW} , before I_{SW} is lower than Internal MOS turn off threshold, the driver voltage of the switch is pulled down to zero after 10ns delay.



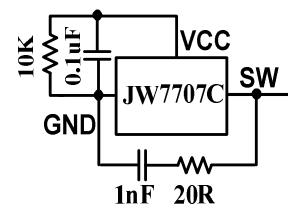
Turn on and turn off delay

Startup

During the startup period, when the VCC is lower than startup voltage, the internal MOSFET is turned off. The current flows though body diode until the VCC exceeds the startup voltage.

RC Snubber Circuit

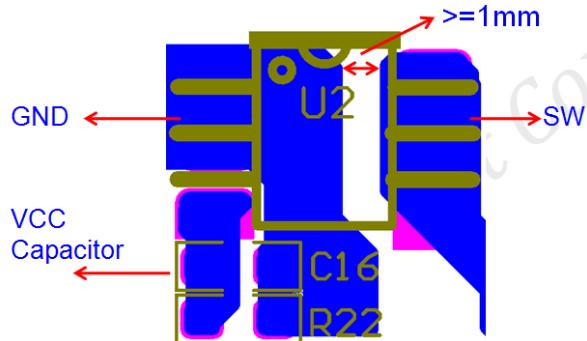
In some applications (output short circuit protection), the inductor current may go into slight CCM condition. To avoid the voltage spike across the synchronous rectifier, we suggest RC snubber should be placed between SW and GND, and a resistor should be paralleled with VCC capacitor.



RC Snubber circuit

PCB Layout Guidelines

1. The VCC pin must be locally bypassed with a capacitor.
2. The PAD must be connected to main power loop.
3. NC pins can be connected to SW pins for ESOP package.
4. The distance between GND and SW should be more than 1mm in case of too much coupled noise.
5. The EUT, a 5V/2A adaptor⁴⁾, can pass \pm 25KV air discharging test by following with the recommended layout .

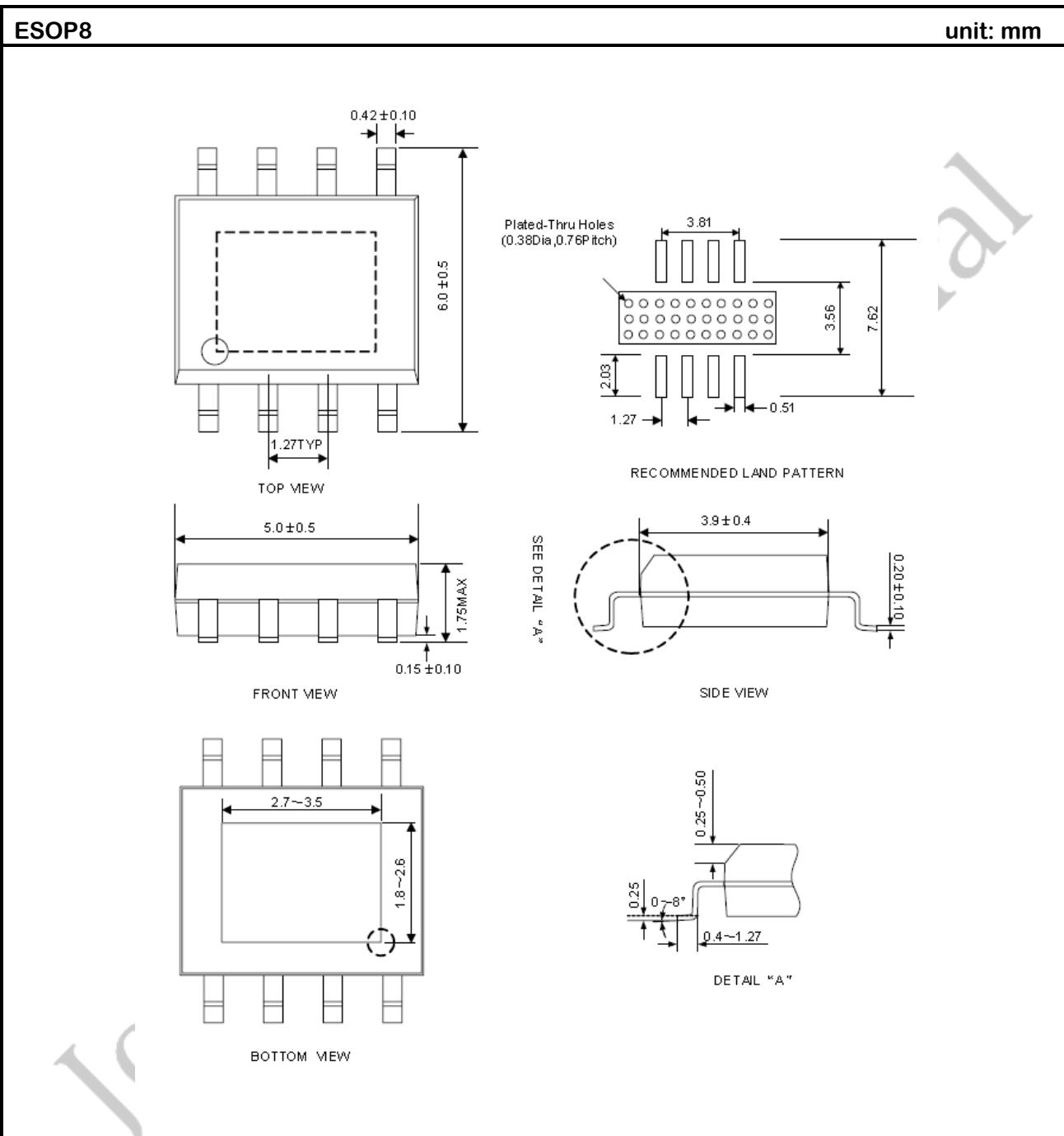


The recommended layout

Note:

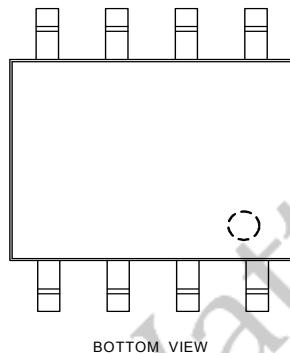
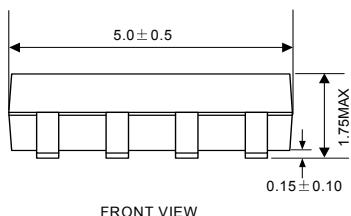
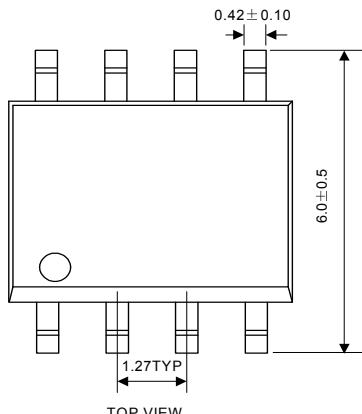
- 4) ESD pass criterion is $\pm 15\text{KV}$ air discharging according to IEC61000-4-2. And the test equipment is TESEQ NSG 438.

PACKAGE OUTLINE

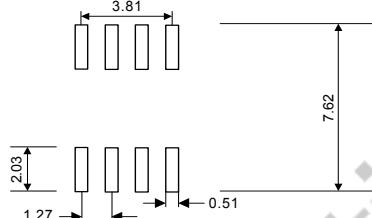


SOP8

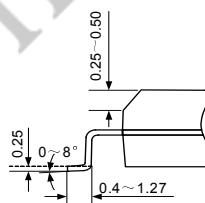
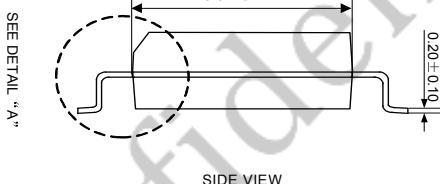
UNIT: mm



BOTTOM VIEW



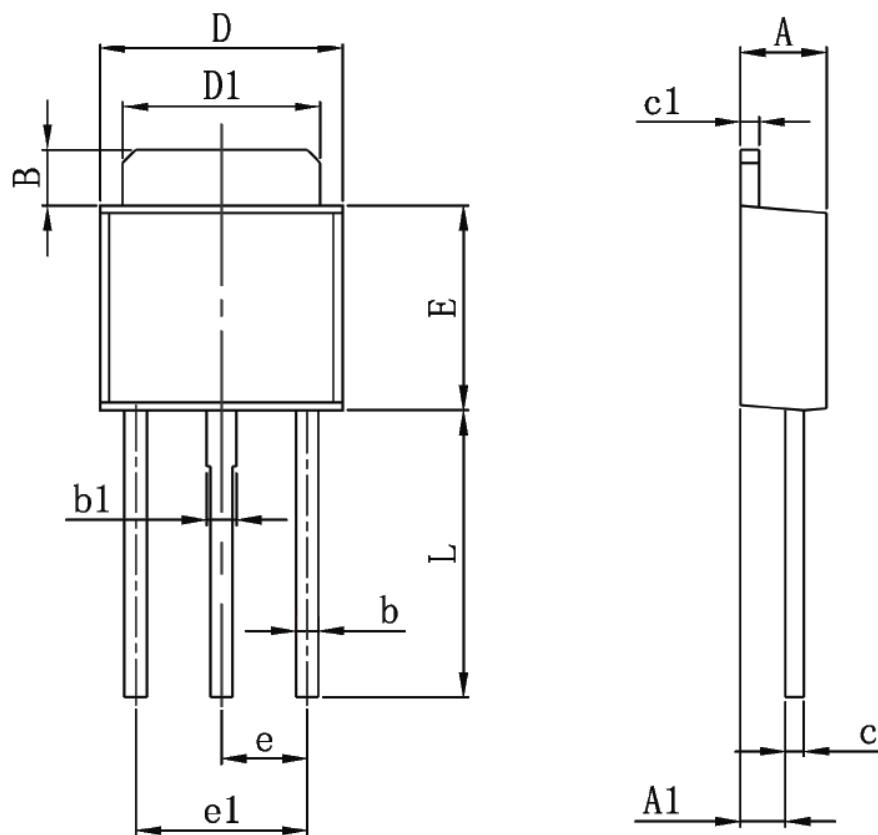
RECOMMENDED LAND PATTERN



DETAIL "A"

TO251-3

UNIT: mm



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	2.200	2.400	0.087	0.094
A1	1.050	1.350	0.042	0.054
B	1.350	1.650	0.053	0.065
b	0.500	0.700	0.020	0.028
b1	0.700	0.900	0.028	0.035
c	0.430	0.580	0.017	0.023
c1	0.430	0.580	0.017	0.023
D	6.350	6.650	0.250	0.262
D1	5.200	5.400	0.205	0.213
E	5.400	5.700	0.213	0.224
e	2.300 TYP.		0.091 TYP.	
e1	4.500	4.700	0.177	0.185
L	7.500	7.900	0.295	0.311

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