

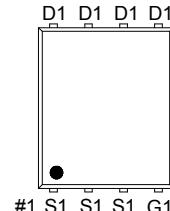
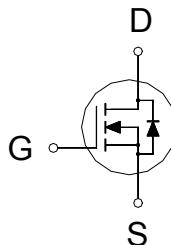
NIKO-SEM**N-Channel Enhancement Mode Field
Effect Transistor****P0303BKA-A**

PDFN 5x6P

Halogen-Free & Lead-Free

PRODUCT SUMMARY

$V_{(BR)DSS}$	$R_{DS(ON)}$	I_D
30V	3.3mΩ	92A



G. GATE
D. DRAIN
S. SOURCE

**ABSOLUTE MAXIMUM RATINGS ($T_A = 25^\circ\text{C}$ Unless Otherwise Noted)**

PARAMETERS/TEST CONDITIONS	SYMBOL	LIMITS	UNITS
Drain-Source Voltage	V_{DS}	30	V
Gate-Source Voltage	V_{GS}	± 20	V
Continuous Drain Current ³	I_D	92	A
$T_C = 100^\circ\text{C}$	I_D	74	
Pulsed Drain Current ¹	I_{DM}	180	A
Continuous Drain Current	I_D	19	
$T_A = 25^\circ\text{C}$	I_D	15	
$T_A = 70^\circ\text{C}$	I_{AS}	58.5	
Avalanche Current	E_{AS}	170	mJ
Avalanche Energy	$T_C = 25^\circ\text{C}$	48	W
Power Dissipation	P_D	19	
Power Dissipation	$T_A = 25^\circ\text{C}$	2.1	W
	$T_A = 70^\circ\text{C}$	1.4	
Operating Junction & Storage Temperature Range	T_j, T_{stg}	-55 to 150	°C

THERMAL RESISTANCE RATINGS

THERMAL RESISTANCE	SYMBOL	TYPICAL	MAXIMUM	UNITS
Junction-to-Ambient ²	$R_{\theta JA}$	57	2.6	°C / W
Junction-to-Case	$R_{\theta JC}$			

¹Pulse width limited by maximum junction temperature.

²The value of $R_{\theta JA}$ is measured with the device mounted on 1in² FR-4 board with 2oz. Copper, in a still air environment with $T_A = 25^\circ\text{C}$. The value in any given application depends on the user's specific board design.

³Package limitation current is 78A

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ELECTRICAL CHARACTERISTICS ($T_J = 25^\circ\text{C}$, Unless Otherwise Noted)

PARAMETER	SYMBOL	TEST CONDITIONS	LIMITS			UNIT
			MIN	TYP	MAX	
STATIC						
Drain-Source Breakdown Voltage	$V_{(\text{BR})\text{DSS}}$	$V_{\text{GS}} = 0\text{V}, I_D = 250\mu\text{A}$	30			V
Gate Threshold Voltage	$V_{\text{GS}(\text{th})}$	$V_{\text{DS}} = V_{\text{GS}}, I_D = 250\mu\text{A}$	1	1.6	3	
Gate-Body Leakage	I_{GSS}	$V_{\text{DS}} = 0\text{V}, V_{\text{GS}} = \pm 20\text{V}$			± 100	nA
Zero Gate Voltage Drain Current	I_{DSS}	$V_{\text{DS}} = 24\text{V}, V_{\text{GS}} = 0\text{V}$			1	
		$V_{\text{DS}} = 20\text{V}, V_{\text{GS}} = 0\text{V}, T_J = 55^\circ\text{C}$			10	μA
Drain-Source On-State Resistance ¹	$R_{\text{DS}(\text{ON})}$	$V_{\text{GS}} = 4.5\text{V}, I_D = 16\text{A}$		4.4	4.9	
		$V_{\text{GS}} = 10\text{V}, I_D = 20\text{A}$		2.7	3.3	$\text{m}\Omega$
Forward Transconductance ¹	g_{fs}	$V_{\text{DS}} = 5\text{V}, I_D = 20\text{A}$		65		S
DYNAMIC						
Input Capacitance	C_{iss}	$V_{\text{GS}} = 0\text{V}, V_{\text{DS}} = 15\text{V}, f = 1\text{MHz}$		2580		pF
Output Capacitance	C_{oss}			430		
Reverse Transfer Capacitance	C_{rss}			290		
Gate Resistance	R_g	$V_{\text{GS}} = 0\text{V}, V_{\text{DS}} = 0\text{V}, f = 1\text{MHz}$		0.85		Ω
Total Gate Charge ²	Q_g	$V_{\text{GS}} = 10\text{V}$		55		nC
		$V_{\text{GS}} = 4.5\text{V}$		26		
Gate-Source Charge ²	Q_{gs}	$V_{\text{DS}} = 0.5V_{(\text{BR})\text{DSS}}, V_{\text{GS}} = 10\text{V}, I_D = 20\text{A}$		9.5		nS
Gate-Drain Charge ²	Q_{gd}			12.5		
Turn-On Delay Time ²	$t_{\text{d}(\text{on})}$			20		
Rise Time ²	t_r			8.2		
Turn-Off Delay Time ²	$t_{\text{d}(\text{off})}$	$V_{\text{DS}} = 15\text{V}, I_D \approx 1\text{A}, V_{\text{GS}} = 10\text{V}, R_{\text{GEN}} = 6\Omega$		115		nS
Fall Time ²	t_f			15.7		
SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS ($T_J = 25^\circ\text{C}$)						
Continuous Current ³	I_S				92	A
Forward Voltage ¹	V_{SD}	$I_F = 20\text{A}, V_{\text{GS}} = 0\text{V}$			1	V
Reverse Recovery Time	t_{rr}	$I_F = 20\text{A}, dI_F/dt = 100\text{A}/\mu\text{s}$		32		nS
Reverse Recovery Charge	Q_{rr}			18		nC

¹Pulse test : Pulse Width $\leq 300\ \mu\text{sec}$, Duty Cycle $\leq 2\%$.²Independent of operating temperature.³Package limitation current is 78A

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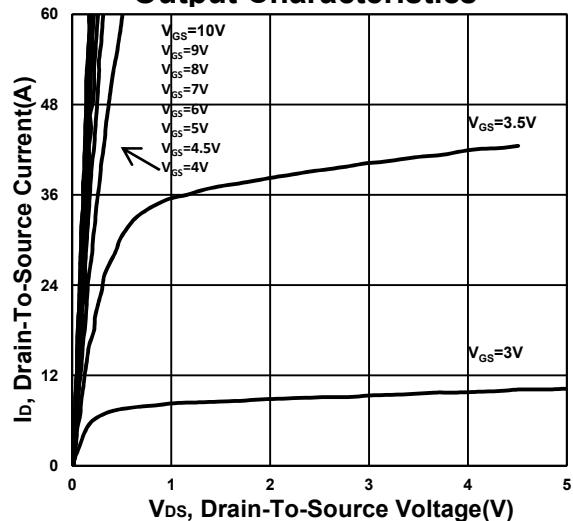
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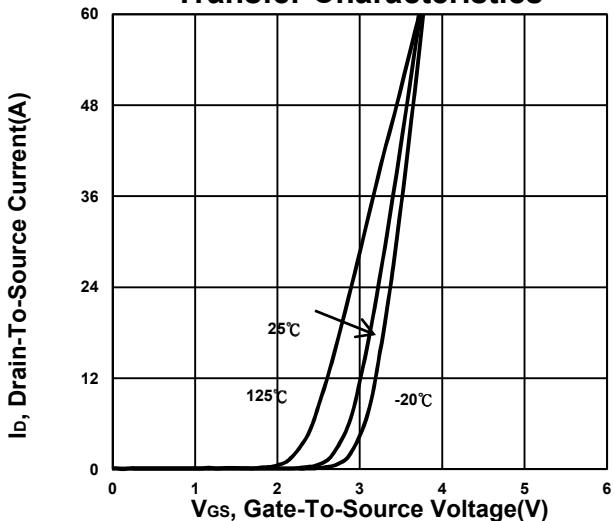
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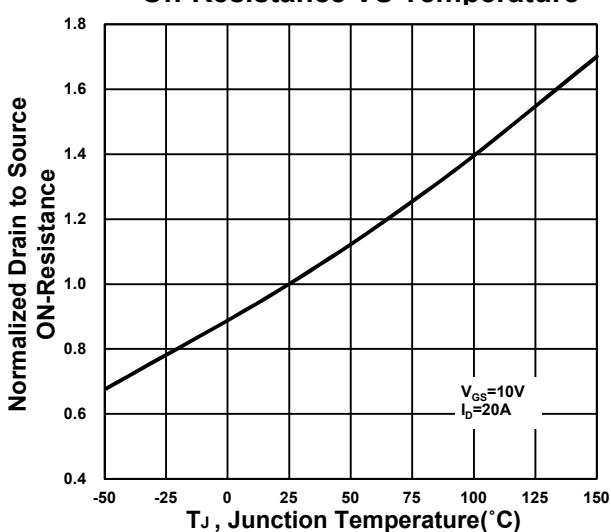
Output Characteristics



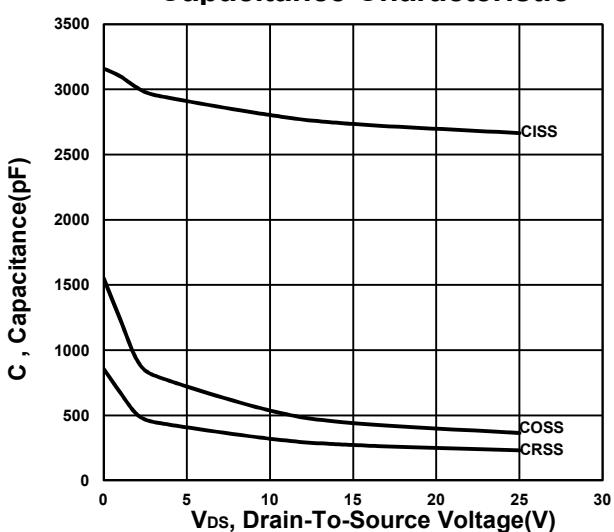
Transfer Characteristics



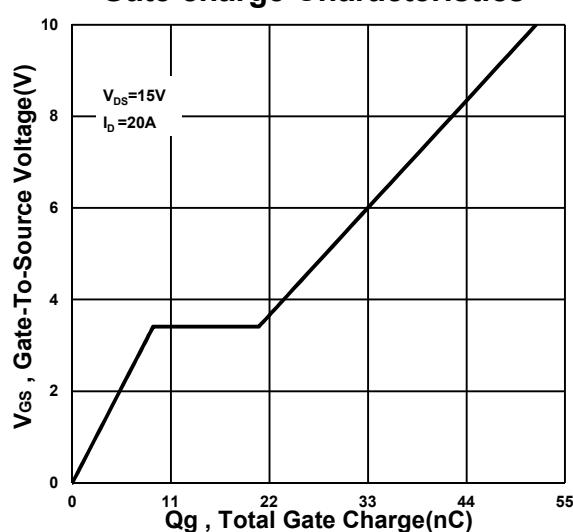
On-Resistance VS Temperature



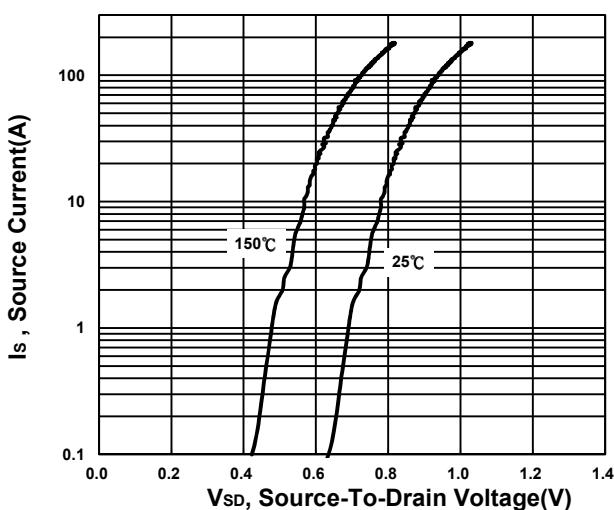
Capacitance Characteristic



Gate charge Characteristics



Source-Drain Diode Forward Voltage



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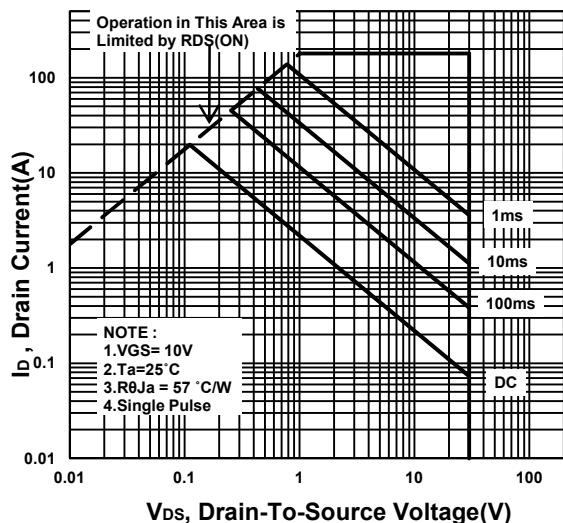
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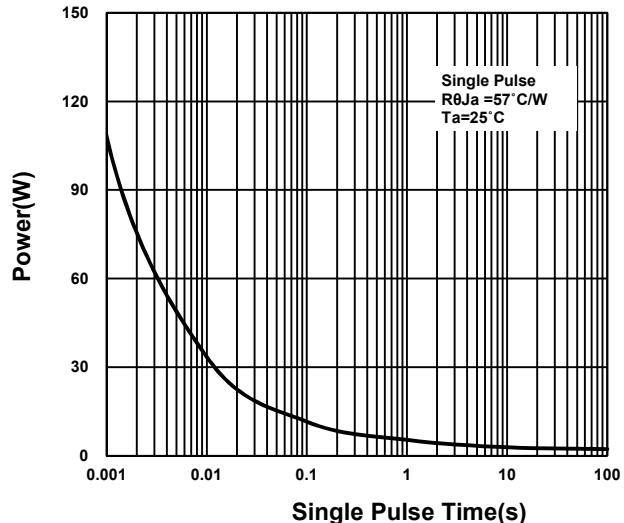
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Safe Operating Area



Single Pulse Maximum Power Dissipation



Transient Thermal Response Curve

