

Table 1. High density value line STM8L05xxx low power device features and peripheral counts

Features		STM8L052R8
Flash (Kbytes)		64
Data EEPROM (bytes)		256
RAM (Kbytes)		4
LCD		8x24 or 4x28
Timers	Basic	1 (8-bit)
	General purpose	3 (16-bit)
	Advanced control	1 (16-bit)
Communication interfaces	SPI	2
	I2C	1
	USART	3
GPIOs		54(1)
12-bit synchronized ADC (number of channels)		1 (28)
Others		RTC, window watchdog, independent watchdog, 16-MHz and 38-kHz internal RC, 1- to 16-MHz and 32-kHz external oscillator
CPU frequency		16 MHz
Operating voltage		1.8 V to 3.6 V
Operating temperature		-40 to +85 °C
Package		LQFP64

1. The number of GPIOs given in this table includes the NRST/PA1 pin but the application of NRST/PA1 pin as general purpose output only (PA1).

