

# LCM Specification

Preliminary specification

Final Specification

Project No. 项目编号	TFT-H020A1LQ TIL2N40		
Customer 客户名称			
Module No. 客户型号			
Product type 产品内容	TFT LCD Module 320 x 3RGB x 240 Dots 2.0" TFT LCD		
Signature by customer: 客户确认签章:			
<input type="checkbox"/> Trial production <input type="checkbox"/> Mass production			
编 制	电子审核	结构审核	批 准
Y. L			

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**1 Document revision history :**

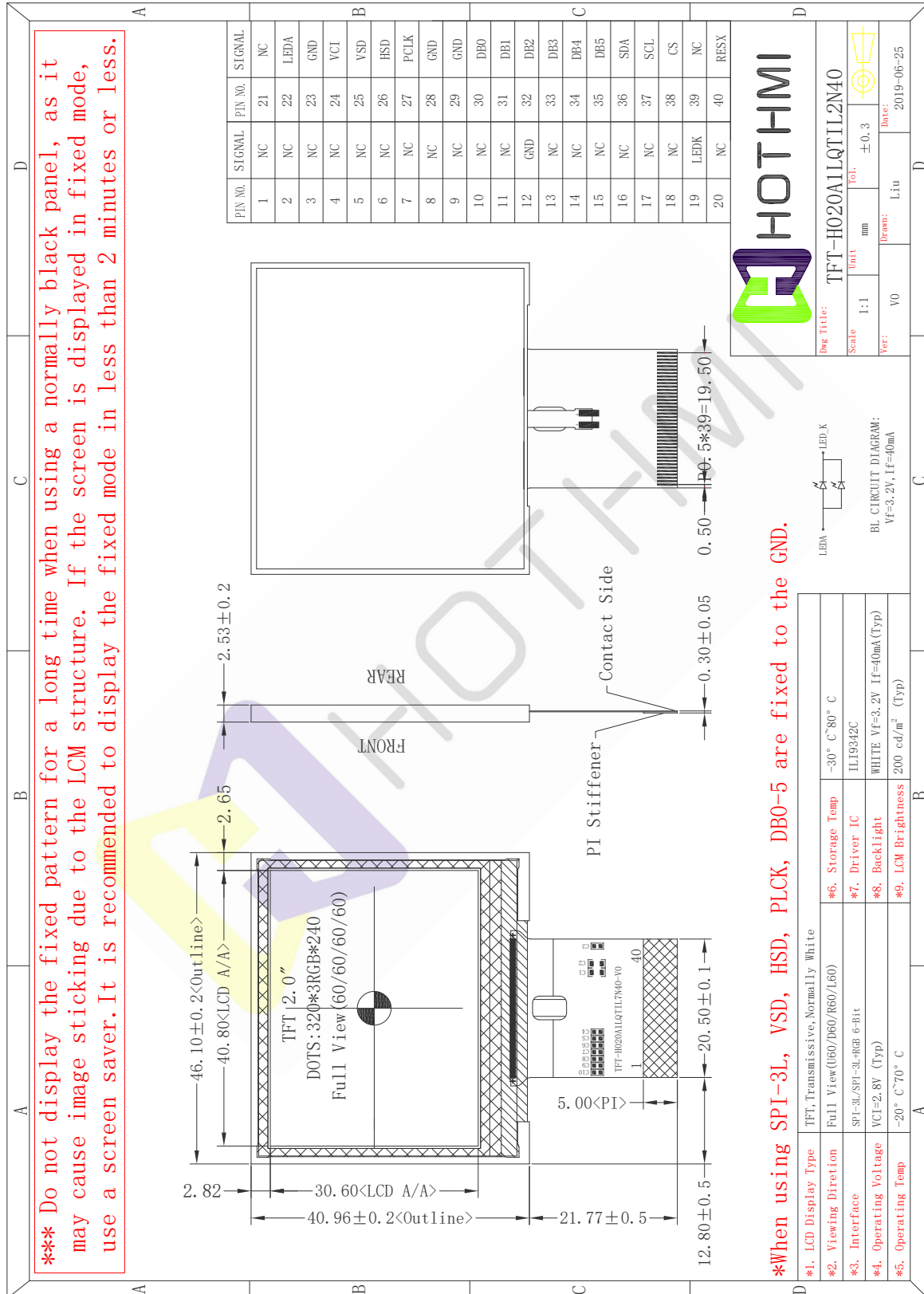
DOCUMENT REVISION	DATE	DESCRIPTION	PREPARED BY	APPROVED BY
A	2019-6-27	First Release.	Y.L	



## 1. General Feature:

Item	Standard Value	Unit
Display Size	2.0"	--
Number of Pixels	320(H)x3(RGB)*240(V)	--
Active Area	40.80(H) *30.60(V)	mm
Outline Dimension	46.10(H) × 40.96 × 2.53(V)	mm
Viewing Direction	Full O'clock	-
Interface	SPI-3L/SPI-3L+RGB 6-Bit	-
Driver IC	ILI9342C	-
Driver Condition	VCI=2.8V (Type)	V
Backlight	White LED	-
Touch Panel	No Touch Panel	-
Operation Temperature	-20~70	°C
Storage Temperature	-30~80	°C

## 2. Outline Dimensions

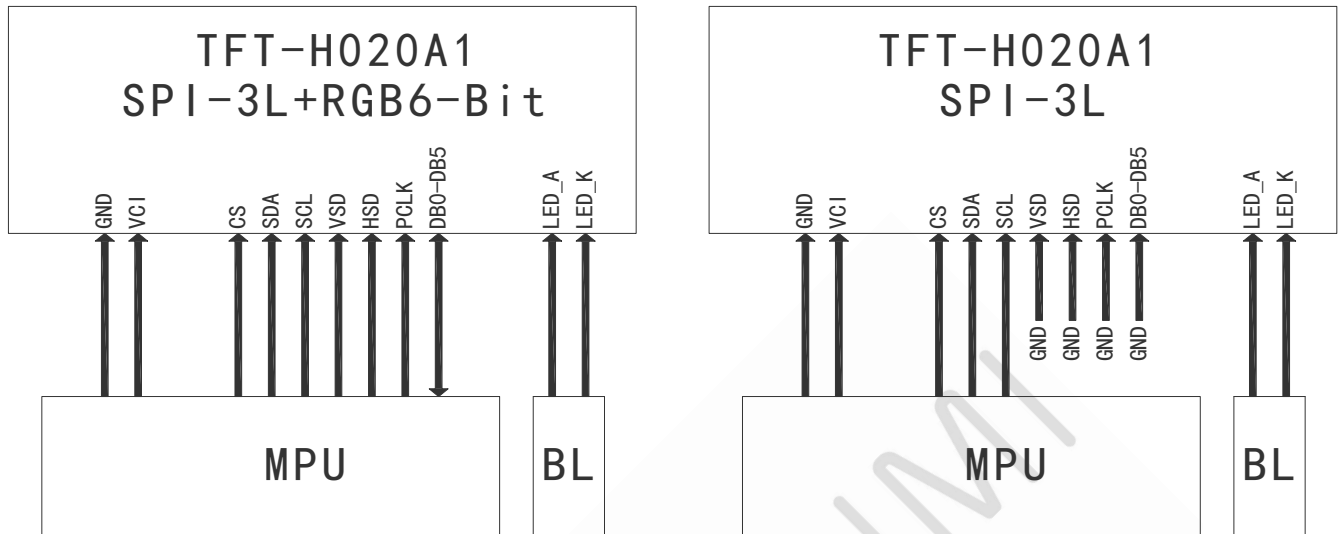


### 3. Pin Description

#### 3.1 Pin Description

Pin NO.	Symbol	Description
1-11	NC	No Connector
12	GND	Ground
13-18	NC	No Connector
19	LED_K	LED Cathode
20,21	NC	No Connector
22	LED_A	LED Anode
23	GND	Ground
24	VCI	Analog Power
25	VSD	Frame synchronizing signal, Fix to GND level when not in use
26	HSD	Line synchronizing signal, Fix to GND level when not in use
27	PCLK	Dot clock signal, Fix to GND level when not in use
28,29	GND	Ground
30	DB0	Data Bus, Fix to GND level when not in use
31	DB1	Data Bus, Fix to GND level when not in use
32	DB2	Data Bus, Fix to GND level when not in use
33	DB3	Data Bus, Fix to GND level when not in use
34	DB4	Data Bus, Fix to GND level when not in use
35	DB5	Data Bus, Fix to GND level when not in use
36	SDA	Serial in/out signal.The data is applied on the rising edge of the SCL signal
37	SCL	Serial interface clock
38	CS	Chip select input pin ( “Low” enable).
39	NC	No Connector
40	RESET	Reset Pin
---END---		

### 3.2 Wiring Diagram



## 4. Electrical Characteristics

### 4-1 TFT LCD Module Operating Conditions

Item	Symbol	Condition	Min	Type	Max	Unit
Interface logic circuits	IOVCC	-	1.65	1.80	3.30	V
Analog Power supply	VCI	-	2.50	2.80	3.30	V
TFT Gate on voltage	VGH	-	10.0	-	16.0	V
TFT Gate off voltage	VGL	-	-16.0	-	-10.0	V

### 4-2 LED back light specification (pera chip)

Item	Symbol	Condition	Min	Type	Max	Unit
Forward voltage	Vt	If=20mA	-	3.2	-	V
Forward current	Ipn	/1-chip	-	40	-	mA
Luminance(With LCD)	Lv	If=40mA	-	200	-	cd/m <sup>2</sup>
Luminous color	White					

## 5. OPTICAL SPECIFICATION

### 5.1 Overview

The test of Optical specifications shall be measured in a dark room (ambient luminance 1lux and temperature = 25 ± 2°C) with the equipment of Luminance meter system (Goniometer system and TOPCON BM-5) and test unit shall be located at an approximate distance 50cm from the LCD surface at a viewing angle of  $\theta$  and  $\Phi$  equal to 0°. The center of the measuring spot on the Display surface shall stay fixed. The backlight should be operating for 30 minutes prior to measurement.

### 5.2 Optical Specifications

Parameter		Symbol	Condition	Min.	Typ.	Max.	Unit	Remark
Viewing Angle Range	Horizontal	$\Theta$ 3	CR>10	-	60	-	Deg.	Note 1
		$\Theta$ 9		-	60	-	Deg.	
	Vertical	$\Theta$ 12		-	60	-	Deg.	
		$\Theta$ 6		-	60	-	Deg.	
Contrast ratio		CR	$\Theta = 0^\circ$	-	690	-		Note2
Color Gamut		CG		-	60	-	%	
White Chromaticity		Wx		-	-	-		
		Wy		-	-	-		
Reproduction of color	Red	Rx	$\Theta = 0^\circ$	-	0.634	-		Note4 (Based on C Light)
		Ry		-	0.325	-		
	Green	Gx		-	0.296	-		
		Gy		-	0.575	-		
	Blue	Bx		-	0.142	-		
		By		-	0.125	-		
Response Time (Rising + Falling)		Tr+Tf	$\Theta = 0^\circ$ Ta= 25°C	-	25	-	ms	Note5
Transmittance		Tr		-	4.5	-	%	Note3

### Note:

1.Viewing angle is the angle at which the contrast ratio is greater than 10. The viewing angles are determined for the horizontal or 3, 9 o' clock direction and the vertical or 6, 12 o' clock direction with respect to the optical axis which is normal to the LCD surface (see FIGURE 1).

2.Contrast measurements shall be made at viewing angle of  $\Theta = 0$  and at the center of the LCD surface. Luminance shall be measured with all pixels in the view field set first to white, then to the dark (black)

state . (see FIGUR 1) Luminance Contrast Ratio (CR) is defined mathematically.

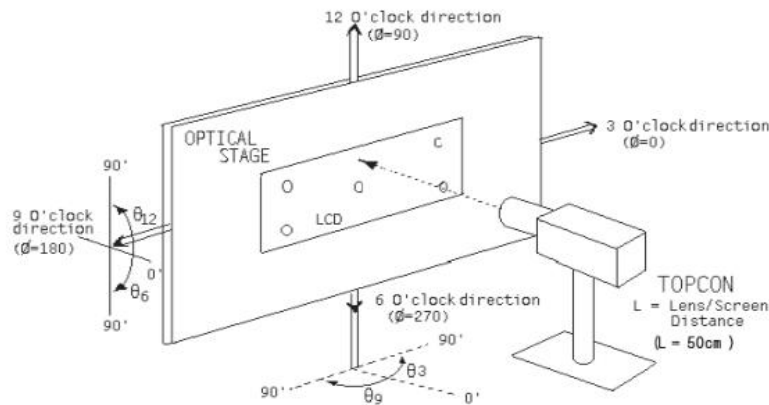
$$CR = \frac{\text{Luminance when displaying a white raster}}{\text{Luminance when displaying a black raster}}$$

3. Transmittance is the Value without APF and without CG.

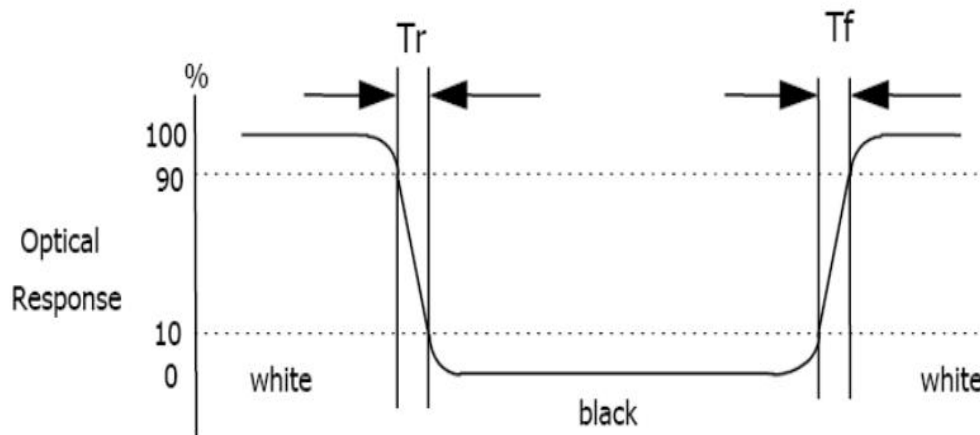
4. The color chromaticity coordinates specified in the above table shall be calculated from the spectral data measured with all pixels first in red, green, blue and white. Measurements shall be made at the center of the panel.

5. The electro-optical response time measurements shall be made as FIGURE 2 by switching the “data” input signal ON and OFF. The times needed for the luminance to change from 10% to 90% is  $T_r$ , and 90% to 10% is  $T_f$ .

**Figure1 Measurement Set Up**



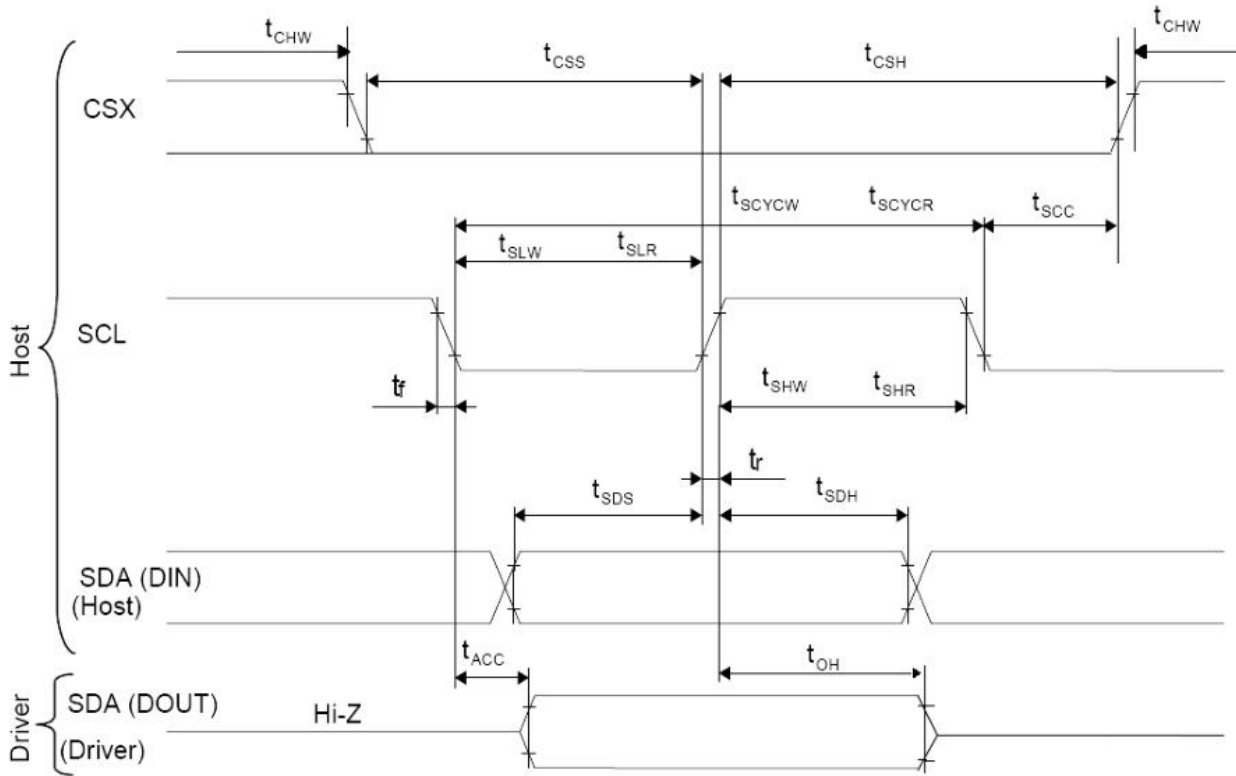
**Figure 2. Response Time Testing**





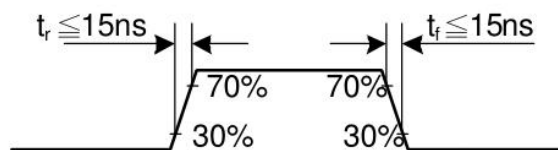
## 6. Timing Characteristics of Input Signals

### 6-1 Display Serial Interface Timing Characteristics

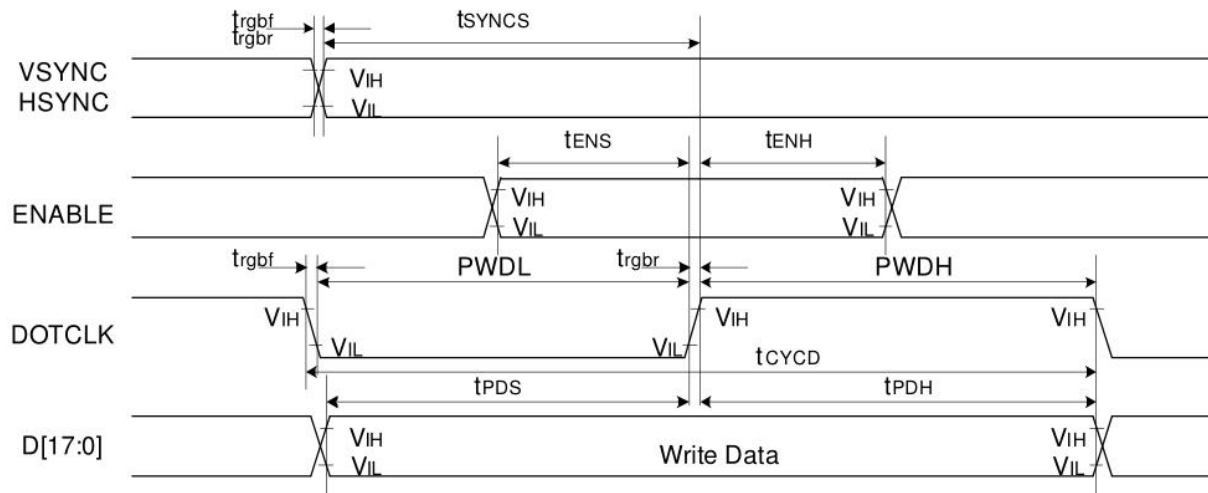


Signal	Symbol	Parameter	min	max	Unit	Description
SCL	tscycw	Serial Clock Cycle (Write)	100	-	ns	
	tshw	SCL "H" Pulse Width (Write)	35	-	ns	
	tslw	SCL "L" Pulse Width (Write)	35	-	ns	
	tscycr	Serial Clock Cycle (Read)	150	-	ns	
	tshr	SCL "H" Pulse Width (Read)	60	-	ns	
	tslr	SCL "L" Pulse Width (Read)	60	-	ns	
SDA (Input)	tsds	Data setup time (Write)	30	-	ns	
	tsdh	Data hold time (Write)	30	-	ns	
SDA (Output)	tacc	Access time (Read)	10	-	ns	
	toh	Output disable time (Read)	15	50	ns	
CSX	tsc	SCL-CSX	20	-	ns	
	tch	CSX "H" Pulse Width	40	-	ns	
	tcss	CSX-SCL Time(write)	30	-	ns	
	tcs		30	-	ns	

Note:  $T_a = 25\text{ }^\circ\text{C}$ ,  $IOVCC=1.65\text{V to }3.3\text{V}$ ,  $VCI=2.6\text{V to }3.3\text{V}$ ,  $AGND=GND=0\text{V}$

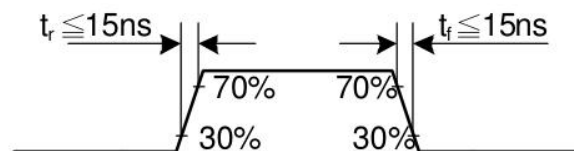


### 6-2 Parallel 18/16/6-bit RGB Interface Timing Characteristics



Signal	Symbol	Parameter	min	max	Unit	Description
VSYNC / HSYNC	$t_{SYNCS}$	VSYNC/HSYNC setup time	15	-	ns	18/16-bit bus RGB interface mode
	$t_{SYNCH}$	VSYNC/HSYNC hold time	15	-	ns	
DE	$t_{ENS}$	DE setup time	15	-	ns	
	$t_{ENH}$	DE hold time	15	-	ns	
D[17:0]	$t_{POS}$	Data setup time	15	-	ns	
	$t_{PDH}$	Data hold time	15	-	ns	
DOTCLK	PWDH	DOTCLK high-level period	33	-	ns	
	PWDL	DOTCLK low-level period	33	-	ns	
	$t_{CYCD}$	DOTCLK cycle time(18 bit)	100	-	ns	
	$t_{rgbr}, t_{rgbf}$	DOTCLK,HSYNC,VSYNC rise/fall time	-	15	ns	
VSYNC / HSYNC	$t_{SYNCS}$	VSYNC/HSYNC setup time	15	-	ns	6-bit bus RGB interface mode
	$t_{SYNCH}$	VSYNC/HSYNC hold time	15	-	ns	
DE	$t_{ENS}$	DE setup time	15	-	ns	
	$t_{ENH}$	DE hold time	15	-	ns	
D[17:0]	$t_{POS}$	Data setup time	15	-	ns	
	$t_{PDH}$	Data hold time	15	-	ns	
DOTCLK	PWDH	DOTCLK high-level pulse period	25	-	ns	
	PWDL	DOTCLK low-level pulse period	25	-	ns	
	$t_{CYCD}$	DOTCLK cycle time	50	-	ns	
	$t_{rgbr}, t_{rgbf}$	DOTCLK,HSYNC,VSYNC rise/fall time	-	15	ns	

Note:  $T_a = -30$  to  $70$  °C,  $IOVCC=1.65V$  to  $3.3V$ ,  $VCI=2.6V$  to  $3.3V$ ,  $AGND=GND=0V$



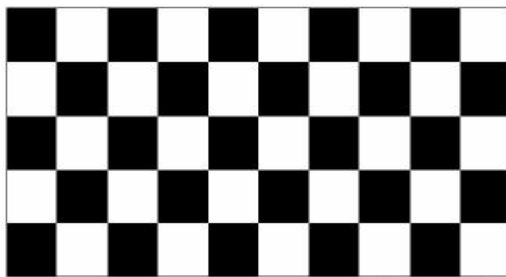
## 7. RELIABILITY TEST

### 7-1 Temperature and Humidity

TEST ITEMS	CONDITIONS	NOTE
High Temperature Operation	60°C ; 120hrs	
High Temperature Storage	70°C ; 120hrs	
High Temperature High Humidity Operation	60°C; 90%RH ; 120hrs (No condensation)	
Low Temperature Operation	-10°C ; 120hrs	
Low Temperature Storage	-20°C ; 120hrs	
Thermal Shock	-30°C (0.5hr) ~ 80°C (0.5hr) ; 100 Cycles	Non-Operating
Image Sticking	25°C ; 2hrs	1

Note 1: Condition of Image Sticking test: 25°C ±2°C

Operation with test pattern sustained for 4 hrs, then change to gray pattern immediately. After 5 mins, the mura must be disappeared completely .



(a) Test Pattern (chess board Pattern )



(b) Gray Pattern

### 7-2 Shock and Vibration

ITEMS	CONDITIONS
Packing Shock (Non-Operation)	<ul style="list-style-type: none"> <li>● Shock level:980m/s<sup>2</sup></li> <li>● Waveform:1/2 Sine wave,6msec</li> <li>● ±X, ±Y ±Z,each axis 1 times</li> </ul>
Packing Vibration (Non-Operation)	<ul style="list-style-type: none"> <li>● Frequency range:8-33.3HZ</li> <li>● Stoke:1.0mm</li> <li>● Sweep: 10Hz-50Hz</li> <li>● x,y,z 2 hours for each direction</li> </ul>

### 7-3 Electrostatic Discharge

TEST ITEM	CONDITIONS
ESD (Non-operation)	150pF,330Ω , Contact±4KV,Air :±8KV.Note 1
	200pF,0Ω , ±200V Contact test.Note 2

Note:Measure Point:

- 1.LCD glass and metal bezel
- 2.IF connector pins

## 8.HANDDLING & CAUTIONS

### 8-1 Caution For Operation

◆Since the LCM is made of glass, do not apply strong mechanical impact or static load onto it. Handling with care since shock, vibration, and careless handling may seriously affect the product. If it falls from a high place or receives a strong shock, the glass maybe broken.

◆It is indispensable to drive the LCM within the specified voltage limit since the higher voltage than the limit causes LCM's life shorter. An electro-chemical reaction due to DC causes undesirable deterioration of the LCM so that the use of DC drive should avoid.

◆Do not connect or disconnect the LCM to or from the system when power is on.

◆Never use the LCM under abnormal conditions of high temperature and high humidity.

◆When expose to drastic fluctuation of temperature(hot to cold or cold to hot), the LCM may be affected; specifically, drastic temperature fluctuation from cold to hot, produces dew on the LCM's surface which may affect the operation of the polarizer on the LCM.

◆Response time will be extremely delay at lower temperature than the operating temperature range and on the other hand LCM may turn black at temperature above its operational range. However those phenomenon do not mean malfunction or out of order with the LCM. The LCM will revert to normal operation once the temperature returns to the recommended temperature range for normal operation.

◆Do not display the fixed pattern for a long time because it may develop image sticking due to the LCM structure. If the screen is displayed with fixed pattern, use a screen saver. It is recommended to display the fixed mode for no more than 2 minutes or less.

◆Do not disassemble and/or re-assemble LCM module

### 7-2 Caution Against Static Charge

◆The LCM use C-MOS LSI drivers, so customers are recommended that any unused input terminal would be connected to Vdd or Vss, do not input any signals before power is turn on, and ground you body, work/assembly area, assembly equipments to protect against static electricity.

◆Remove the protective film slowly, keeping the removing direction approximate 30-degree not vertical from panel surface, if possible, under ESD control device like ion blower, and the humidity of working room should be kept over 50%RH to reduce the risk of static charge.

◆Avoid the use work clothing made of synthetic fibers. We recommend cotton clothing or other conductivity-treated fibers.

◆In handling the LCM, wear non-charged material gloves. And the conducting wrist to the earth and the conducting shoes to the earth are necessary

## 9.LCD display initialization code

```
Void Panel_initial_code(void)
{
WriteComm(0xC8);
WriteData(0xFF);
WriteData(0x93);
WriteData(0x42);           //Set EXTC
WriteComm(0xC0);
WriteData(0x0E);
WriteData(0x0E);           //Power Control 1 VREG1OUT= 4.5V VREG2OUT= -4.5V
WriteComm(0xC1);
WriteData(0x11);           //Power Control 2
WriteComm(0x36);
WriteData(0xC8);

WriteComm(0xC5);
WriteData(0xF2);           //VCOM
WriteComm(0xB1);
WriteData(0x00);
WriteData(0x1B);
WriteComm(0xB4);
WriteData(0x02);
WriteComm(0xB6);
WriteData(0x0A);
WriteData(0x00);
WriteData(0x1D);
WriteData(0x04);
WriteComm(0x3A);
WriteData(0x55);

WriteComm(0xF6);
WriteData(0x01);
WriteData(0x30);
WriteData(0x00);

WriteComm(0xE0);
WriteData(0x00);
WriteData(0x00);
WriteData(0x12);
```

```
WriteData(0x07);  
WriteData(0x14);  
WriteData(0x0C);  
WriteData(0x38);  
WriteData(0x00);  
WriteData(0x50);  
WriteData(0x07);  
WriteData(0x0C);  
WriteData(0x09);  
WriteData(0x3A);  
WriteData(0x2D);  
WriteData(0x0F);
```

```
WriteComm(0xE1);  
WriteData(0x00);  
WriteData(0x00);  
WriteData(0x12);  
WriteData(0x07);  
WriteData(0x14);  
WriteData(0x0C);  
WriteData(0x38);  
WriteData(0x00);  
WriteData(0x50);  
WriteData(0x03);  
WriteData(0x0C);  
WriteData(0x09);  
WriteData(0x3A);  
WriteData(0x2D);  
WriteData(0x0F);
```

```
WriteComm(0x11);  
//WriteData(0x00);  
Delays (120);  
WriteComm(0x29);  
//WriteData(0x00);  
Delays (120);
```

```
}
```

```
Void Panel_SleepIn_Mode (void)
{
WriteComm(0x10);
Delays (120);
}
Void Panel_SleepOut_Mode (void)
{
WriteComm(0x11);
Delays (120);
}
```

--- END ---