

## NCE N-Channel Enhancement Mode Power MOSFET

### **Description**

The NCE3010S uses advanced trench technology and design to provide excellent  $R_{DS(ON)}$  with low gate charge. It can be used in a wide variety of applications.

#### **General Features**

V<sub>DS</sub> =30V,I<sub>D</sub> =10A

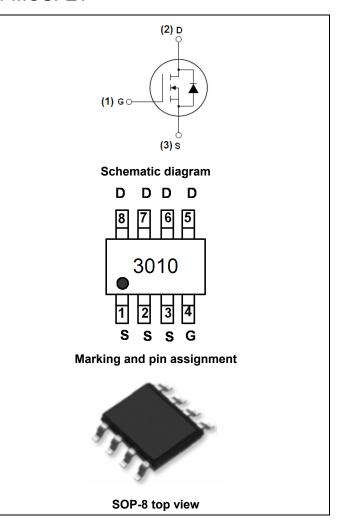
 $R_{DS(ON)}$  < 12m $\Omega$  @  $V_{GS}$ =10V

 $R_{DS(ON)}$  <16m $\Omega$  @  $V_{GS}$ =4.5V

- High density cell design for ultra low Rdson
- Fully characterized Avalanche voltage and current

### **Application**

- Power switching application
- Hard Switched and High Frequency Circuits
- Uninterruptible Power Supply



### **Package Marking and Ordering Information**

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
3010	NCE3010S	SOP-8	Ø330mm	12mm	2500 units

### Absolute Maximum Ratings (T<sub>A</sub>=25 ℃unless otherwise noted)

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Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V <sub>DS</sub>	30	V
Gate-Source Voltage	V <sub>G</sub> s	±20	V
Drain Current-Continuous	I <sub>D</sub>	10	Α
Drain Current-Continuous(T <sub>C</sub> =100℃)	I <sub>D</sub> (100℃)	6	Α
Pulsed Drain Current	I <sub>DM</sub>	50	Α
Maximum Power Dissipation	P <sub>D</sub>	2.5	W
Operating Junction and Storage Temperature Range	$T_{J}, T_{STG}$	-55 To 150	$^{\circ}$

### **Thermal Characteristic**

Thermal Resistance, Junction-to-Case (Note 2)	$R_{ heta JC}$	50	°C/W

Page 1 v1.0



# NCE3010S

Electrical Characteristics (T<sub>A</sub>=25°C unless otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Max	Unit	
Off Characteristics			•				
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V I <sub>D</sub> =250μA	30	33	-	V	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =30V,V <sub>GS</sub> =0V	-	-	1	μΑ	
Gate-Body Leakage Current	I <sub>GSS</sub>	$V_{GS}$ =±20 $V$ , $V_{DS}$ =0 $V$	-	-	±100	nA	
On Characteristics (Note 3)			•				
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}$ , $I_{D}=250\mu A$	1	1.6	3	V	
Danier Courses On Otata Basintana		V <sub>GS</sub> =10V, I <sub>D</sub> =10A	-	8	12	mΩ	
Drain-Source On-State Resistance	R <sub>DS(ON)</sub>	V <sub>GS</sub> =4.5V, I <sub>D</sub> =5A	-	11	16		
Forward Transconductance	<b>g</b> FS	V <sub>DS</sub> =5V,I <sub>D</sub> =10A	15	-	-	S	
Dynamic Characteristics (Note4)	1						
Input Capacitance	C <sub>lss</sub>	\\ 45\\\\ a\\	-	1550	-	PF	
Output Capacitance	C <sub>oss</sub>	$V_{DS}$ =15V, $V_{GS}$ =0V,	-	300	-	PF	
Reverse Transfer Capacitance	C <sub>rss</sub>	F=1.0MHz	-	180	-	PF	
Switching Characteristics (Note 4)			•				
Turn-on Delay Time	t <sub>d(on)</sub>		-	30	-	nS	
Turn-on Rise Time	t <sub>r</sub>	$V_{DD}$ =25 $V$ , $I_{D}$ =1 $A$	-	20	-	nS	
Turn-Off Delay Time	t <sub>d(off)</sub>	$V_{GS}\text{=}10V, R_{GEN}\text{=}6\Omega$	-	100	-	nS	
Turn-Off Fall Time	t <sub>f</sub>		-	80	-	nS	
Total Gate Charge	Qg	\/ 45\/  400	-	13	-	nC	
Gate-Source Charge	$Q_{gs}$	$V_{DS}=15V, I_{D}=10A,$	-	5.5	-	nC	
Gate-Drain Charge	$Q_{gd}$	V <sub>GS</sub> =5V	-	3.5	-	nC	
Drain-Source Diode Characteristics			•				
Diode Forward Voltage (Note 3)	V <sub>SD</sub>	V <sub>GS</sub> =0V,I <sub>S</sub> =10A	-	-	1.2	V	
Diode Forward Current (Note 2)	Is		-	-	10	Α	

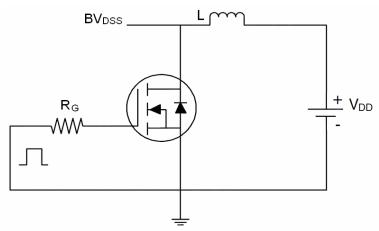
#### Notes:

- 1. Repetitive Rating: Pulse width limited by maximum junction temperature.
- 2. Surface Mounted on FR4 Board, t ≤ 10 sec.
- 3. Pulse Test: Pulse Width ≤ 300µs, Duty Cycle ≤ 2%.
- 4. Guaranteed by design, not subject to production

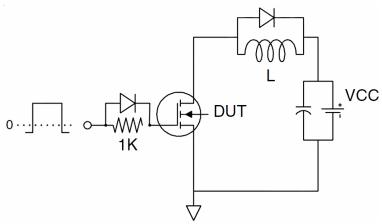


# **Test Circuit**

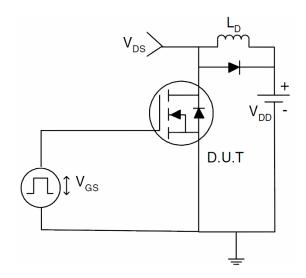
# 1) E<sub>AS</sub> Test Circuits



# 2) Gate Charge Test Circuit:



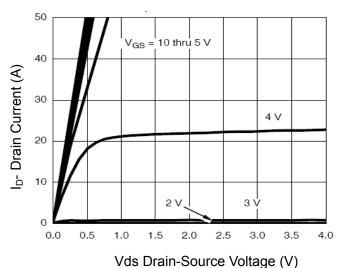
# 3) Switch Time Test Circuit:



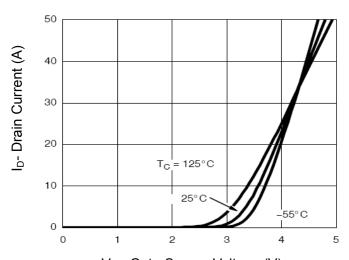
Page 3 v1.0



# **Typical Electrical and Thermal Characteristics (Curves)**



**Figure 1 Output Characteristics** 



Vgs Gate-Source Voltage (V)

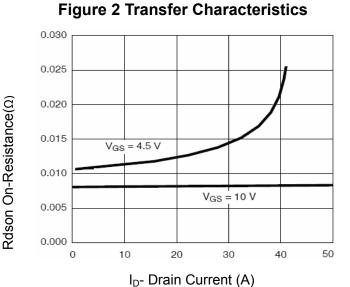


Figure 3 Rdson- Drain Current

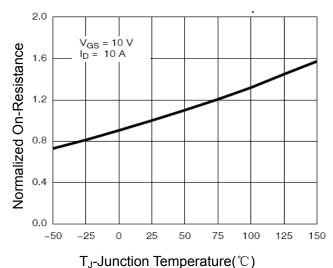


Figure 4 Rdson-JunctionTemperature

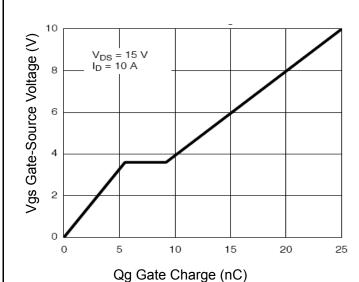


Figure 5 Gate Charge

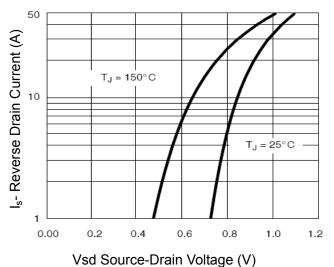


Figure 6 Source- Drain Diode Forward

Page 4 v1.0



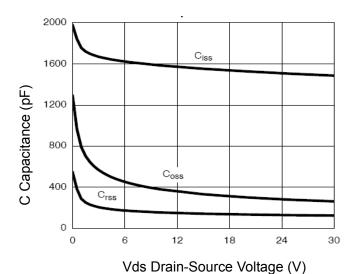
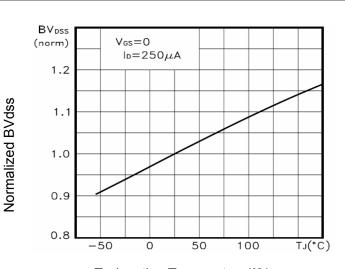
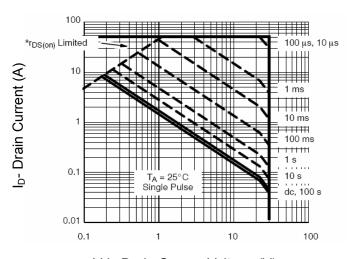


Figure 7 Capacitance vs Vds

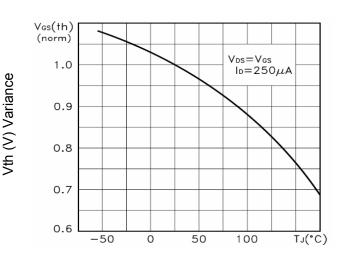


T<sub>J</sub>-Junction Temperature(°C)

Figure 9 BV<sub>DSS</sub> vs Junction Temperature

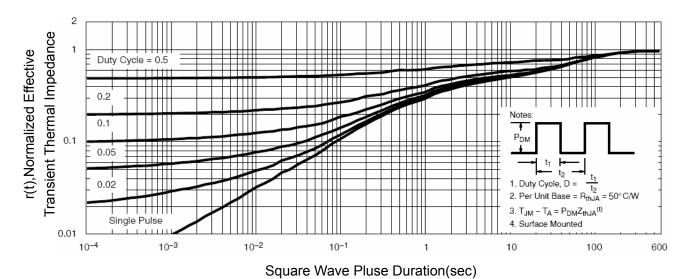


Vds Drain-Source Voltage (V)
Figure 8 Safe Operation Area



T<sub>J</sub>-Junction Temperature(°C)

Figure 10 V<sub>GS(th)</sub> vs Junction Temperature



**Figure 11 Normalized Maximum Transient Thermal Impedance** 

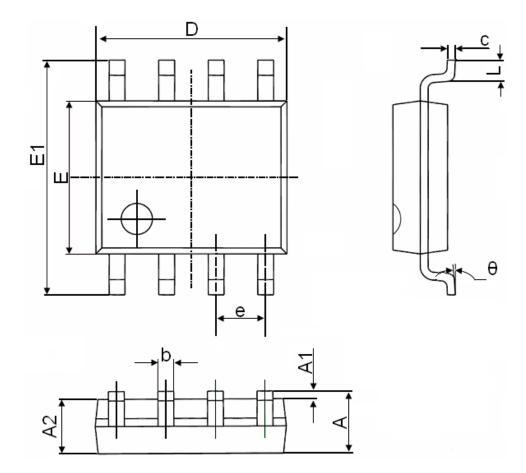
Page 5 v1.0



# NCE3010S

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# **SOP-8 Package Information**



Cumhal	Dimensions	In Millimeters	Dimensions In Inches		
Symbol	Min.	Max.	Min.	Max.	
Α	1.350	1.750	0.053	0.069	
A1	0.100	0.250	0.004	0.010	
A2	1.350	1.550	0.053	0.061	
b	0.330	0.510	0.013	0.020	
С	0.170	0.250	0.006	0.010	
D	4.700	5.100	0.185	0.200	
E	3.800	4.000	0.150	0.157	
E1	5.800	6.200	0.228	0.244	
е	1.270(BSC)		0.050(BSC)		
L	0.400	1.270	0.016	0.050	
θ	0°	8°	0°	8°	

Page 6 v1.0



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**NCE3010S** 

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Page 7 v1.0