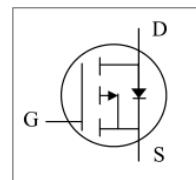


-100V P-Channel Enhancement Mode MOSFET**Description**

The 30P10 uses advanced trench technology

to provide excellent $R_{DS(ON)}$, low gate charge and operation with gate voltages as low as 4.5V. This device is suitable for use as a

Battery protection or in other Switching application.

**General Features**

$V_{DS} = -100V$ $I_D = -30 A$

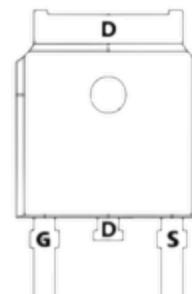
$R_{DS(ON)} < 95m\Omega$ @ $V_{GS}=10V$

Application

Battery protection

Load switch

Uninterruptible power supply

**Package Marking and Ordering Information**

Product ID	Pack	Marking	Qty(PCS)
30P10	TO-252-3L	30P10 XXX YYYY	2500

Absolute Maximum Ratings ($T_c=25^\circ C$ unless otherwise noted)

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	-100	V
V_{GS}	Gate-Source Voltage	± 20	V
$I_D@T_c=25^\circ C$	Continuous Drain Current, $V_{GS} @ -10V^1$	-30	A
$I_D@T_c=100^\circ C$	Continuous Drain Current, $V_{GS} @ -10V^1$	-15	A
I_{DM}	Pulsed Drain Current ²	-75	A
EAS	Single Pulse Avalanche Energy ³	157.2	mJ
I_{AS}	Avalanche Current	18.9	A
$P_D@T_c=25^\circ C$	Total Power Dissipation ⁴	54	W
T_{STG}	Storage Temperature Range	-55 to 150	°C
T_J	Operating Junction Temperature Range	-55 to 150	°C
R_{eJA}	Thermal Resistance Junction-Ambient ¹	62	°C/W
R_{eJC}	Thermal Resistance Junction-Case ¹	2.3	°C/W

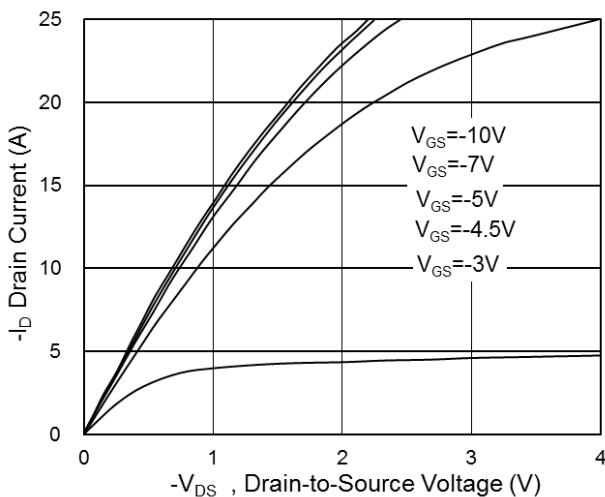
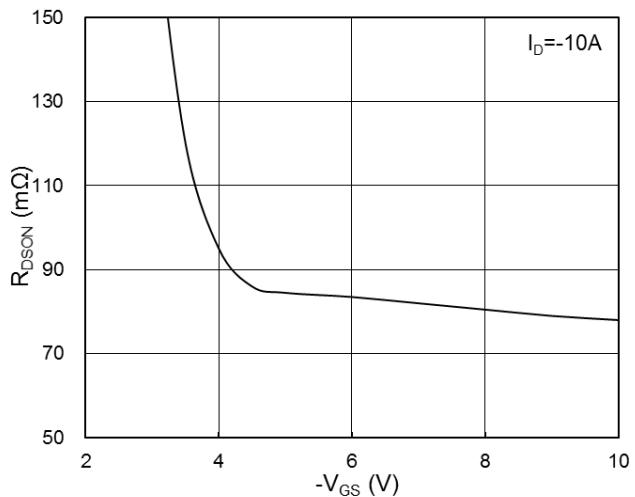
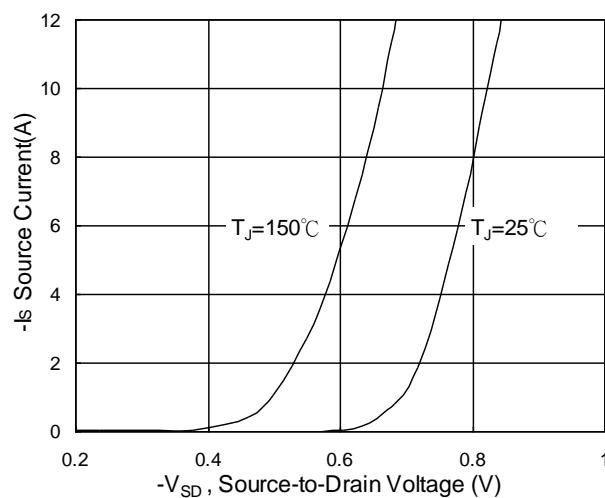
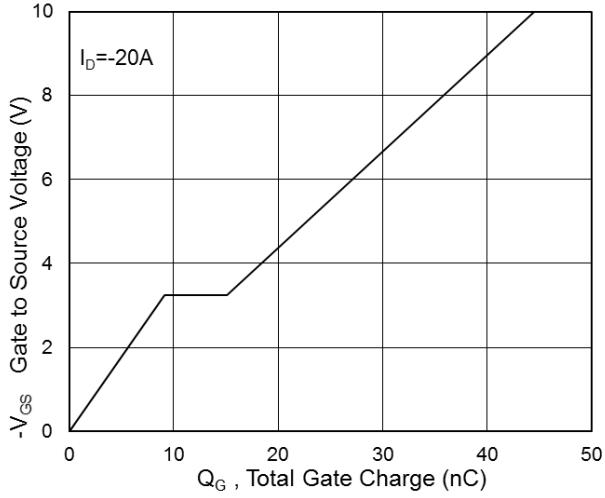
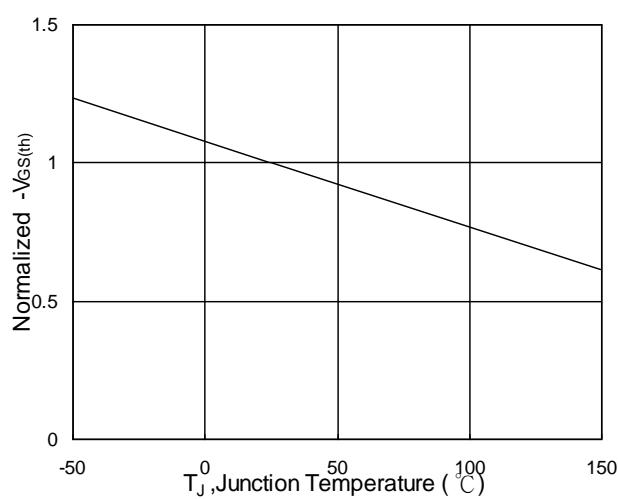
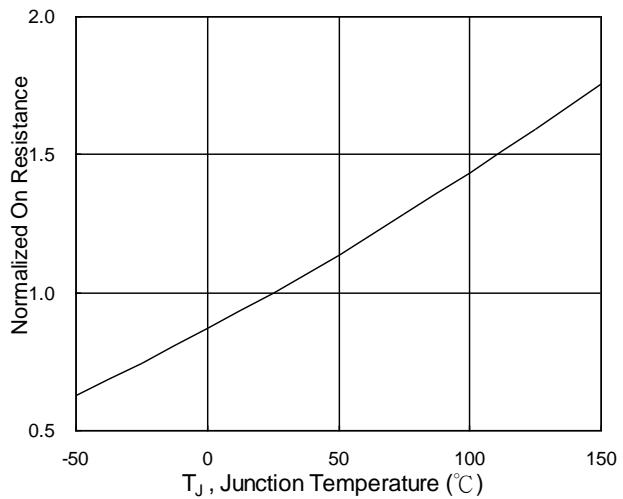
-100V P-Channel Enhancement Mode MOSFET

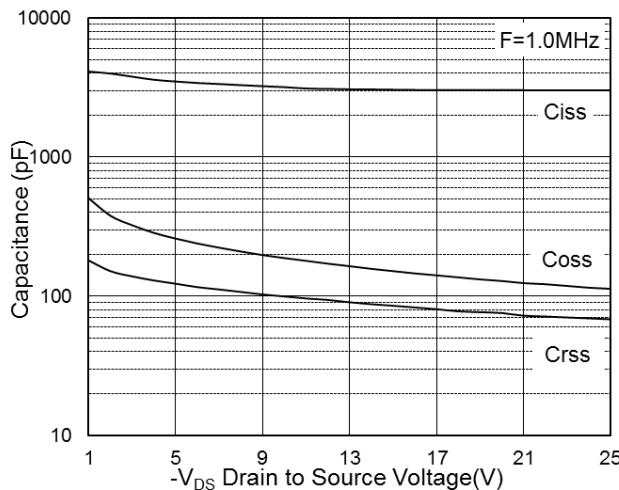
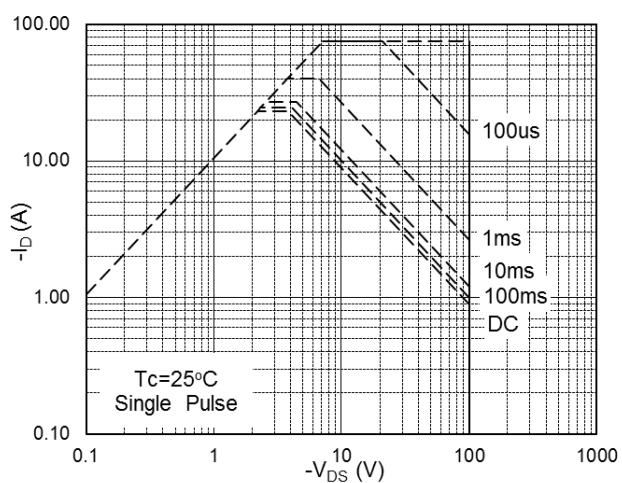
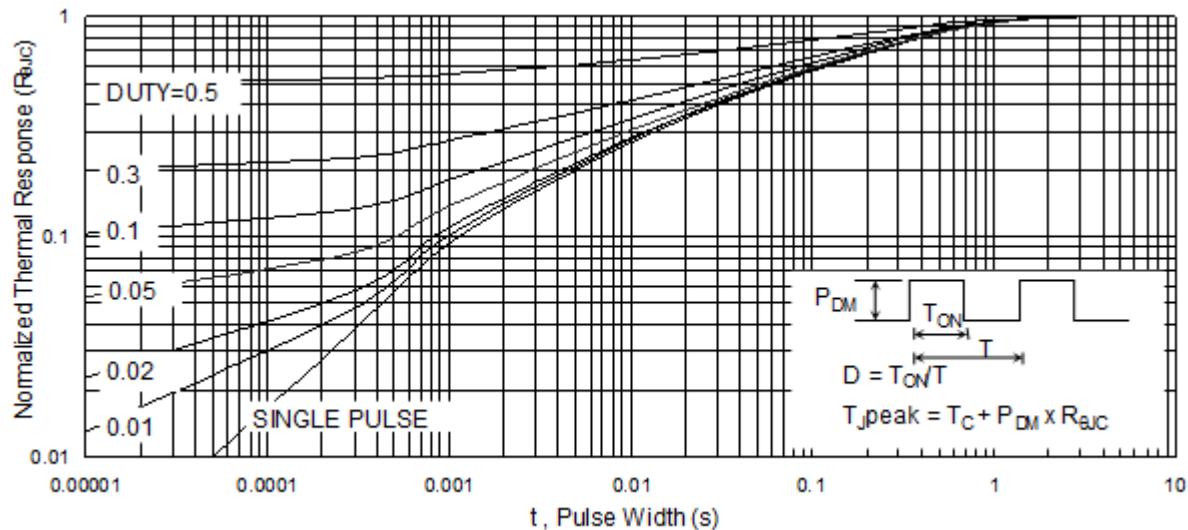
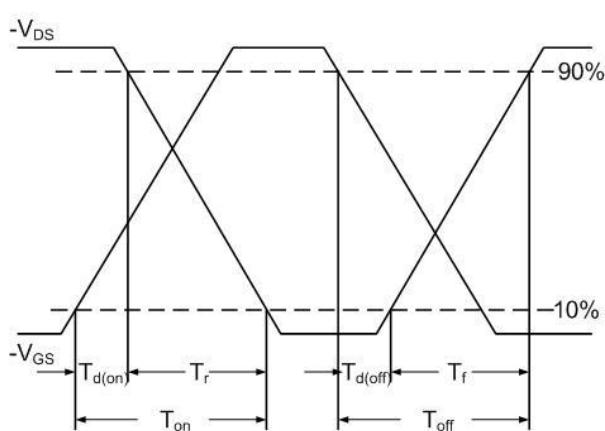
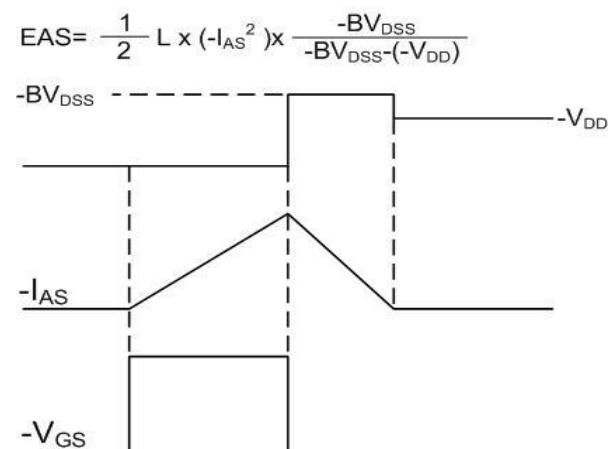
Electrical Characteristics ($T_J=25^\circ\text{C}$, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}$, $I_D=-250\mu\text{A}$	-100	---	---	V
$\text{R}_{\text{DS(ON)}}$	Static Drain-Source On-Resistance ²	$V_{\text{GS}}=-10\text{V}$, $I_D=-10\text{A}$	---	78	95	$\text{m}\Omega$
		$V_{\text{GS}}=-4.5\text{V}$, $I_D=-8\text{A}$	---	86	110	
$V_{\text{GS(th)}}$	Gate Threshold Voltage	$V_{\text{GS}}=V_{\text{DS}}$, $I_D=-250\mu\text{A}$	-1.2	-1.7	-2.5	V
I_{DSS}	Drain-Source Leakage Current	$V_{\text{DS}}=-100\text{V}$, $V_{\text{GS}}=0\text{V}$, $T_J=25^\circ\text{C}$	---	---	-50	μA
I_{GSS}	Gate-Source Leakage Current	$V_{\text{GS}}=\pm20\text{V}$, $V_{\text{DS}}=0\text{V}$	---	---	±100	nA
g_{fs}	Forward Transconductance	$V_{\text{DS}}=-10\text{V}$, $I_D=-10\text{A}$	---	24	---	S
Q_g	Total Gate Charge	$V_{\text{DS}}=-50\text{V}$, $V_{\text{GS}}=-10\text{V}$, $I_D=-20\text{A}$	---	44.5	---	nC
Q_{gs}	Gate-Source Charge		---	9.13	---	
Q_{gd}	Gate-Drain Charge		---	5.93	---	
$\text{T}_{\text{d(on)}}$	Turn-On Delay Time	$V_{\text{DD}}=-50\text{V}$, $V_{\text{GS}}=-10\text{V}$, $R_G=3.3\text{ }\Omega$, $I_D=-10\text{A}$	---	12	---	ns
T_r	Rise Time		---	27.4	---	
$\text{T}_{\text{d(off)}}$	Turn-Off Delay Time		---	79	---	
T_f	Fall Time		---	53.6	---	
C_{iss}	Input Capacitance	$V_{\text{DS}}=-20\text{V}$, $V_{\text{GS}}=0\text{V}$, $f=1\text{MHz}$	---	3029	---	pF
C_{oss}	Output Capacitance		---	129	---	
C_{rss}	Reverse Transfer Capacitance		---	76	---	
I_{s}	Continuous Source Current ^{1,5}	$V_G=V_D=0\text{V}$, Force Current	---	---	-18	A
V_{SD}	Diode Forward Voltage ²	$V_{\text{GS}}=0\text{V}$, $\text{I}_{\text{s}}=-1\text{A}$, $T_J=25^\circ\text{C}$	---	---	-1.2	V
t_{rr}	Reverse Recovery Time	$\text{I}_{\text{F}}=-8\text{A}$, $d\text{I}/dt=-100\text{A}/\mu\text{s}$, $T_J=25^\circ\text{C}$	---	38.7	---	nS
Q_{rr}	Reverse Recovery Charge		---	22.4	---	nC

Note :

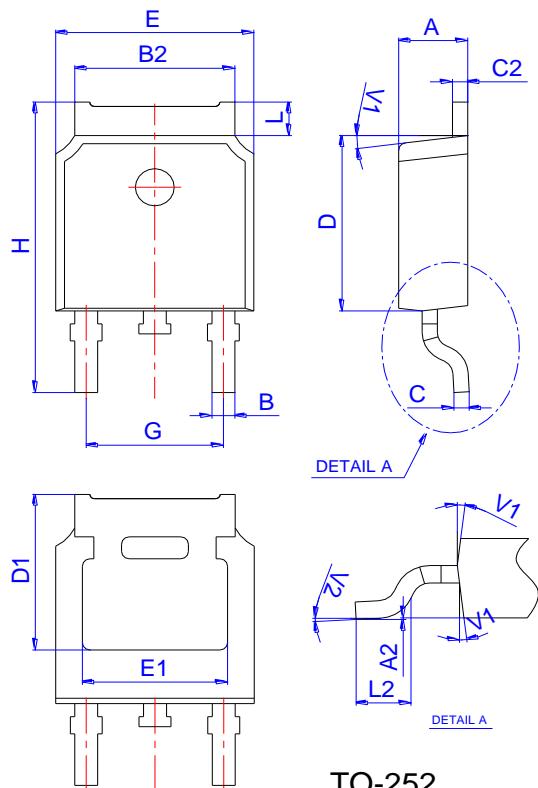
- 1.The data tested by surface mounted on a 1 inch FR-4 board with 2OZ copper.
- 2.The data tested by pulsed , pulse width $\leq 300\mu\text{s}$, duty cycle $\leq 2\%$
- 3.The EAS data shows Max. rating . The test condition is $V_{\text{DD}}=-25\text{V}$, $V_{\text{GS}}=-10\text{V}$, $L=0.88\text{mH}$, $\text{I}^{\text{AS}}=-18.9\text{A}$
- 4.The power dissipation is limited by 150°C junction temperature
- 5 .The data is theoretically the same as I_{D} and I_{DM} , in real applications , should be limited by total power dissipation.

-100V P-Channel Enhancement Mode MOSFET**Typical Characteristics****Fig.1 Typical Output Characteristics****Fig.2 On-Resistance vs G-S Voltage****Fig.3 Typical S-D Diode Forward Voltage****Fig.4 Gate-Charge Characteristics****Fig.5 Normalized $V_{GS(th)}$ vs T_J** **Fig.6 Normalized $R_{DS(on)}$ vs T_J**

-100V P-Channel Enhancement Mode MOSFET**Fig.7 Capacitance****Fig.8 Safe Operating Area****Fig.9 Normalized Maximum Transient Thermal Impedance****Fig.10 Switching Time Waveform****Fig.11 Unclamped Inductive Waveform**

-100V P-Channel Enhancement Mode MOSFET

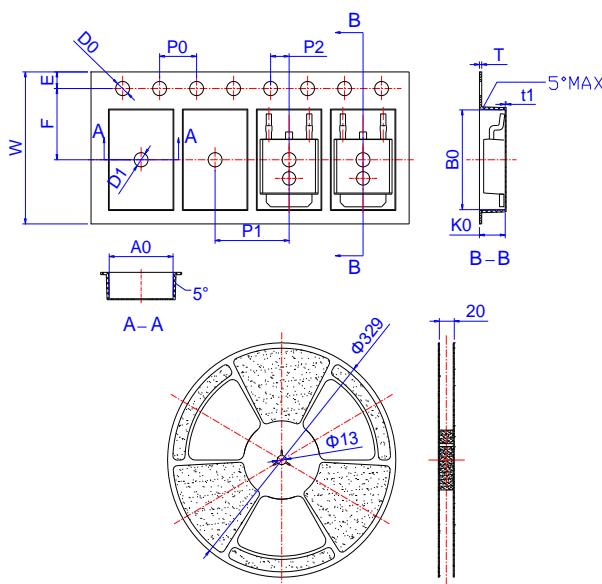
Package Mechanical Data



TO-252

Ref.	Dimensions					
	Millimeters			Inches		
	Min.	Typ.	Max.	Min.	Typ.	Max.
A	2.10		2.50	0.083		0.098
A2	0		0.10	0		0.004
B	0.66		0.86	0.026		0.034
B2	5.18		5.48	0.202		0.216
C	0.40		0.60	0.016		0.024
C2	0.44		0.58	0.017		0.023
D	5.90		6.30	0.232		0.248
D1	5.30REF			0.209REF		
E	6.40		6.80	0.252		0.268
E1	4.63			0.182		
G	4.47		4.67	0.176		0.184
H	9.50		10.70	0.374		0.421
L	1.09		1.21	0.043		0.048
L2	1.35		1.65	0.053		0.065
V1		7°			7°	
V2	0°		6°	0°		6°

Reel Specification-TO-252



Ref.	Dimensions					
	Millimeters			Inches		
	Min.	Typ.	Max.	Min.	Typ.	Max.
W	15.90	16.00	16.10	0.626	0.630	0.634
E	1.65	1.75	1.85	0.065	0.069	0.073
F	7.40	7.50	7.60	0.291	0.295	0.299
D0	1.40	1.50	1.60	0.055	0.059	0.063
D1	1.40	1.50	1.60	0.055	0.059	0.063
P0	3.90	4.00	4.10	0.154	0.157	0.161
P1	7.90	8.00	8.10	0.311	0.315	0.319
P2	1.90	2.00	2.10	0.075	0.079	0.083
A0	6.85	6.90	7.00	0.270	0.271	0.276
B0	10.45	10.50	10.60	0.411	0.413	0.417
K0	2.68	2.78	2.88	0.105	0.109	0.113
T	0.24		0.27	0.009		0.011
t1	0.10			0.004		
10P0	39.80	40.00	40.20	1.567	1.575	1.583