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AO3401 P-Channel Enhancement MOSFET  
SOT-23-3 Plastic-Encapsulate MOSFETS

产 品 规 格 书      承 认 书

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日期				

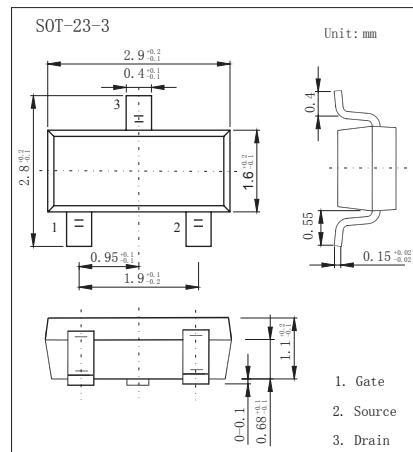
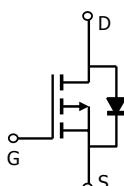


## SOT-23-3 Plastic-Encapsulate MOSFETS

### AO3401 P-Channel Enhancement MOSFET

#### ■ Features

- $V_{DS} (V) = -30V$
- $I_D = -4.2 A (V_{GS} = -10V)$
- $R_{DS(ON)} < 50m\Omega (V_{GS} = -10V)$
- $R_{DS(ON)} < 65m\Omega (V_{GS} = -4.5V)$
- $R_{DS(ON)} < 120m\Omega (V_{GS} = -2.5V)$



#### ■ Absolute Maximum Ratings $T_a = 25^\circ C$

Parameter	Symbol	Rating	Unit
Drain-Source Voltage	$V_{DS}$	-30	V
Gate-Source Voltage	$V_{GS}$	$\pm 12$	
Continuous Drain Current $T_a = 25^\circ C$	$I_D$	-4.2	A
$T_a = 70^\circ C$		-3.5	
Pulsed Drain Current	$I_{DM}$	-30	W
Power Dissipation $T_a = 25^\circ C$	$P_D$	1.4	
$T_a = 70^\circ C$		1	
Thermal Resistance.Junction- to-Ambient $t \leq 10s$	$R_{thJA}$	90	$^\circ C/W$
Thermal Resistance.Junction- to-Ambient		125	
Thermal Resistance.Junction- to-Case	$R_{thJC}$	60	
Junction Temperature	$T_J$	150	$^\circ C$
Junction and Storage Temperature Range	$T_{stg}$	-55 to 150	

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## ■ Electrical Characteristics $T_a = 25^\circ C$

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Drain-Source Breakdown Voltage	$V_{DSS}$	$I_D=-250 \mu A, V_{GS}=0V$	-30			V
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS}=-24V, V_{GS}=0V$			-1	$\mu A$
		$V_{DS}=-24V, V_{GS}=0V, T_J=55^\circ C$			-5	
Gate-Body leakage current	$I_{GSS}$	$V_{DS}=0V, V_{GS}=\pm 12V$			$\pm 100$	nA
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}, I_D=-250 \mu A$	-0.4	-1	-1.3	V
Static Drain-Source On-Resistance	$R_{DS(on)}$	$V_{GS}=-10V, I_D=-4.2A$		42	50	$m\Omega$
		$V_{GS}=-10V, I_D=-4.2A, T_J=125^\circ C$			75	
		$V_{GS}=-4.5V, I_D=-4A$		53	65	
		$V_{GS}=-2.5V, I_D=-1A$		80	120	
On state drain current	$I_{D(ON)}$	$V_{GS}=-4.5V, V_{DS}=-5V$	-25			A
Forward Transconductance	$g_{FS}$	$V_{DS}=-5V, I_D=-5A$	7	11		S
Input Capacitance	$C_{iss}$	$V_{GS}=0V, V_{DS}=-15V, f=1MHz$		954		$pF$
Output Capacitance	$C_{oss}$			115		
Reverse Transfer Capacitance	$C_{rss}$			77		
Gate resistance	$R_g$	$V_{GS}=0V, V_{DS}=0V, f=1MHz$		6		$\Omega$
Total Gate Charge	$Q_g$	$V_{GS}=-4.5V, V_{DS}=-15V, I_D=-4A$		9.4		$nC$
Gate Source Charge	$Q_{gs}$			2		
Gate Drain Charge	$Q_{gd}$			3		
Turn-On DelayTime	$t_{d(on)}$	$V_{GS}=-10V, V_{DS}=-15V, R_L=3.6 \Omega, R_{GEN}=6 \Omega$		6.3		$ns$
Turn-On Rise Time	$t_r$			3.2		
Turn-Off DelayTime	$t_{d(off)}$			38.3		
Turn-Off Fall Time	$t_f$			12		
Body Diode Reverse Recovery Time	$t_{rr}$	$I_F=-4A, dI/dt=100A/\mu s$		20.2		
Body Diode Reverse Recovery Charge	$Q_{rr}$	$I_F=5A, dI/dt=100A/\mu s$		11.2		$nC$
Maximum Body-Diode Continuous Current	$I_s$				-2.2	A
Diode Forward Voltage	$V_{SD}$	$I_s=-1A, V_{GS}=0V$		-0.75	-1	V

## ■ Marking

Marking	3401/A1**
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## ■ Typical Characteristics

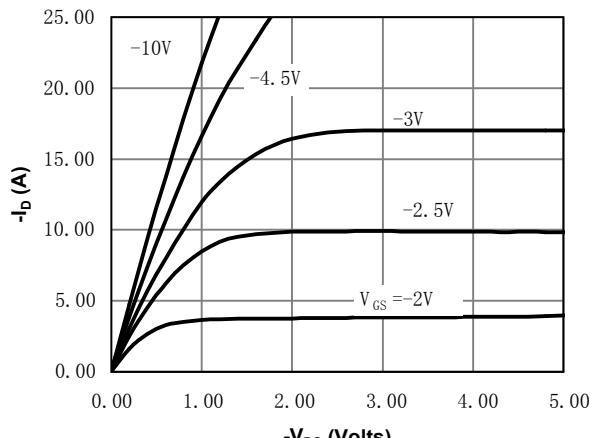


Fig 1: On-Region Characteristics

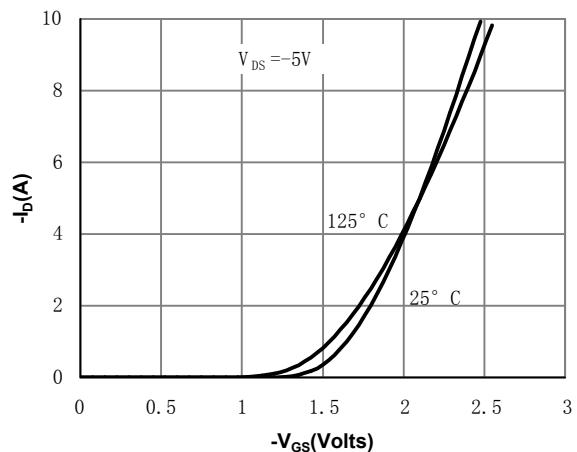


Figure 2: Transfer Characteristics

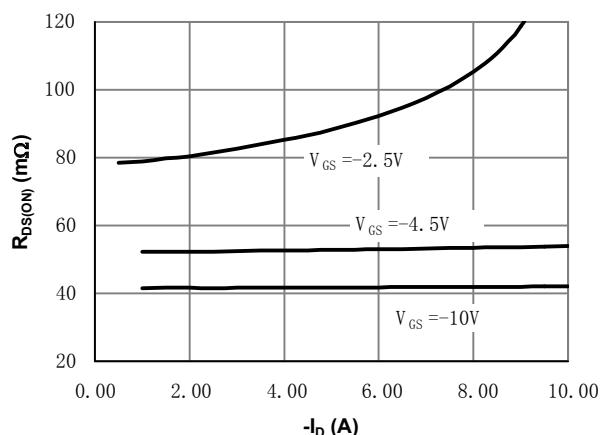


Figure 3: On-Resistance vs. Drain Current and Gate Voltage

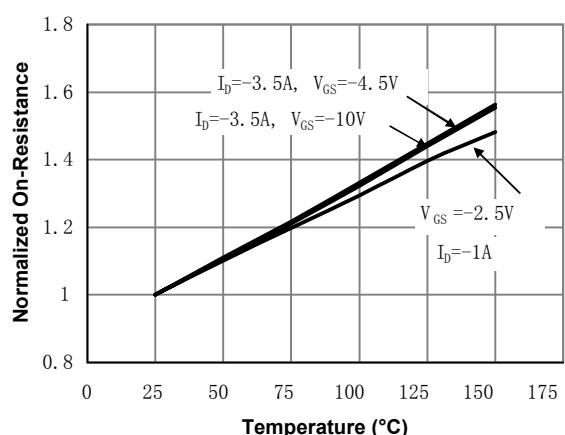


Figure 4: On-Resistance vs. Junction Temperature

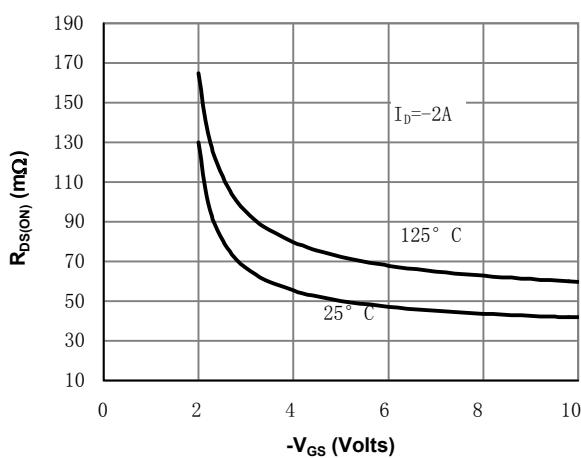


Figure 5: On-Resistance vs. Gate-Source Voltage

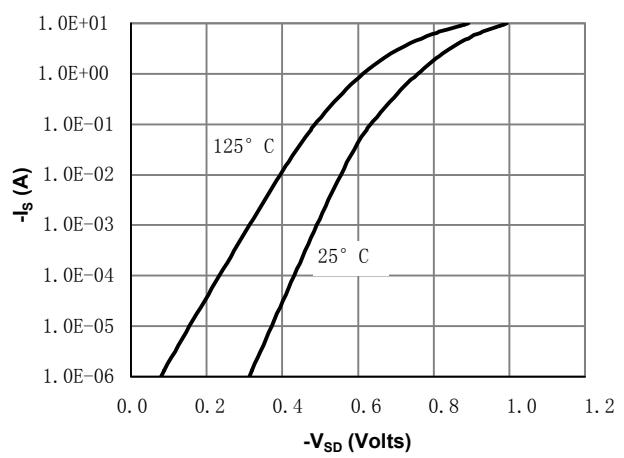


Figure 6: Body-Diode Characteristics

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## ■ Typical Characteristics

