

# isc N-Channel MOSFET Transistor

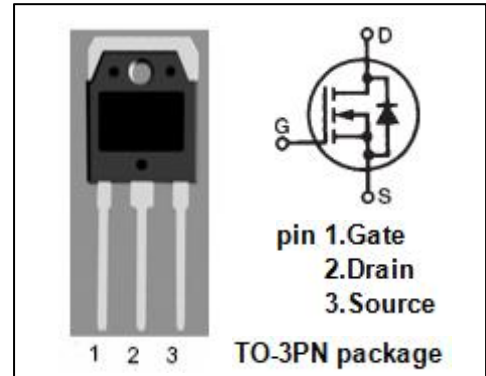
# 2SK2313

## DESCRIPTION

- Drain Current  $I_D = 60A @ T_c=25^\circ C$
- Drain Source Voltage  $V_{DSS} = 60V(\text{Min})$
- Fast Switching Speed
- 100% avalanche tested
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

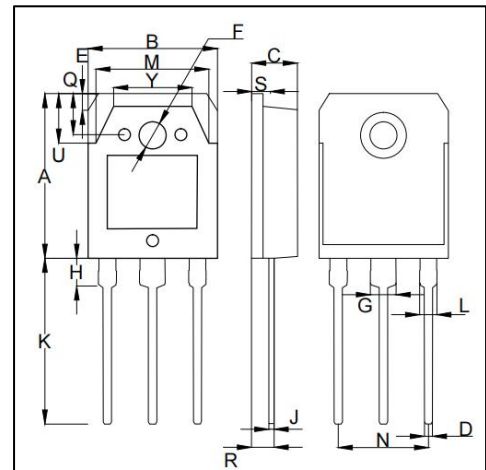
## APPLICATIONS

- Switch Mode Power Supply (SMPS)
- Uninterruptible Power Supply (UPS)
- High current switching applications
- DC-DC converter and motor drive applications



## ABSOLUTE MAXIMUM RATINGS( $T_c=25^\circ C$ )

SYMBOL	PARAMETER	VALUE	UNIT
$V_{DSS}$	Drain-Source Voltage ( $V_{GS}=0$ )	60	V
$V_{GS}$	Gate-Source Voltage	$\pm 20$	V
$I_D$	Drain Current-continuous@ $T_c=25^\circ C$	60	A
$I_{DP}^*$	Drain Current-Single Pluse	240	A
$P_{tot}$	Total Dissipation@ $T_c=25^\circ C$	150	W
$E_{AS}^{**}$	Single pulse avalanche energy	1054	mJ
$I_{AS}^*$	Avalanche Current	72.6	A
$T_j$	Max. Operating Junction Temperature	150	$^\circ C$
$T_{stg}$	Storage Temperature Range	-55~150	$^\circ C$



DIM	mm		
	MIN	TYP.	MAX
A	19.60	20.00	20.30
B	15.30	15.60	15.90
C	4.70	4.80	4.90
D	0.90	1.00	1.10
E	1.90	2.00	2.10
F	3.40	3.50	3.60
G	2.90	3.00	3.20
H	3.20	3.30	3.40
J	0.595	0.600	0.605
K	19.80	20.20	20.70
L	1.90	2.05	2.20
M	13.30	13.60	13.90
N	10.89	10.90	10.91
Q	4.90	5.00	5.10
R	3.35	3.40	3.45
S	1.995	2.000	2.100
U	5.90	6.00	6.20
Y	9.90	10.00	10.10

## THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	MAX	UNIT
$R_{thj-c}$	Thermal Resistance, Junction to Case	0.833	$^\circ C/W$

Note:

\* Repetitive Rating: Pulse width limited by maximum junction temperature

\* Pulse Test: Pulse width  $\leq 300\mu s$ , Duty Cycle  $\leq 1\%$

\*\*  $V_{DD}=50V$ ,  $L=0.4mH$ ,  $R_G=25\Omega$ ,  $I_{AS}=72.6A$

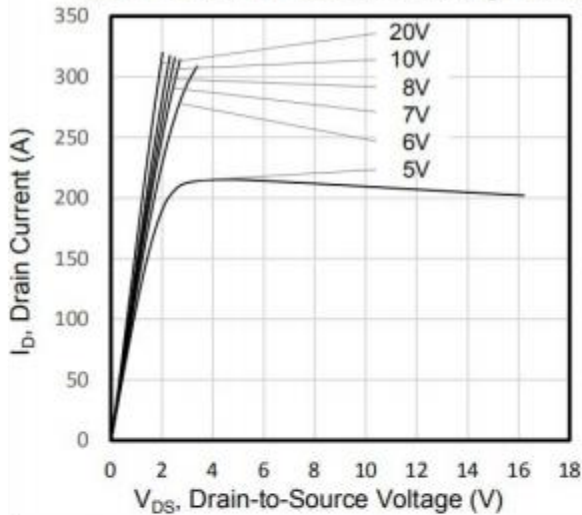
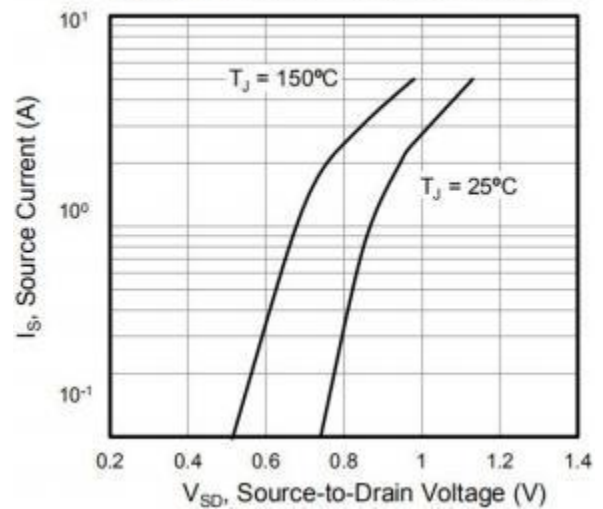
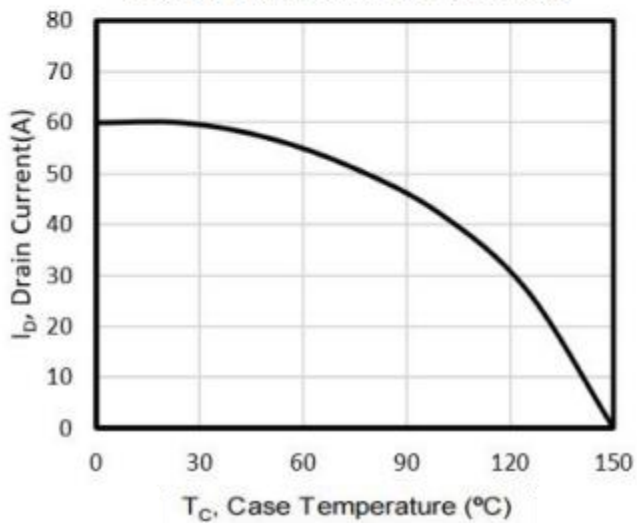
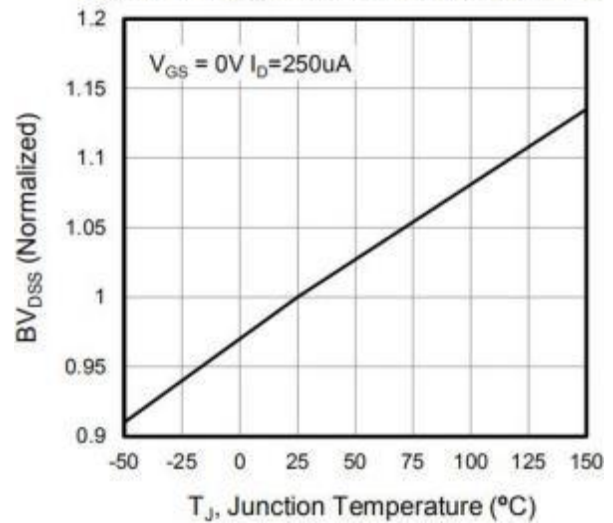
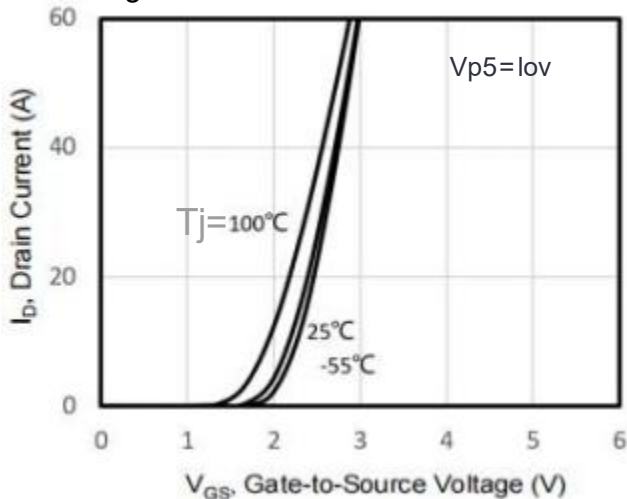
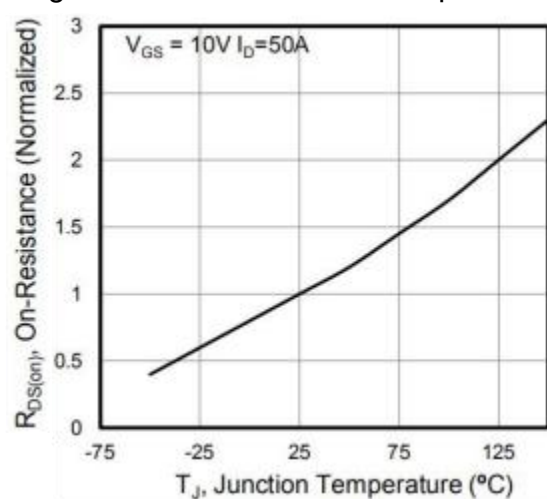
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**• ELECTRICAL CHARACTERISTICS (T<sub>c</sub>=25°C)**

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V <sub>(BR)DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> = 0V; I <sub>D</sub> = 10mA	60	-	-	V
V <sub>GS(th)</sub>	Gate Threshold Voltage	V <sub>DS</sub> = 10V, I <sub>D</sub> = 1mA	0.8	-	2.0	V
R <sub>DS(on)</sub>	Drain-Source On-stage Resistance	V <sub>GS</sub> = 10V; I <sub>D</sub> = 30A	-	-	11	mΩ
I <sub>GSS</sub>	Gate Source Leakage Current	V <sub>GS</sub> = ±16V; V <sub>DS</sub> = 0V	-	-	±10	uA
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	V <sub>DS</sub> = 60V, V <sub>GS</sub> = 0V	-	-	100	μA
C <sub>iss</sub>	Input Capacitance	V <sub>GS</sub> = 0V, V <sub>DS</sub> = 25V, f = 1.0MHz	-	3795	-	pF
C <sub>oss</sub>	Output Capacitance		-	1456	-	
C <sub>rss</sub>	Reverse Transfer Capacitance		-	1101	-	
R <sub>G</sub>	Gate resistance	f = 1.0MHz open drain	-	0.75	-	Ω
Q <sub>g</sub>	Total Gate Charge	V <sub>DD</sub> = 48V, I <sub>D</sub> = 60A, V <sub>GS</sub> = 10V	-	346	-	nC
Q <sub>gs</sub>	Gate-Source Charge		-	6	-	
Q <sub>gd</sub>	Gate-Drain Charge		-	126	-	
t <sub>d(on)</sub>	Turn-on Delay Time	V <sub>DD</sub> = 40V, I <sub>D</sub> = 60A, R <sub>G</sub> = 25Ω	-	52	-	ns
t <sub>r</sub>	Turn-on Rise Time		-	312	-	
t <sub>d(off)</sub>	Turn-off Delay Time		-	905	-	
t <sub>f</sub>	Turn-off Fall Time		-	649	-	

**Drain - Source Body Diode Characteristics**

I <sub>SD</sub>	Continuous Source Current	T <sub>c</sub> = 25 °C	-	-	60	A
I <sub>SM</sub>	Pulsed Source Current		-	-	240	
V <sub>SD</sub>	Diode Forward Voltage	I <sub>SD</sub> = 60A; V <sub>GS</sub> = 0V	-	-	1.7	V
t <sub>rr</sub>	Reverse Recovery Time	V <sub>DD</sub> = 40V, I <sub>F</sub> = 60A, di <sub>F</sub> /dt = 60A/μs	-	300	-	ns
Q <sub>rr</sub>	Reverse Recovery Charge		-	2.5	-	uC

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**Typical Characteristics**  $T_J = 25^\circ\text{C}$ , unless otherwise noted

**Figure 1. Output Characteristics ( $T_J = 25^\circ\text{C}$ )**

**Figure 2. Body Diode Forward Voltage**

**Figure 3. Drain Current vs. Temperature**

**Figure 4.  $BV_{DSS}$  Variation vs. Temperature**

**Figure 5. Transfer characteristics**

**Figure 6. on-Resistance vs. Temperature**


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Figure 7. Capacitance

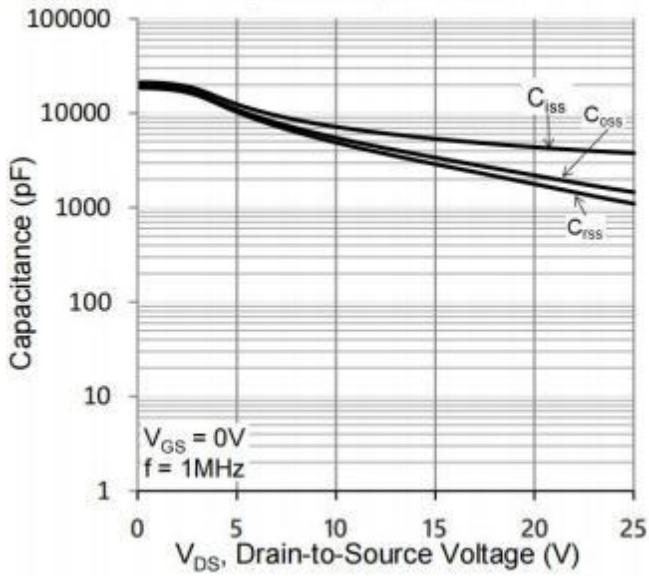


Figure 8. Gate Charge

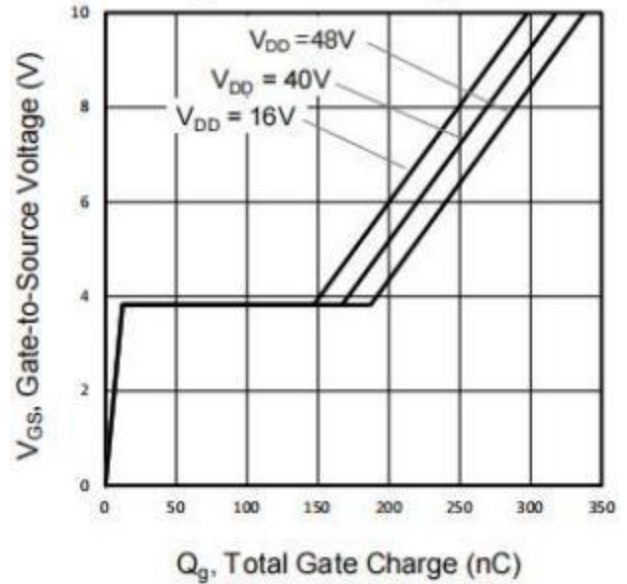


Figure 9. Transient Thermal Impedance

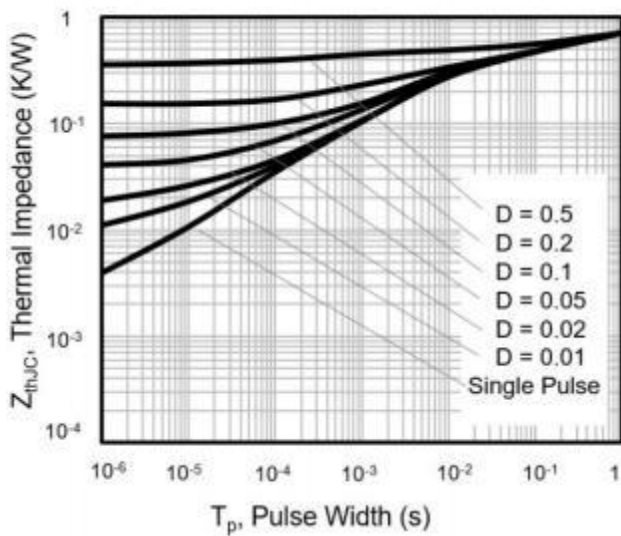
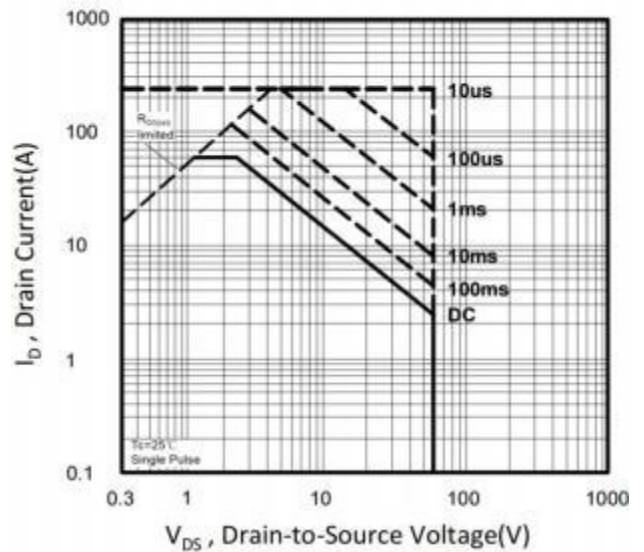


Figure 10. Safe Operating Area



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Figure A: Gate Charge Test Circuit and Waveform

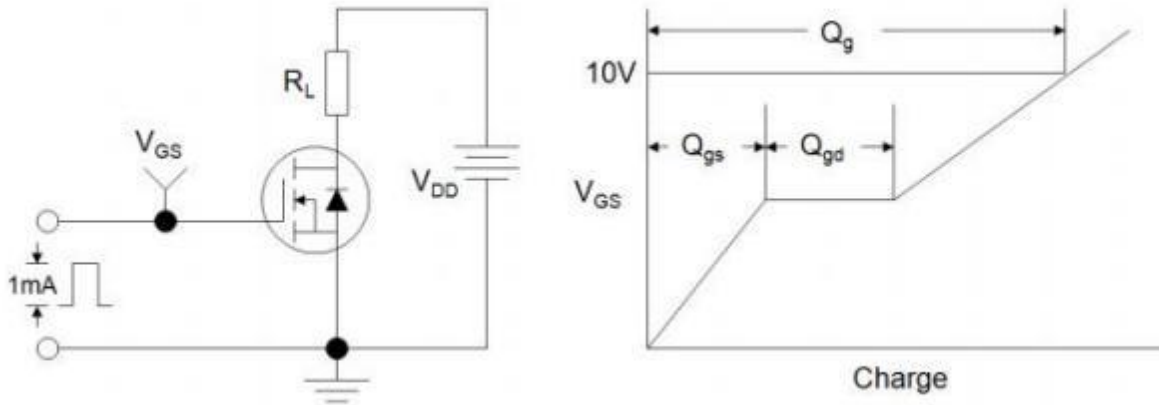


Figure B: Resistive Switching Test Circuit and Waveform

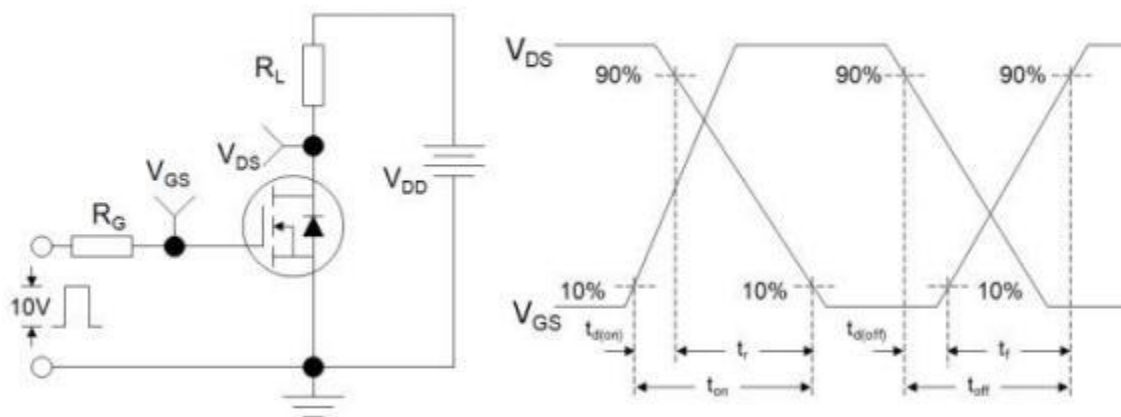
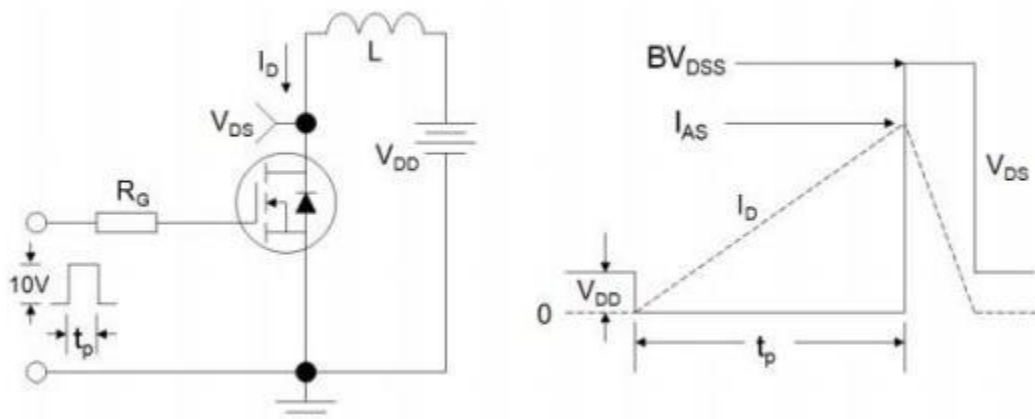


Figure C: Unclamped Inductive Switching Test Circuit and Waveform



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