

isc Silicon NPN Power Transistor
2SC3632
DESCRIPTION

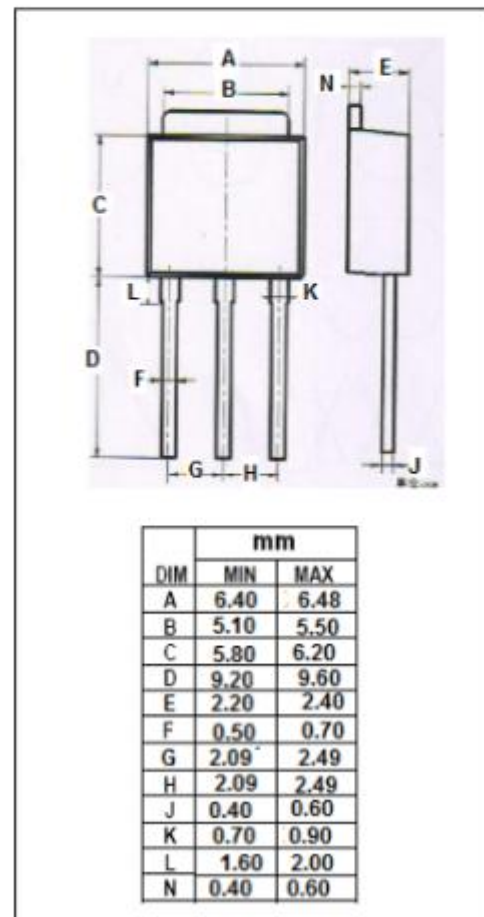
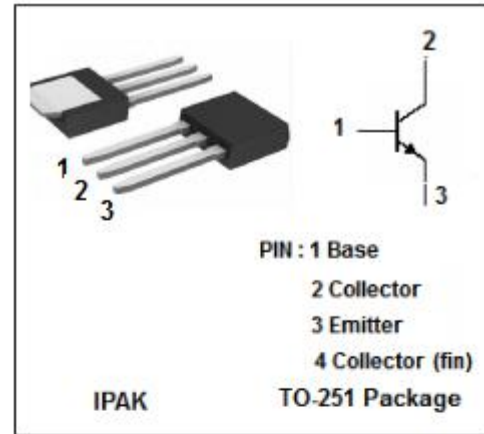
- High Collector-Emitter Voltage
- Low collector saturation voltage
- 100% avalanche tested
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

APPLICATIONS

- High voltage switching.

ABSOLUTE MAXIMUM RATINGS($T_a=25^\circ\text{C}$)

SYMBOL	PARAMETER	VALUE	UNIT
V_{CBO}	Collector-Base Voltage	600	V
V_{CEO}	Collector-Emitter Voltage	600	V
V_{EBO}	Emitter-Base Voltage	7	V
I_C	Collector Current-Continuous	1	A
P_C	Collector Power Dissipation	2.0	W
T_J	Junction Temperature	150	$^\circ\text{C}$
T_{stg}	Storage Temperature Range	-55~150	$^\circ\text{C}$



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ELECTRICAL CHARACTERISTICS

T_c=25°C unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V _{CE(sat)} ^{NOTE}	Collector-Emitter Saturation Voltage	I _C = 400mA; I _B = 80mA			1	V
V _{BE(sat)} ^{NOTE}	Base-Emitter Saturation Voltage	I _C = 400mA; I _B = 80mA			1.2	V
I _{CBO}	Collector Cutoff Current	V _{CB} = 600V; I _E = 0			10	μ A
I _{EBO}	Emitter Cutoff Current	V _{EB} = 7V; I _C = 0			10	μ A
h _{FE-1} ^{NOTE}	DC Current Gain	I _C = 100mA; V _{CE} = 5V	30		120	
h _{FE-2} ^{NOTE}	DC Current Gain	I _C = 500mA; V _{CE} = 2V	5			

NOTE:Pulse test PW≤350us,duty cycle ≤2%

◆ h_{FE-1} Classifications

M	L	K
30-60	40-80	60-120

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