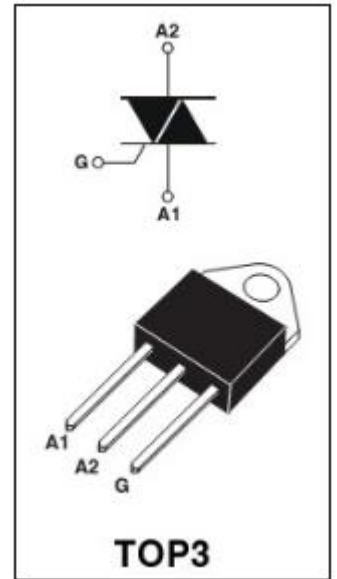


**isc Triacs**
**BTA41-600B**
**FEATURES**

- With TOP3 insulated package
- Suitable for general purpose where high surge current capability is required.  
Application such as phase control and tatic switching on inductive or resistive load.
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

**ABSOLUTE MAXIMUM RATINGS(Ta=25°C)**

SYMBOL	PARAMETER	MIN	UNIT
V <sub>DRM</sub>	Repetitive peak off-state voltage	600	V
V <sub>RRM</sub>	Repetitive peak reverse voltage	600	V
I <sub>T(RMS)</sub>	RMS on-state current (full sine wave)T <sub>j</sub> =80°C	41	A
I <sub>TSM</sub>	Non-repetitive peak on-state current t <sub>p</sub> =20ms	410	A
T <sub>j</sub>	Operating junction temperature	125	°C
T <sub>stg</sub>	Storage temperature	-40~150	°C
P <sub>G(AV)</sub>	Average gate power dissipation(T <sub>j</sub> =125°C)	1	W
R <sub>th(j-c)</sub>	Thermal resistance, junction to case	0.9	°C/W
R <sub>th(j-a)</sub>	Thermal resistance, junction to ambient	50	°C/W


**ELECTRICAL CHARACTERISTICS (T<sub>c</sub>=25°C unless otherwise specified)**

SYMBOL	PARAMETER	CONDITIONS	MAX	UNIT	
I <sub>RRM</sub>	Repetitive peak reverse current	V <sub>R</sub> =V <sub>RRM</sub> , V <sub>R</sub> =V <sub>RRM</sub> , T <sub>j</sub> =125°C	0.005 5.0	mA	
I <sub>DRM</sub>	Repetitive peak off-state current	V <sub>D</sub> =V <sub>DRM</sub> , V <sub>D</sub> =V <sub>DRM</sub> , T <sub>j</sub> =125°C	0.005 5.0	mA	
I <sub>GT</sub>	Gate trigger current	V <sub>D</sub> =12V; R <sub>L</sub> = 100 Ω	I	50	mA
			II	50	
			III	50	
			IV	100	
I <sub>H</sub>	Holding current	I <sub>GT</sub> = 0.5A, Gate Open	80	mA	
V <sub>GT</sub>	Gate trigger voltage all quadrant	V <sub>D</sub> =12V; R <sub>L</sub> = 100 Ω	1.3	V	
V <sub>TM</sub>	On-state voltage	I <sub>TM</sub> = 60A; t <sub>p</sub> = 380 μ s	1.55	V	

**NOTICE:**

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