

# **isc Silicon NPN Power Transistor**

### **DESCRIPTION**

- · Fast switching speed
- NPN epitaxial planar silicon transistor
- 100% avalanche tested
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

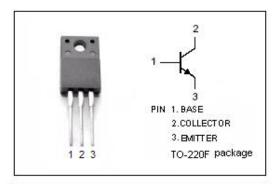
### **APPLICATIONS**

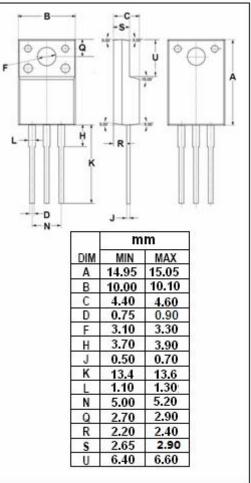


- · High deflection CRT display
- Horizontal deflection output applications

# ABSOLUTE MAXIMUM RATINGS(Ta=25℃)

SYMBOL	PARAMETER	VALUE	UNIT
V <sub>CBO</sub>	Collector-Base Voltage	400	V
Vceo	Collector-Emitter Voltage	200	V
V <sub>EBO</sub>	Emitter-Base Voltage	6	V
l <sub>C</sub>	Collector Current-Continuous	7	Α
l <sub>B</sub>	Base Current-Continuous	4	А
Pc	Collector Power Dissipation @Tc=25℃	30	W
TJ	Junction Temperature	150	$^{\circ}$
T <sub>stg</sub>	Storage Temperature	-55~150	







## **ISC Silicon NPN Power Transistor**

2SC4478

#### **ELECTRICAL CHARACTERISTICS**

Tj=25℃ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V <sub>(BR)CEO</sub>	Collector-Emitter Breakdown Voltage	I <sub>C</sub> = 25mA; I <sub>B</sub> = 0	200			V
V <sub>CE</sub> (sat)	Collector-Emitter Saturation Voltage	I <sub>C</sub> = 5A; I <sub>B</sub> = 0.5A			0.8	V
V <sub>BE</sub> (sat)	Base-Emitter Saturation Voltage	I <sub>C</sub> = 5A; I <sub>B</sub> = 0.5A			1.5	V
І <sub>СВО</sub>	Collector Cutoff Current	V <sub>CB</sub> = 250V; I <sub>E</sub> = 0			100	μА
I <sub>EBO</sub>	Emitter Cutoff Current	V <sub>EB</sub> = 5V; I <sub>C</sub> = 0			100	μА
h <sub>FE-1</sub>	DC Current Gain	I <sub>C</sub> = 1A; V <sub>CE</sub> = 1V	15			
h <sub>FE-2</sub>	DC Current Gain	I <sub>C</sub> = 5A; V <sub>CE</sub> = 1V	10		50	
f⊤	Current-Gain—Bandwidth Product	I <sub>E</sub> = -0.5A; V <sub>CE</sub> = 10V		40		MHz

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