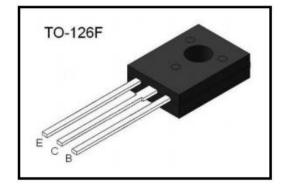


### **isc Silicon NPN Power Transistor**

# **KSE340**

### DESCRIPTION

- High Collector-Emitter breakdown voltage
- Low Collector Saturation Voltage
- Complement to Type KSE350
- Minimum Lot-to-Lot variations for robust device performance and reliable operation



### **APPLICATIONS**

- High voltage general purpose applications
- Suitable for transform

#### SYMBOL PARAMETER VALUE UNIT V<sub>СВО</sub> 300 V Collector-Base Voltage 300 $V_{\text{CEO}}$ Collector-Emitter Voltage V 5 V VEBO Emitter-Base Voltage **Collector Current-Continuous** 0.3 lc А Collector Power Dissipation 20 @ Tc=25°C Pc W **Collector Power Dissipation** 1.2 @ T<sub>a</sub>=25℃ ТJ Junction Temperature 150 °C -55~150 T<sub>stg</sub> Storage Temperature Range °C

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### ABSOLUTE MAXIMUM RATINGS(Ta=25℃)



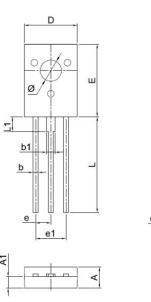
## **isc Silicon NPN Power Transistor**

# **KSE340**

### **ELECTRICAL CHARACTERISTICS**

#### $\mathsf{Tc}\text{=}25\,^\circ\!\!\mathbb{C}$ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	MAX	UNIT
V <sub>(BR)CEO</sub>	Collector-Emitter Breakdown Voltage	I <sub>C</sub> = 10mA; I <sub>B</sub> = 0	300		V
V <sub>(BR)CBO</sub>	Collector-Base Breakdown Voltage	Ic= 0.1mA; I <sub>E</sub> = 0	300		V
I <sub>CBO</sub>	Collector Cutoff Current	V <sub>CB</sub> = 300V; I <sub>E</sub> = 0		100	μA
Іево	Emitter Cutoff Current	V <sub>EB</sub> = 3V; I <sub>C</sub> = 0		100	μA
V <sub>CE(sat)</sub>	Collector-emitter saturation voltage	$I_{\rm C} = 50 \text{mA}, I_{\rm B} = 5 \text{mA}$		0.75	V
V <sub>BE(sat)</sub>	Base -emitter saturation voltage	I <sub>C</sub> = 10mA, I <sub>B</sub> = 1mA		0.75	V
h <sub>FE</sub>	DC Current Gain	I <sub>C</sub> = 50mA; V <sub>CE</sub> = 10V	30	240	



Sumbal	Millir	neter
Symbol	Min.	Max.
A	3.00	3.30
A1	1.60	1.80
b	0.66	0.86
b1	1.17	1.37
С	0.30	0.50
D	7.50	7.90
E	10.60	<mark>11.0</mark> 0
е	2.25	2.33
e1	4.50	4.66
L	14.50	<mark>15.5</mark> 0
L1	1.90	2.50
Φ	3.10	3.30

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