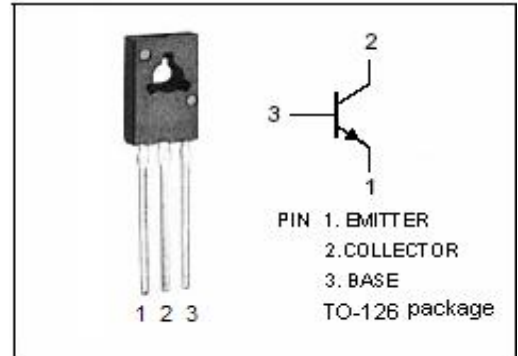


**isc Silicon NPN Power Transistor**
**MJE200**
**DESCRIPTION**

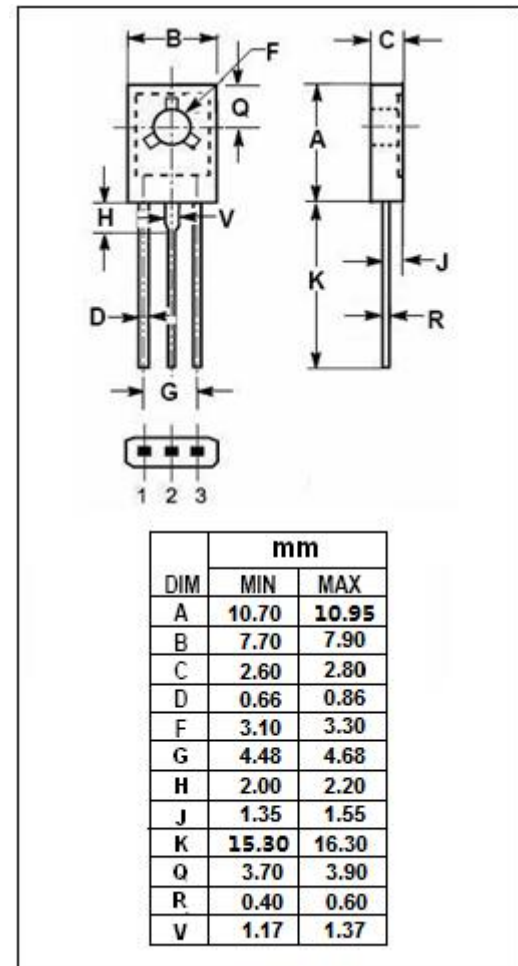
- Collector–Emitter Sustaining Voltage–  
:  $V_{CEO(SUS)} = 25V(\text{Min})$
- DC Current Gain–  
:  $h_{FE} = 70(\text{Min}) @ I_C = 500\text{mA}$
- Low Collector-Emitter Saturation Voltage–  
:  $V_{CE(sat)} = 0.3V(\text{Max}) @ I_C = 500\text{mA}$
- High Current-Gain–Bandwidth Product  
:  $f_T = 65\text{MHz}(\text{Min}) @ I_C = 100\text{mA}$
- Minimum Lot-to-Lot variations for robust device performance and reliable operation


**APPLICATIONS**

- Designed for low voltage, low-power, high-gain audio amplifier applications.

**ABSOLUTE MAXIMUM RATINGS ( $T_a = 25^\circ\text{C}$ )**

SYMBOL	PARAMETER	VALUE	UNIT
$V_{CBO}$	Collector-Base Voltage	40	V
$V_{CEO}$	Collector-Emitter Voltage	25	V
$V_{EBO}$	Emitter-Base Voltage	8	V
$I_C$	Collector Current-Continuous	5.0	A
$I_{CM}$	Collector Current-Peak	10	A
$I_B$	Base Current-Continuous	1.0	A
$P_C$	Collector Power Dissipation $T_C = 25^\circ\text{C}$	15	W
$T_j$	Junction Temperature	150	$^\circ\text{C}$
$T_{stg}$	Storage Temperature Range	-65~150	$^\circ\text{C}$


**THERMAL CHARACTERISTICS**

SYMBOL	PARAMETER	MAX	UNIT
$R_{th\ j-c}$	Thermal Resistance, Junction to Case	8.34	$^\circ\text{C/W}$
$R_{th\ j-a}$	Thermal Resistance, Junction to Ambient	83.4	$^\circ\text{C/W}$

## isc Silicon NPN Power Transistor

## MJE200

## ELECTRICAL CHARACTERISTICS

T<sub>c</sub> =25°C unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	MAX	UNIT
V <sub>CEO(SUS)</sub>	Collector-Emitter Sustaining Voltage	I <sub>C</sub> = 10mA; I <sub>B</sub> = 0	25		V
I <sub>CBO</sub>	Collector Cutoff Current	V <sub>CB</sub> = 40V; I <sub>E</sub> = 0		100	nA
I <sub>EBO</sub>	Emitter Cutoff Current	V <sub>EB</sub> = 8V; I <sub>C</sub> = 0		100	nA
h <sub>FE-1</sub>	DC Current Gain	I <sub>C</sub> = 500m A ; V <sub>CE</sub> = 1V	70		
h <sub>FE-2</sub>	DC Current Gain	I <sub>C</sub> = 2A ; V <sub>CE</sub> = 1V	45	180	
h <sub>FE-3</sub>	DC Current Gain	I <sub>C</sub> = 5A ; V <sub>CE</sub> = 2V	10		
V <sub>CE(sat)-1</sub>	Collector-Emitter Saturation Voltage	I <sub>C</sub> = 500mA ;I <sub>B</sub> = 50mA		0.3	V
V <sub>CE(sat)-2</sub>	Collector-Emitter Saturation Voltage	I <sub>C</sub> = 2A ;I <sub>B</sub> = 200mA		0.75	V
V <sub>CE(sat)-3</sub>	Collector-Emitter Saturation Voltage	I <sub>C</sub> = 5A ;I <sub>B</sub> = 1A		1.8	V
V <sub>BE(sat)</sub>	Base-Emitter Saturation Voltage	I <sub>C</sub> =5A; I <sub>B</sub> = 1A		2.5	V
V <sub>BE(on)</sub>	Base-Emitter On Voltage	I <sub>C</sub> = 2A; V <sub>CE</sub> = 1V		1.6	V
f <sub>T</sub>	Current-Gain—Bandwidth Product	I <sub>C</sub> = 100m A; V <sub>CE</sub> = 10V;	65		MHz
C <sub>OB</sub>	Output Capacitance	I <sub>E</sub> = 0 ; V <sub>CB</sub> = 10V,f <sub>test</sub> = 0.1MHz		80	pF

Pulse test PW=300us,duty cycle≤2%

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