

R7F701304EAFP

The high-end microcomputer for chassis which carries a variegated safety feature and offers further relief and safety

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OVERVIEW

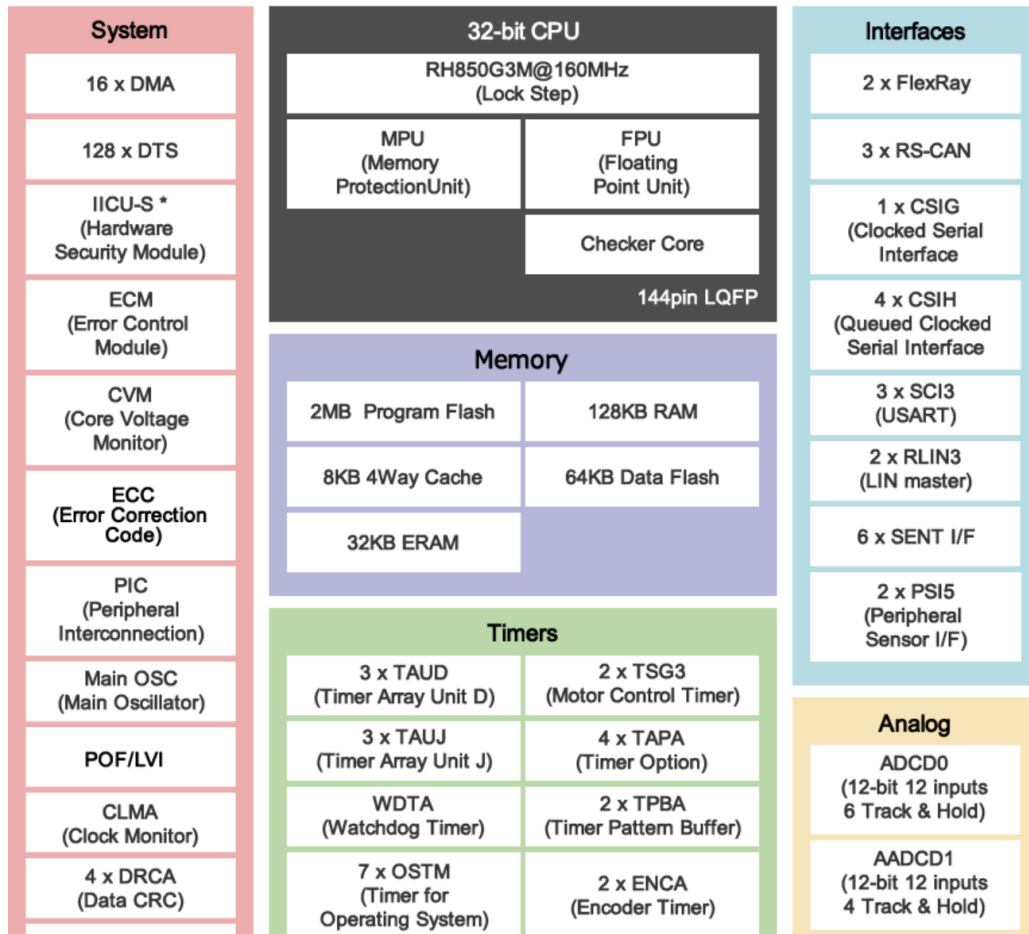
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The RH850/P1M microcontroller is low power consumption and it has up to 2 MB flash and up to 128 KB RAM lineup and enhanced motor control timer, CAN interface, digital interface for sensor such as SENT and PSi5 and safety functions such as lockstep CPU, ECC, BIST (Built-in Self Test) and ECM (Error Control Module) and suitable for chassis systems. Furthermore Only 2 MB flash product is built-in ICU-S (Hardware Security Module) as optional.

Product Status: Product Inquiry
Family: RH850
Series: RH850/P1x
Group: RH850/P1M

BLOCK DIAGRAM

144-pin Code Flash 2 MB Product



JTAG / LDU

Temperature
Sensor

* Optional

PARAMETRICS

Parameters	R7F701304EAFP
Package	
Pin Count	100
Package Type	LFQFP
Memory	
Program memory	512 KB Flash memory
RAM	64 KB
Data Flash	32 KB
Cache Memory Remark	8KB
CPU (Bit, Clock, DMA, External Bus etc)	
CPU	G3M (32-bit)
Max. Frequency	160 MHz
On-chip Oscillator Freq.	8 MHz
PLL	YES
Power-On Reset	YES
Low Voltage Detection	YES
Floating Point Unit	YES
DMA Remarks	DMAC x 16 ch, DTS x 128 ch
External Interrupt Pins	14
I/O Ports	52
Timers	
16-bit Timers	48 ch
32-bit Timers	12 ch
Watchdog Timers	1 ch
Other Timers	TSG3 x2unit, ENCA x2unit, OSTM x7unit, TPBA x2unit
PWM Output	32
3-Phase PWM Output Function	YES
Analog	
A/D Converters	12-bit x 19 ch
Interfaces	
CAN	2 ch
CSIs	5 ch
UARTs	3 ch
LIN	2 ch

Operating Conditions	
Operating Voltage	3 to 5.5 V
Power Supply	3.0V to 5.5V (Built in voltage regulator for core)
Operating Temperature	-40 to 150 °C

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