

FP7208A

Non-Synchronous PWM Boost Controller for LED Driver

General Description

The FP7208A is boost topology switching regulator for LED driver. It provides built-in gate driver pin for driving external N-MOSFET. The non-inverting input of error amplifier connects to a 0.2V reference voltage. It has programmable soft start time set by external capacitor, and over voltage protection set by external resistor. The LED current can be adjusted by an external signal connecting to the EN pin. EN pin accepts either a DC voltage or a PWM signal. The PWM signal filter components are contained within the chip.

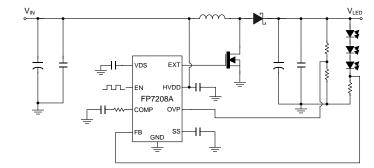
Features

- Start-up Voltage: 2.5V
- ➤ Wide Supply Voltage Operating Range: 4.5V to 24V
- > Precision Feedback Reference Voltage: 0.2V (Max.)
- > Analog and Digital Dimming Control
- Shutdown Current: 6µA (Max.)
- Internal Fixed Switching Frequency: 300KHz
- Programmable Soft Start Function (SS)
- Programmable Over Voltage Protection (OVP)
- Over Temperature Protection (OTP)
- Package: SOP-8L(EP)

Applications

- LED Module
- Display Backlight
- Car Lighting
- Portable LED Lighting

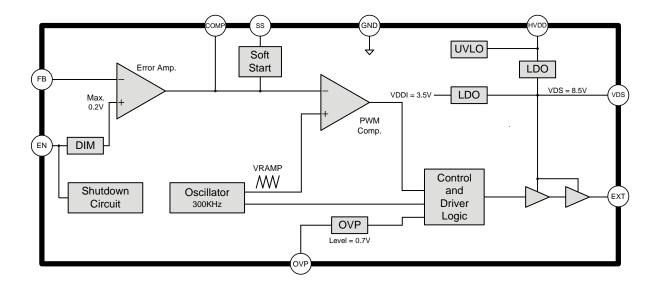
Typical Application Circuit



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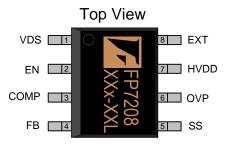


Function Block Diagram



Pin Descriptions

SOP-8L (EP)



Bottom View



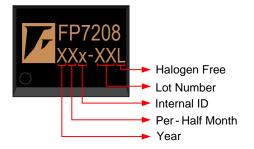
Name	No.	1/0	Description
VDS	1	Р	Power Supply for Internal Control Circuits and Gate Drivers
EN	2	I	Enable and Dimming Control
COMP	3	0	Compensation
FB	4	I	Error Amplifier Inverting Input
SS	5	I	Soft Start Programming
OVP	6	I	Over Voltage Protection
HVDD	7	Р	IC Power Supply
EXT	8	0	Gate Driver Output
GND	9	Р	IC Ground (Exposed PAD) – Must Connect to Ground

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Marking Information

SOP-8L(EP)



Halogen Free: Halogen free product indicator Lot Number: Wafer lot number's last two digits

For Example \rightarrow Lot : 123466 \rightarrow XXx-66L

Internal ID: Internal Identification Code

Per-Half Month: Production period indicator in half month time unit

For Example : A \rightarrow First Half Month of January

- $B \rightarrow$ Second Half Month of January
- $\mathsf{C} \ \rightarrow \text{First Half Month of February}$
- $\mathsf{D} \ \rightarrow \text{Second Half Month of February}$

Year: Production year's last digit

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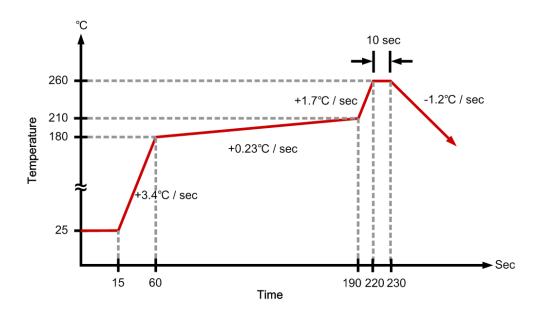
Ordering Information

Part Number	Operating Temperature	Package	MOQ	Description
FP7208AXR-G1	-25°C ~ 85°C	SOP-8L(EP)	2500EA	Tape & Reel

Absolute Maximum Ratings

Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Supply Voltage	HVDD	-	0	-	25	V
VDS,EXT Voltage	-	-	0	-	16	V
Others Pin Voltage	-	-	0	-	6	V
Thermal Resistance (Junction to Ambient)	θ _{JA}	SOP-8L (EP)	-	-	+60	°C / W
Thermal Resistance (Junction to Case)	θ _{JC}	SOP-8L (EP)	-	-	+10	°C / W
Junction Temperature	TJ	-	-	-	+150	°C
Operating Temperature	T _{OP}	-	-25	-	+85	°C
Storage Temperature	T _{ST}	-	-65	-	+150	°C
Lead Temperature	-	(soldering, 10 sec)	-	-	+260	°C

IR Re-flow Soldering Curve



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Recommended Operating Conditions

Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Supply Voltage	HVDD	-	4.5	-	24	V
Operating Temperature Range	T _A	Ambient Temperature	-25	-	+85	°C

DC Electrical Characteristics (HVDD=12V, T_A=25°C, unless otherwise specified)

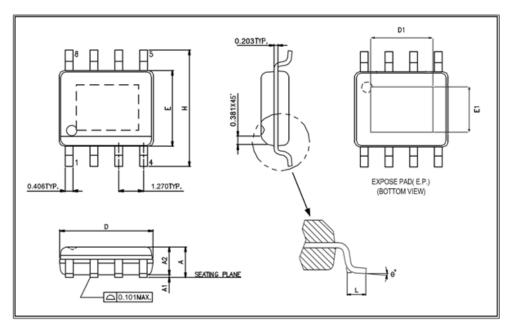
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
System Supply Input	L					
Start-up Voltage	HV_{DD}	-	2.5			V
Input Supply Range	HV _{DD}	-	4.5	-	24	V
Under Voltage Lockout	V _{UVLO}	-	-	2.2	-	V
UVLO Hysteresis	-	-	-	0.1	-	V
Average Current	Icc	FB=0V, Switching	-	5	-	mA
Quiescent Current	I _{CC}	FB=0.55V, No Switching	-	245	-	μA
Shutdown Current	Icc	V _{EN} =GND	-	-	6	μA
Input Supply Voltage	V _{DS}	HV _{DD} =12V, I _{DS} =0A	-	8.5	-	V
Oscillator						
Operation Frequency	f _{OSC}	FB=0V	250	300	350	KHz
Maximum Duty Ratio	%	FB=0V	-	90	-	%
Soft Start						
Soft-Start bias Current	I _{SS}	V _{ss} =0V	-	1.5	-	μA
Reference Voltage						
Feedback Voltage	V _{FB}	Connect EN to VDS with a 100kΩ, EN≒3V	0.197	0.2	0.203	V
Enable Control						
Enable Voltage	V_{EN}	-	0.25	-	-	V
Shutdown Voltage	V_{EN}	-	-	-	0.07	V
External Transistor Conne	ction curre	ent				
EXT Pull-UP Resistance	R _{EXTH}	V _{DS} =8.5V	-	1	-	Ω
EXT Pull-Down Resistance	R _{EXTL}	V _{DS} =8.5V	-	1	-	Ω
Over Voltage Protection						
OVP Threshold	VOVP	-	0.65	0.70	0.75	V
Thermal Shutdown						
Thermal Shutdown Threshold	T _{TS}	-	-	+150	-	°C

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Package Outline

SOP-8L (EP)



Symbols	Min. (mm)	Max. (mm)
A	1.30	1.70
A1	0	0.15
A2	1.25	1.55
D	4.70	5.10
E	3.80	4.00
Н	5.80	6.20
L	0.40	1.27

UNIT: mm

Exposed PAD Dimensions:

Symbols	Min. (mm)	Max. (mm)
D1	2.60	3.45
E1	1.90	2.56

Note:

- 1. Package dimensions are in compliance with JEDEC outline: MS-012 AA.
- 2. Dimension "D" does not include molding flash, protrusions or gate burrs.
- 3. Dimension "E" does not include inter-lead flash or protrusions.

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