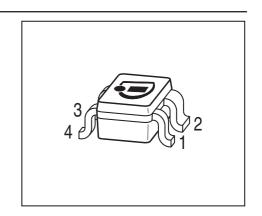


### Low Noise Silicon Bipolar RF Transistor

- For high gain and low noise amplifiers
- Minimum noise figure  $NF_{min}$  = 1.1 dB at 1.8 GHz Outstanding  $G_{ms}$  = 21 dB at 1.8 GHz
- For oscillators up to 10 GHz
- Transition frequency  $f_T$  = 25 GHz
- Pb-free (RoHS compliant) and halogen-free package with visible leads
- Qualification report according to AEC-Q101 available







## **ESD** (Electrostatic discharge) sensitive device, observe handling precaution!

Туре	Marking	Pin Configuration					Package	
BFP420	AMs	1=B	2=E	3=C	4=E	-	-	SOT343

# **Maximum Ratings** at $T_A$ = 25 °C, unless otherwise specified

Parameter	Symbol	Value	Unit
Collector-emitter voltage	$V_{CEO}$		V
T <sub>A</sub> = 25 °C		4.5	
<i>T</i> <sub>A</sub> = -55 °C		4.1	
Collector-emitter voltage	V <sub>CES</sub>	15	
Collector-base voltage	$V_{\mathrm{CBO}}$	15	
Emitter-base voltage	$V_{EBO}$	1.5	
Collector current	$I_{\mathbb{C}}$	60	mA
Base current	$I_{B}$	9	
Total power dissipation <sup>1)</sup>	P <sub>tot</sub>	210	mW
<i>T</i> <sub>S</sub> ≤ 98 °C			
Junction temperature	$T_{J}$	150	°C
Storage temperature	T <sub>Stg</sub>	-55 150	

 $<sup>^{1}</sup>T_{\mathrm{S}}$  is measured on the emitter lead at the soldering point to the pcb

#### **Thermal Resistance**

Parameter	Symbol	Value	Unit
Junction - soldering point <sup>1)</sup>	R <sub>thJS</sub>	250	K/W



**Electrical Characteristics** at  $T_A$  = 25 °C, unless otherwise specified

Parameter	Symbol	Values			Unit
		min.	typ.	max.	
DC Characteristics	•			•	
Collector-emitter breakdown voltage	V <sub>(BR)CEO</sub>	4.5	5	_	V
$I_{\rm C} = 1 \text{ mA}, I_{\rm B} = 0$					
Collector-emitter cutoff current	I <sub>CES</sub>	-	-	10	μΑ
$V_{CE} = 15 \text{ V}, V_{BE} = 0$					
Collector-base cutoff current	I <sub>CBO</sub>	-	-	100	nA
$V_{\rm CB} = 5 \text{ V}, I_{\rm E} = 0$					
Emitter-base cutoff current	I <sub>EBO</sub>	-	-	3	μΑ
$V_{\rm EB} = 0.5  \text{V}, I_{\rm C} = 0$					
DC current gain	h <sub>FE</sub>	60	95	130	_
$I_{\rm C}$ = 20 mA, $V_{\rm CE}$ = 4 V, pulse measured					

 $<sup>^{1}</sup>$ For the definition of  $R_{\mathrm{thJS}}$  please refer to Application Note AN077 (Thermal Resistance Calculation)



**Electrical Characteristics** at  $T_{\Delta}$  = 25 °C, unless otherwise specified

g)   f <sub>T</sub>	min.	typ.	max.	
Ĭ			1	
$f_{T}$		1		
	18	25	-	GHz
C <sub>cb</sub>	-	0.15	0.3	pF
$C_{ce}$	-	0.37	-	
C <sub>eb</sub>	-	0.55	-	
NF <sub>min</sub>	-	1.1	-	dB
G <sub>ms</sub>	-	21	-	dB
$ S_{21} ^2$	14	17	-	
IP3	-	22	-	dBm
P <sub>-1dB</sub>	-	12	-	
	$C_{ m ce}$ $C_{ m ce}$ $NF_{ m min}$ $G_{ m ms}$ $ S_{21} ^2$ $IP3$	$C_{ m ce}$ - $C_{ m ce}$ - $C_{ m eb}$ - $C_{ m min}$	$C_{ce}$ - 0.37 $C_{eb}$ - 0.55 $NF_{min}$ - 1.1 $G_{ms}$ - 21 $ S_{21} ^2$ 14 17	$C_{ce}$ - 0.37 - $C_{eb}$ - 0.55 - $C_{min}$ - 1.1 - $C_{min}$ - 21 - $C_{min}$ - 22 - $C_{min}$ - 22 - $C_{min}$

 $<sup>^{1}</sup>G_{ms} = |S_{21} / S_{12}|$ 

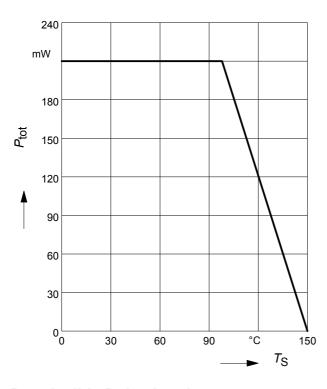
<sup>&</sup>lt;sup>2</sup>IP3 value depends on termination of all intermodulation frequency components.

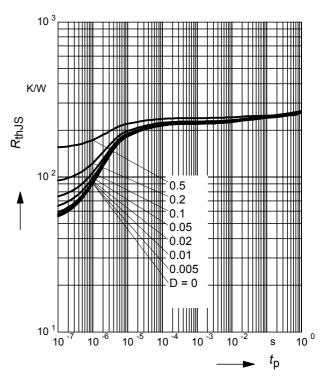
Termination used for this measurement is  $50\Omega$  from 0.1 MHz to 6 GHz



Total power dissipation  $P_{tot} = f(T_S)$ 

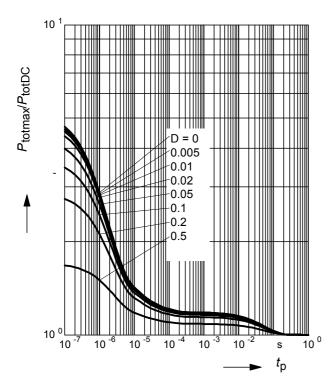
Permissible Pulse Load  $R_{thJS} = f(t_p)$ 



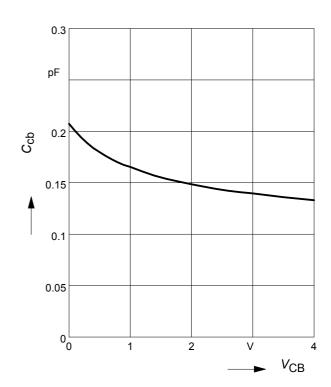


## **Permissible Pulse Load**

 $P_{\text{totmax}}/P_{\text{totDC}} = f(t_{p})$ 



Collector-base capacitance  $C_{CD}$ =  $f(V_{CB})$ f = 1MHz

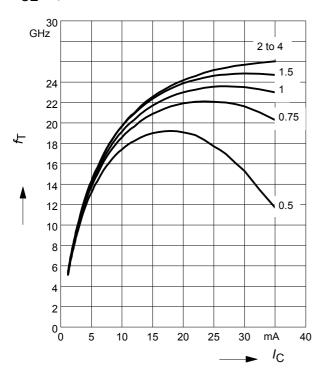




## Transition frequency $f_T = f(I_C)$

f = 2 GHz

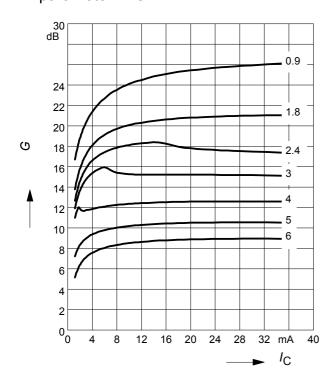
 $V_{CE}$  = parameter in V



Power gain  $G_{ma}$ ,  $G_{ms} = f(I_C)$ 

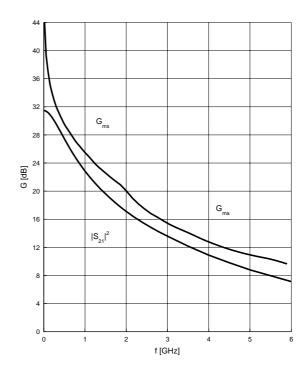
 $V_{CE} = 2V$ 

f = parameter in GHz



**Power gain**  $G_{ma}$ ,  $G_{ms}$ ,  $|S_{21}|^2 = f(f)$ 

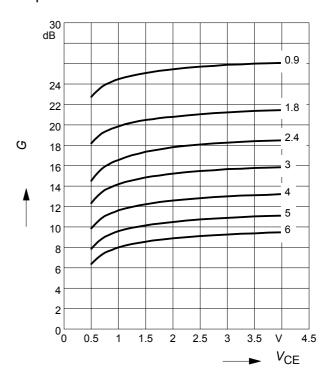
 $V_{\rm CE}$  = 2 V,  $I_{\rm C}$  = 20 mA



Power gain  $G_{ma}$ ,  $G_{ms} = f(V_{CE})$ 

 $I_{\rm C}$  = 20 mA

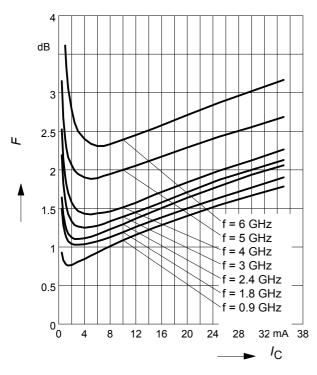
f = parameter in GHz





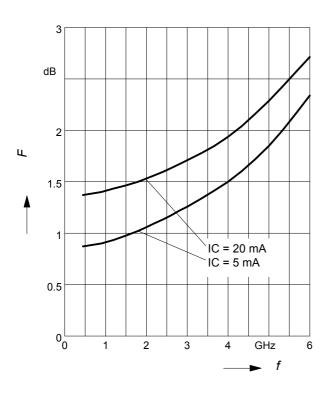
Noise figure  $F = f(I_C)$ 

$$V_{CE}$$
 = 2 V,  $Z_{S}$  =  $Z_{Sopt}$ 



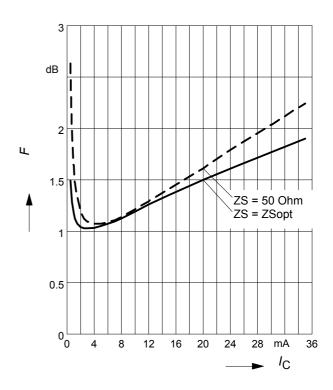
Noise figure F = f(f)

$$V_{CE}$$
 = 2 V,  $Z_{S}$  =  $Z_{Sopt}$ 



Noise figure  $F = f(I_C)$ 

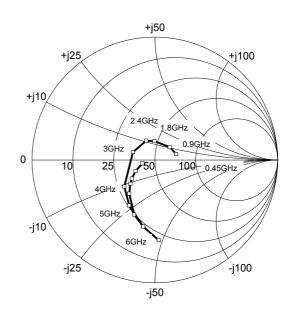
$$V_{CE} = 2 \text{ V}, f = 1.8 \text{ GHz}$$



Source impedance for min.

noise figure vs. frequency

$$V_{\rm CE}$$
 = 2 V,  $I_{\rm C}$  = 5 mA / 20 mA





#### SPICE GP Model

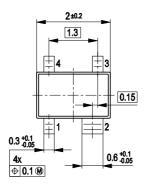
For the SPICE Gummel Poon (GP) model as well as for the S-parameters (including noise parameters) please refer to our internet website www.infineon.com/rf.models.

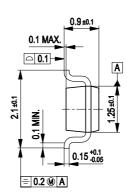
Please consult our website and download the latest versions before actually starting your design. You find the BFP420 SPICE GP model in the internet in MWO- and ADS-format, which you can import into these circuit simulation tools very quickly and conveniently. The model already contains the package parasitics and is ready to use for DC and high frequency simulations. The terminals of the model circuit correspond to the pin configuration of the device. The model parameters have been extracted and verified up to 10 GHz using typical devices. The BFP420 SPICE GP model reflects the typical DC- and RF-performance within the limitations which are given by the SPICE GP model itself. Besides the DC characteristics all S-parameters in magnitude and phase, as well as noise figure (including optimum source impedance, equivalent noise resistance and flicker noise) and intermodulation have been extracted.



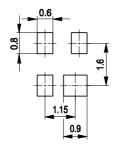
## Package Outline



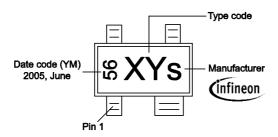




#### **Foot Print**

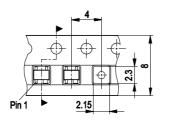


## Marking Layout (Example)



## Standard Packing

Reel ø180 mm = 3.000 Pieces/Reel Reel ø330 mm = 10.000 Pieces/Reel







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