Pb Free Product

NCE N-Channel Super Trench Power MOSFET

Description

The NCEP1505S uses **Super Trench** technology that is uniquely optimized to provide the most efficient high frequency switching performance. Both conduction and switching power losses are minimized due to an extremely low combination of $R_{DS(ON)}$ and Q_g . This device is ideal for high-frequency switching and synchronous rectification.

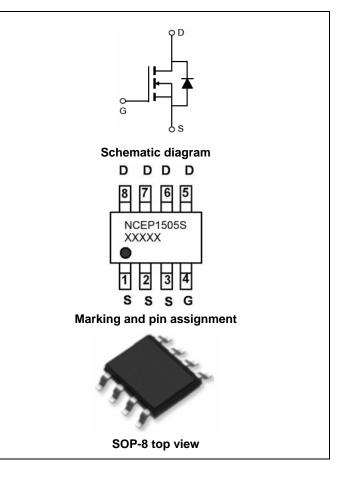
General Features

- V_{DS} =150V, I_D =5.1A $R_{DS(ON)} < 65m\Omega$ @ V_{GS} =10V (Typ: 55m Ω)
- Excellent gate charge x R_{DS(on)} product (FOM)
- Very low on-resistance R_{DS(on)}
- 150 °C operating temperature

Application

- DC/DC converters and Off-Line UPS
- High Voltage Synchronous Rectifier
- Hard switched and high frequency circuits
- Uninterruptible power supply

100% ΔVds TESTED!



Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
NCEP1505S	NCEP1505S	SOP-8	Ø330mm	12mm	4000 units

Absolute Maximum Ratings (T_A=25 ℃ unless otherwise noted)

Para	ameter	Symbol	Limit	Unit
Drain-Source Voltage		V _{DS}	150	V
Gate-Source Voltage		V _{GS}	±20	V
Drain Current-Continuous		I _D	5.1	А
Drain Current-Continuous(T _C =10	00℃)	I _D (100℃)	3.6	Α
Pulsed Drain Current(Note 1)		I _{DM}	20	А
Single pulse avalanche energy	Note 5)	E _{AS}	60	mJ
Maximum Dawar Dissination	T _C = 25 °C	Ъ	5	W
Maximum Power Dissipation	T _A = 25 °C	P _D	3	W
Operating Junction and Storage	Temperature Range	T_{J}, T_{STG}	-55 To 150	°C

Thermal Characteristic

Thermal Resistance, Junction-to-Ambient ^(Note 2)	$R_{\theta JA}$	41.7	°C/W
Thermal Resistance, Junction-to-Case ^(Note 2)	R _{eJC}	25	C/VV

NCEP1505S

Electrical Characteristics (T_A=25 ℃ unless otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Max	Unit
Off Characteristics			•			
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V I _D =250μA	150	-	-	V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =150V,V _{GS} =0V	-	-	1	μA
Gate-Body Leakage Current	I _{GSS}	V _{GS} =±20V,V _{DS} =0V	-	-	±100	nA
On Characteristics (Note 3)	<u> </u>			•		
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}$, $I_D=250\mu A$	2.5	3.3	4.5	V
Drain-Source On-State Resistance	R _{DS(ON)}	V _{GS} =10V, I _D =5.1A	-	55	65	mΩ
Forward Transconductance	g FS	V _{DS} =5V,I _D =5.1A	-	12.5	-	S
Dynamic Characteristics (Note4)	<u> </u>			•		
Input Capacitance	C _{lss}	\/ 75\/\\ 0\/	-	550	730	PF
Output Capacitance	C _{oss}	V _{DS} =75V,V _{GS} =0V, F=1.0MHz	-	62	80	PF
Reverse Transfer Capacitance	C _{rss}	Γ=1.UIVIΠZ	-	2.5	4.5	PF
Switching Characteristics (Note 4)						
Turn-on Delay Time	t _{d(on)}		-	7.5	14	nS
Turn-on Rise Time	t _r	V_{DD} =75V, I_{D} =5.1A	-	1.4	8.5	nS
Turn-Off Delay Time	t _{d(off)}	V_{GS} =10 V , R_{G} =3 Ω	-	12.5	21	nS
Turn-Off Fall Time	t _f		-	2.5	8	nS
Total Gate Charge	Qg	\/ -75\/ -5.44	-	8.5	12	nC
Gate-Source Charge	Q_{gs}	$V_{DS}=75V,I_{D}=5.1A,$	-	2.8		nC
Gate-Drain Charge	Q_{gd}	V _{GS} =10V	-	1.9		nC
Drain-Source Diode Characteristics	<u> </u>			•		
Diode Forward Voltage (Note 3)	V_{SD}	V _{GS} =0V,I _S =5.1A	-	-	1.2	V
Diode Forward Current (Note 2)	Is		-	-	5.1	Α
Reverse Recovery Time	t _{rr}	$T_J = 25$ °C, $I_F = I_S$	-	58	95	nS
Reverse Recovery Charge	Qrr	$di/dt = 100A/\mu s^{(Note3)}$	-	69	110	nC

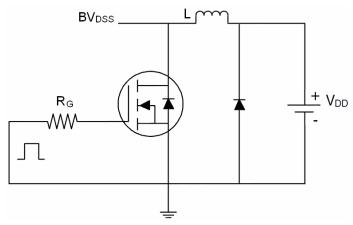
Notes:

- 1. Repetitive Rating: Pulse width limited by maximum junction temperature.
- 2. The value of R_{BJA} is measured with the device mounted on 1in2 FR-4 board with 2oz. Copper, in a still air environment with T_A =25°C. The value in any given application depends on the user's specific board design.
- 3. Pulse Test: Pulse Width \leq 300 μ s, Duty Cycle \leq 2%.
- 4. Guaranteed by design, not subject to production
- 5. EAS condition : Tj=25 $^{\circ}\text{C}$,V $_{DD}$ =50 V ,V $_{G}$ =10 V ,L=0.5 mH ,Rg=25 Ω

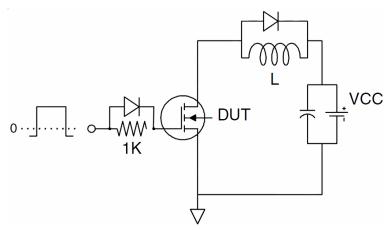


Test Circuit

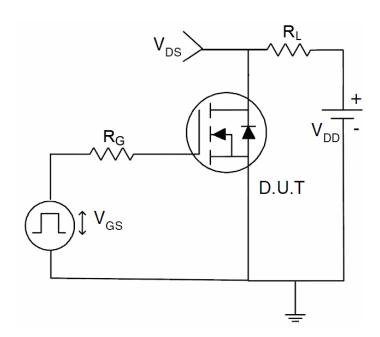
1) E_{AS} test Circuit



2) Gate charge test Circuit



3) Switch Time Test Circuit





Typical Electrical and Thermal Characteristics (Curves)

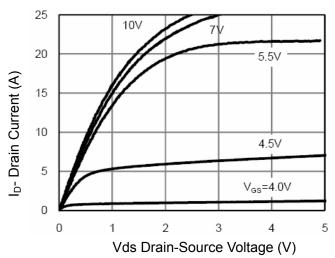


Figure 1 Output Characteristics

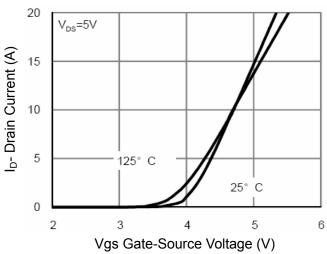


Figure 2 Transfer Characteristics

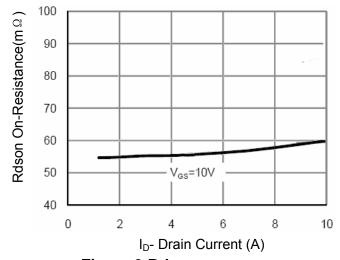


Figure 3 Rdson- Drain Current

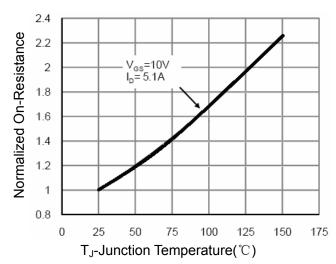


Figure 4 Rdson-JunctionTemperature

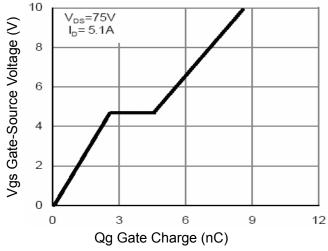


Figure 5 Gate Charge

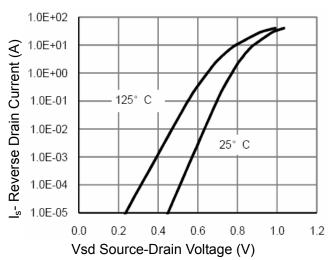


Figure 6 Source- Drain Diode Forward



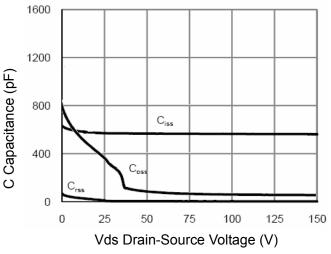


Figure 7 Capacitance vs Vds

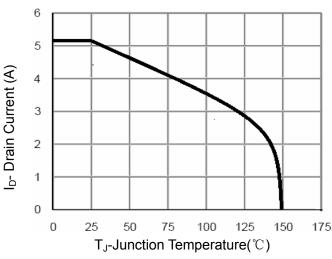


Figure 9 Current De-rating

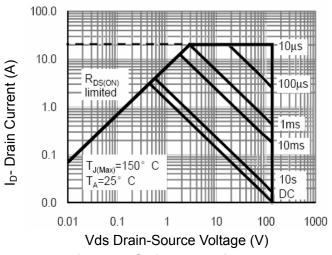


Figure 8 Safe Operation Area

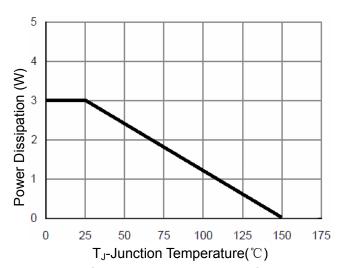


Figure 10 Power De-rating

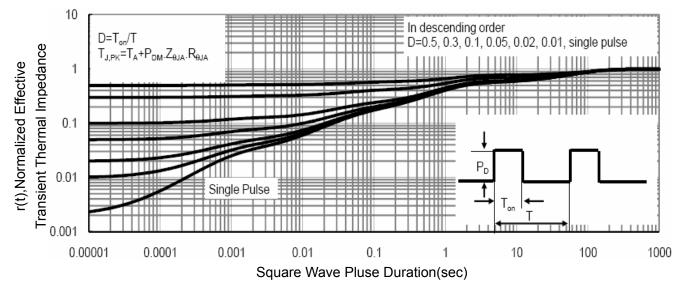
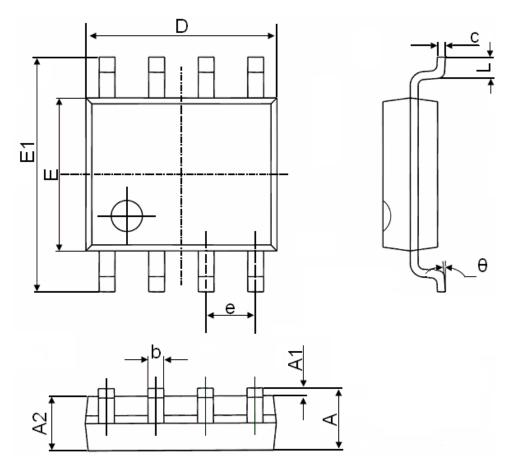


Figure 11 Normalized Maximum Transient Thermal Impedance



SOP-8 Package Information



Cumbal	Dimensions In Millimeters		Dimensions In Inches		
Symbol	Min.	Max.	Min.	Max.	
Α	1.350	1.750	0.053	0.069	
A1	0.100	0.250	0.004	0.010	
A2	1.350	1.550	0.053	0.061	
b	0.330	0.510	0.013	0.020	
С	0.170	0.250	0.006	0.010	
D	4.700	5.100	0.185	0.200	
Е	3.800	4.000	0.150	0.157	
E1	5.800	6.200	0.228	0.244	
е	1.270(BSC)		0.050(I	BSC)	
L	0.400	1.270	0.016	0.050	
θ	0°	8°	0°	8°	



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NCEP1505S

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