NCE6005AN

NCE N-Channel Enhancement Mode Power MOSFET

Description

The NCE6005AN uses advanced trench technology and design to provide excellent $R_{DS(ON)}$ with low gate charge. It can be used in a wide variety of applications.

General Features

● V_{DS}=60V,I_D=5A

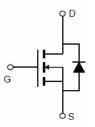
 $R_{DS(ON)}$ <35m Ω @ V_{GS} =10V (Typ.26m Ω)

 $R_{DS(ON)}$ <45m Ω @ V_{GS} =4.5V (Typ.32m Ω)

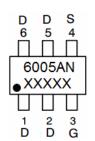
- High density cell design for ultra low Rdson
- Fully characterized avalanche voltage and current
- Good stability and uniformity with high E_{AS}
- Excellent package for good heat dissipation
- Special process technology for high ESD capability

Application

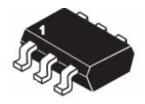
- Power switching application
- Hard switched and high frequency circuits
- Uninterruptible power supply



Schematic diagram



Marking and pin assignment



SOT23-6L top view

Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
6005AN	NCE6005AN	SOT23-6L	Ø180mm	8 mm	3000 units

Absolute Maximum Ratings (T_A=25℃unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V _{DS}	60	V
Gate-Source Voltage	V _G s	±20	V
Drain Current-Continuous	I _D	5	Α
Drain Current-Continuous(T _C =100°C)	I _D (100℃)	3.5	А
Pulsed Drain Current	I _{DM}	24	А
Maximum Power Dissipation	P _D	2	W
Operating Junction and Storage Temperature Range	T_{J}, T_{STG}	-55 To 150	°C

Thermal Characteristic

Thermal Resistance, Junction-to-Ambient (Note 2) R _{0JA} 62.5 °C/W

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Electrical Characteristics (T_A=25 $^{\circ}$ C unless otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Max	Unit
Off Characteristics			•	•		
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V I _D =250μA	60	-	-	V
Zero Gate Voltage Drain Current	te Voltage Drain Current I _{DSS} V _{DS} =60V,V _{GS} =0V		-	-	1	μΑ
Gate-Body Leakage Current	I _{GSS}	V _{GS} =±20V,V _{DS} =0V		-	±100	nA
On Characteristics (Note 3)	<u>, </u>		•	•		
Gate Threshold Voltage	V _{GS(th)}	$V_{DS}=V_{GS}$, $I_{D}=250\mu A$	1.2	1.6	2.5	V
Drain Course On Ctate Desistance	R _{DS(ON)}	V _{GS} =10V, I _D =5A	-	26	35	mΩ
Drain-Source On-State Resistance	R _{DS(ON)}	V _{GS} =4.5V, I _D =5A	-	32	45	mΩ
Forward Transconductance			11	-	-	S
Dynamic Characteristics (Note4)	<u>, </u>		•	•		
Input Capacitance	C _{lss}	\/ 00\/\/ 0\/	-	979	-	PF
Output Capacitance	C _{oss}	V_{DS} =30V, V_{GS} =0V, F=1.0MHz	-	120	-	PF
Reverse Transfer Capacitance	C _{rss}	F=1.UIVIHZ	-	100	-	PF
Switching Characteristics (Note 4)		•	I.			
Turn-on Delay Time	t _{d(on)}		-	10	-	nS
Turn-on Rise Time	t _r	V_{DD} =30V, R_L =6.7 Ω	-	3	-	nS
Turn-Off Delay Time	t _{d(off)}	V_{GS} =10 V , R_{G} =3 Ω	-	21	-	nS
Turn-Off Fall Time	t _f		-	5	-	nS
Total Gate Charge	Qg)/ 00)// 5A	-	22		nC
Gate-Source Charge	Q _{gs}	V_{DS} =30 V , I_{D} =5 A ,	-	3.3		nC
Gate-Drain Charge	Q _{gd}	V _{GS} =10V	-	5.2		nC
Drain-Source Diode Characteristics						
Diode Forward Voltage (Note 3)	V _{SD}	V _{GS} =0V,I _S =5A	-		1.2	V
Diode Forward Current (Note 2)	Is		-	-	5	Α

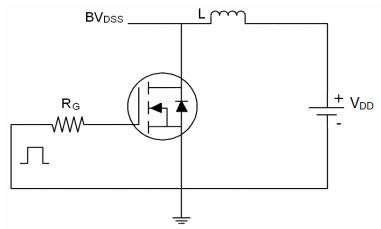
Notes:

- 1. Repetitive Rating: Pulse width limited by maximum junction temperature.
- 2. Surface Mounted on FR4 Board, t ≤ 10 sec.
- 3. Pulse Test: Pulse Width \leq 300µs, Duty Cycle \leq 2%.
- **4.** Guaranteed by design, not subject to production
- 5. EAS condition:Tj=25 $^{\circ}\text{C}$,VDD=30V,VG=10V,L=0.5mH,Rg=25 Ω

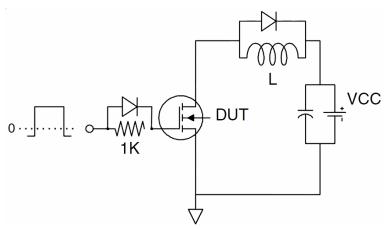
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Test Circuit

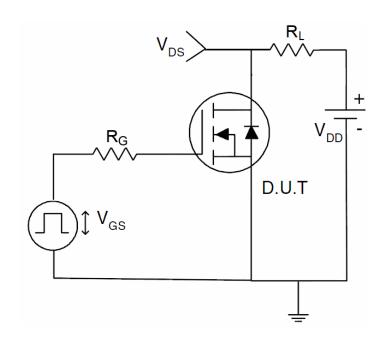
1) E_{AS} test Circuit



2) Gate charge test Circuit



3) Switch Time Test Circuit









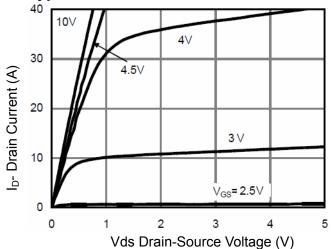


Figure 1 Output Characteristics

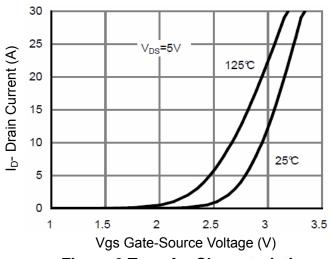


Figure 2 Transfer Characteristics

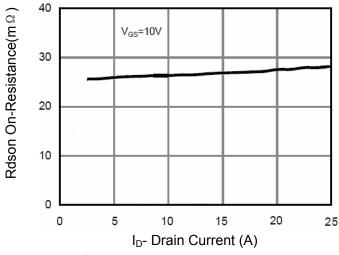


Figure 3 Rdson- Drain Current

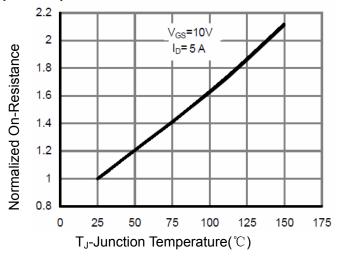


Figure 4 Rdson-Junction Temperature

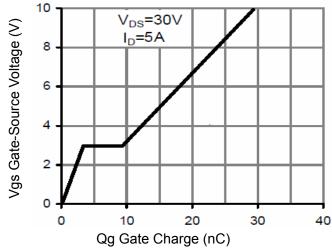


Figure 5 Gate Charge

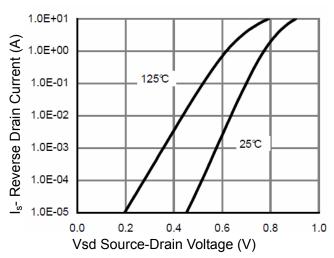


Figure 6 Source- Drain Diode Forward



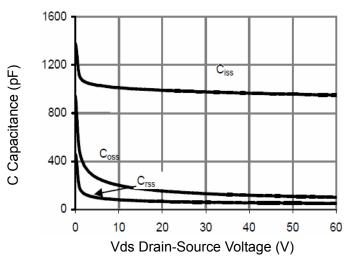


Figure 7 Capacitance vs Vds

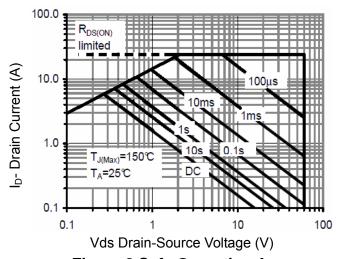


Figure 8 Safe Operation Area

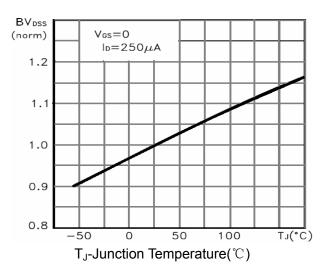


Figure 9 BV_{DSS} vs Junction Temperature

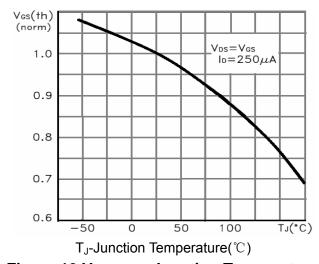


Figure 10 V_{GS(th)} vs Junction Temperature

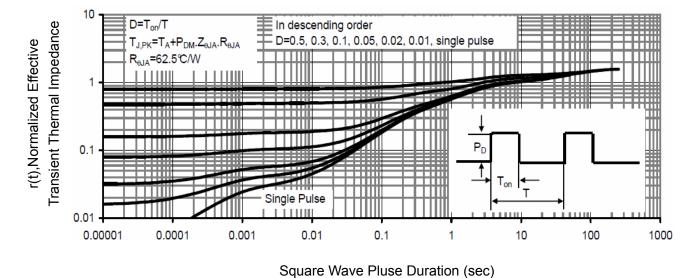
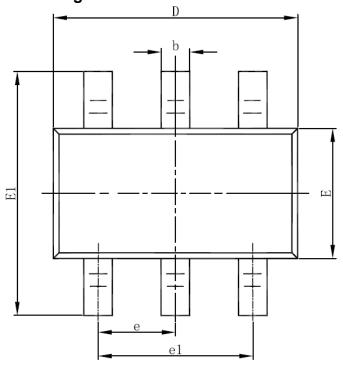


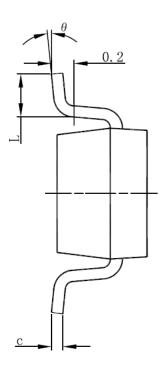
Figure 11 Normalized Maximum Transient Thermal Impedance

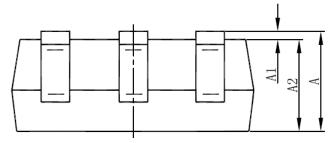
Pb Free Product



SOT23-6L Package Information







Ch a l	Dimensions In Millimeters		Dimensions In Inches		
Symbol	Min	Max	Min	Max	
Α	1.050	1.250	0.041	0.049	
A1	0.000	0.100	0.000	0.004	
A2	1.050	1.150	0.041	0.045	
b	0.300	0.500	0.012	0.020	
С	0.100	0.200	0.004	0.008	
D	2.820	3.020	0.111	0.119	
E	1.500	1.700	0.059	0.067	
E1	2.650	2.950	0.104	0.116	
е	0.950(BSC)		0.037(BSC)		
e1	1.800	2.000	0.071	0.079	
L	0.300	0.600	0.012	0.024	
θ	0°	8°	0°	8°	



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