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## NCE30TD120UT

#### 1200V, 30A, Trench FS II Fast IGBT

#### **General Description:**

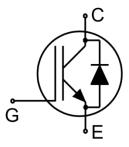
Using NCE's proprietary trench design and advanced FS (Field Stop) second generation technology, the 1200V Trench FSII IGBT offers superior conduction and switching performances, and easy parallel operation;

#### **Features**

- Trench FSII Technology Offering
- Very low V<sub>CE(sat)</sub>
- High speed switching
- Positive temperature coefficient in V<sub>CE(sat)</sub>
- Very tight parameter distribution
- High ruggedness, temperature stable behavior

#### **Application**

• UPS



Schematic diagram

#### **Package Marking and Ordering Information**

	<u> </u>	
Device	Device Package	Device Marking
NCE30TD120UT	TO-247	NCE30TD120UT



TO-247

#### **Absolute Maximum Ratings (Tc=25°C unless otherwise noted)**

Symbol	Parameter	Value	Units
V <sub>CES</sub>	Collector-Emitter Voltage	1200	V
$V_{GES}$	Gate- Emitter Voltage	±30	V
I.	Collector Current	60	Α
lc	Collector Current @T <sub>C</sub> = 100 °C	30	Α
I <sub>Cpuls</sub>	Pulsed Collector Current, t <sub>p</sub> limited by T <sub>jmax</sub>	90	Α
-	turn off safe operating area, V <sub>CE</sub> =1200V, Tj=150°C	90	Α
l <sub>F</sub>	Diode Continuous Forward Current @T <sub>C</sub> = 100 °C	30	Α
I <sub>FM</sub>	Diode Maximum Forward Current	90	Α
0	Power Dissipation @ T <sub>C</sub> = 25°C	468	W
P <sub>D</sub>	Power Dissipation @T <sub>C</sub> = 100 °C	234	W
T <sub>J</sub> ,T <sub>stg</sub>	Operating Junction and Storage Temperature Range	-55 to +175	°C
TL	Maximum Temperature for Soldering	260	°C
t <sub>sc</sub>	Short circuit withstand time $V_{GE}$ =15.0V, $V_{CC} \le 600$ V, Allowed number of short circuits<1000Time between short circuits: $\ge$ 1.0s, $T_j \le$ 150°C	5	us

V1.0

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#### **Thermal Characteristic**

Symbol	Parameter	Value	Units
Rejc	Thermal Resistance, Junction to case for IGBT	0.32	°C/W
Rejc	Thermal Resistance, Junction to case for Diode	0.61	°C/W
RθJA	Thermal Resistance, Junction to Ambient	40	°C/W

## Electrical Characteristics (Tc=25°C unless otherwise noted)

0	Danamatan.	Test Conditions		Value			
Symbol	Parameter			Min.	Тур.	Max.	Units
Static Chara	cteristics						
V <sub>(BR)CES</sub>	Collector-Emitter Breakdown Voltage	V <sub>GE</sub> =0V	,I <sub>CE</sub> =1mA	1200			V
Ices	Collector-Emitter Leakage Current	V <sub>GE</sub> =0V,	V <sub>GE</sub> =0V,V <sub>CE</sub> =1200V			5	uA
I <sub>GES(F)</sub>	Gate to Emitter Forward Leakage	V <sub>GE</sub> =+30	V,Vce=0V			200	nA
I <sub>GES(R)</sub>	Gate to Source Reverse Leakage	V <sub>GE</sub> =-30	V,Vce =0V			200	nA
	O-Ht Fusition Ostumbian Vallana	Ic=30A	Tj=25°C		1.95	2.3	V
$V_{CE(sat)}$	Collector-Emitter Saturation Voltage	V <sub>GE</sub> =15V	Tj=150°C		2.3		V
$V_{GE(th)}$	Gate Threshold Voltage	Ic=1mA	,Vce=Vge	5.0		6.5	V
Dynamic Ch	aracteristics						
Cies	Input Capacitance	VcE=30V,VGE=0V,			6190		pF
Coes	Output Capacitance				185		
Cres	Reverse Transfer Capacitance	_	f=1MHz		133		
Qg	Total Gate Charge		Vcc=960V, Ic=30A, V <sub>GE</sub> =15V		242		nC
Qge	Gate to Emitter Charge				51		
Q <sub>gc</sub>	Gate to Collector Charge	_ VGE	-10 V		115		
Switching Cl	haracteristics						
t <sub>d(ON)</sub>	Turn-on Delay Time				19		
tr	Rise Time				17		
t <sub>d(OFF)</sub>	Turn-Off Delay Time	V <sub>CE</sub> =600	V <sub>CE</sub> =600V,I <sub>C</sub> =30A,		170		ns
t <sub>f</sub>	Fall Time	$V_{GE}$ =0/15V, $R_g$ =8 $\Omega$			18		
Eon	Turn-On Switching Loss	Inducti	Inductive Load		1.7		
E <sub>off</sub>	Turn-Off Switching Loss				1.0		mJ
Ets	Total Switching Loss	1			2.7		

## Electrical Characteristics of the Diode(T<sub>C</sub>= 25°C unless otherwise specified):

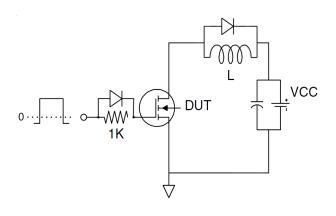
Symbol	Parameter	Test Conditions	Rating			Units
			Min.	Тур.	Max.	Ullits
V <sub>FM</sub>	Diode Forward Voltage	I <sub>F</sub> =30A		2.2	2.8	V
Trr	Reverse Recovery Time	I- 20A		190		ns
I <sub>RRM</sub>	Diode Peak Reverse Recovery Current	I <sub>F</sub> =30A, di/dt=500A/us		12		Α
$Q_{rr}$	Reverse Recovery Charge	ui/ut=500A/us		2.5		uC
Pulse width t <sub>tp</sub> ≤380μs,δ≤2%						



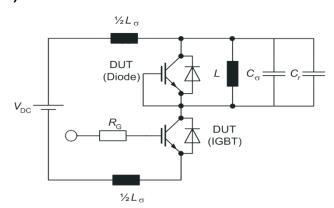
# NCE30TD120UT

#### **Test Circuit**

#### 1) Gate Charge Test Circuit

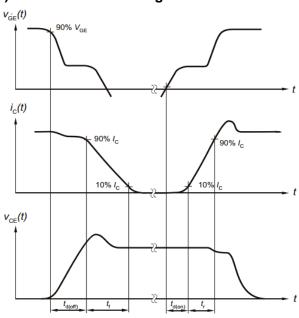


#### 2) Switch Time Test Circuit

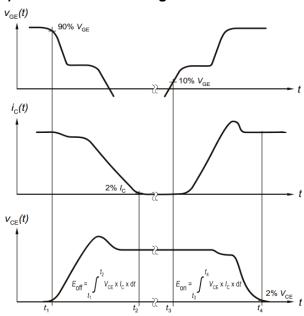


### **Switching characteristics**

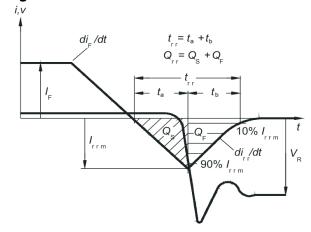
#### 1) Definition of switching times



#### 2) Definition of switching losses



#### 3) Definition of diode switching characteristics



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#### **Typical Electrical and Thermal Characteristics**

#### **Figure 1 Output Characteristics**

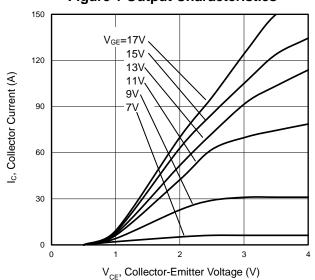
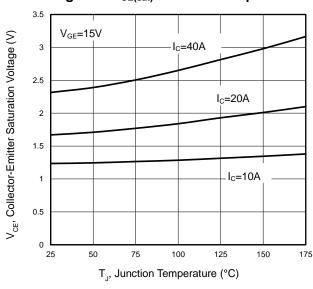


Figure 3 V<sub>CE(sat)</sub> vs. Case Temperature



**Figure 5 Capacitance Characteristics** 

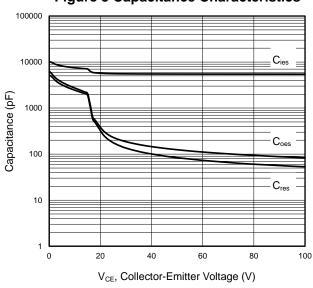


Figure 2 Transfer Characteristics

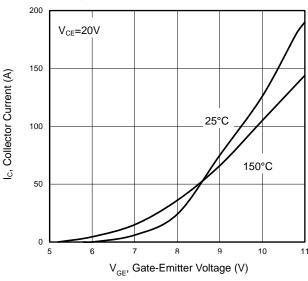


Figure 4 Saturation Voltage vs. V<sub>GE</sub>

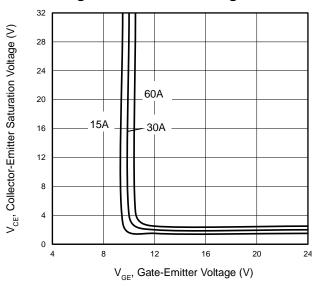
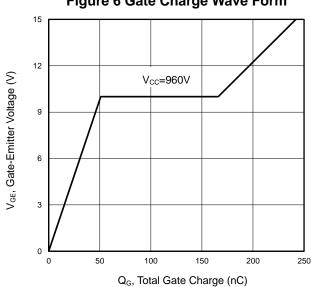


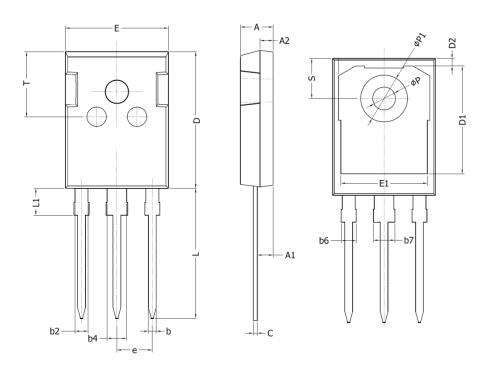
Figure 6 Gate Charge Wave Form



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# **TO-247 Package Information**



Comb al	Dimensions I	n Millimeters	Dimensions In Inches		
Symbol	Min.	Max.	Min.	Max.	
Α	4.90	5.10	0.193	0.201	
A1	2.31	2.51	0.091	0.099	
A2	1.9	2.1	0.075	0.083	
b	1.16	1.26	0.046	0.050	
b2	1.96	2.06	0.077	0.081	
b4	2.96	3.06	0.117	0.120	
b6	-	2.25	-	0.089	
b7	-	3.25	-	0.128	
С	0.59	0.66	0.023	0.026	
D	20.90	21.10	0.823	0.831	
D1	16.25	16.85	0.640	0.663	
D2	1.05	1.35	0.041	0.053	
E	15.70	15.90	0.618	0.626	
E1	13.10	13.50	0.516	0.531	
е	5.436	BSC	0.214 BS	C	
L	19.80	20.10	0.780	0.791	
L1	-	4.30	-	0.169	
Р	3.40	3.60	0.134	0.142	
P1	7.00	7.40	0.276	0.291	
S	6.05	6.25	0.238	0.246	
Т	9.80	10.20	0.386	0.402	

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