

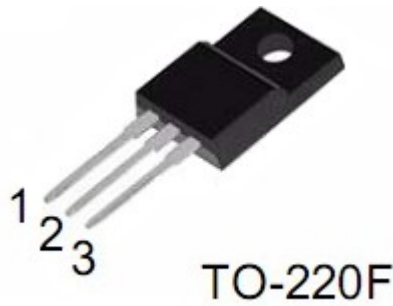
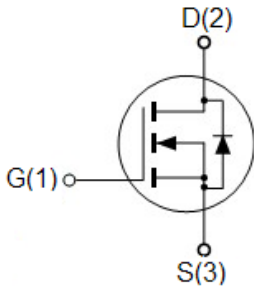


N-CHANNEL POWER MOSFET MEM7N60

General Description

- Switching regulator application.
- High voltage and high speed.
- Switching application.

Pin Configuration



MEM7N60A3G

Features

- 600V, 7A
- $R_{DS(ON)}=1.2\Omega@V_{GS}=10V$
- LOW CRSS
- FAST SWITCHING
- PACKAGE : TO220-F

Maximum Ratings($T_A=25^\circ\text{C}$)

Parameter	Symbol	Ratings	Unit
Drain-Source Voltage	V_{DSS}	600V	V
Gate-Source Voltage	V_{GSS}	± 30	V
Drain Current	I_D	$T_A=25^\circ\text{C}$	7
		$T_A=100^\circ\text{C}$	3.6
Pulsed Drain Current ^{1,2}	I_{DM}	24	A
Total Power Dissipation	P_d	50	W
Operating Temperature Range	T_{Opr}	-55-150	$^\circ\text{C}$
Storage Temperature Range	T_{stg}	-55-150	$^\circ\text{C}$

Thermal Characteristics

Parameter	Symbol	TYP.	MAX.	Unit
Thermal Resistance, Junction-to-Case	$R_{\theta JC}$	2.5	3	$^\circ\text{C/W}$

Electrical Characteristics

Parameter	Symbol	Test Condition	Min	Type	Max	Unit
Static Characteristics						
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS}=0V, I_D=250\mu A$	600	650	-	V
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}, I_D=250\mu A$	2.1	2.8	4.4	V
Gate-Body Leakage	I_{GSS}	$V_{DS}=0V, V_{GS}=30V$	-	0.8	100	nA
		$V_{DS}=0V, V_{GS}=-30V$	-	-4	-100	nA
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS}=600V, V_{GS}=0V$	-	0.2	20	μA
Static Drain-Source On-Resistance	$R_{DS(ON)}$	$V_{GS}=10V, I_D=3A$	-	0.82	1.2	Ω
Forward Transconductance	g_{FS}	$V_{DS}=15V, I_D=3A$	-	2	10	S
Drain-Source Diode Forward Continuous Current	I_S	$V_{GS}=0V$	-	-	7	A
Source-drain (diode forward) voltage	V_{SD}	$V_{GS}=0V, I_S=7A$		0.80	1.4	V
Dynamic Characteristics						
Input Capacitance	C_{iss}	$V_{DS}=25V,$ $V_{GS}=0V,$ $f=1MHz$	-	1260	-	pF
Output Capacitance	C_{oss}		-	152	-	
Reverse Transfer Capacitance	C_{rss}		-	24	-	
Switching Characteristics						
Turn-On Delay Time	$t_{d(on)}$	$V_{DD}=300V,$ $R_G=10\Omega,$ $V_{GS}=10V,$ $I_D=7A$	-	27.5	-	ns
Rise Time	t_r		-	8.2	-	
Turn-Off Delay Time	$t_{d(off)}$		-	48.8	-	
Fall-Time	t_f		-	12.1	-	
Total Gate Charge	Q_g	$V_{DS}=300V,$ $V_{GS}=10V,$ $I_D=7A$		31.4	-	nC
Gate-Source Charge	Q_{gs}		-	8.2	-	
Gate-Drain Charge	Q_{gd}		-	12.2	-	

1、Repetitive rating, pulse width limited by junction temperature.

2、Pulse width <300us , duty cycle <2%.

3、 $I_{SD} \leq 6.0A$ $di/dt \leq 100A/\mu s$, $V_{DD} \leq BV_{DSS}$, $T_J \leq 150^\circ C$.

4、 $L=1.2mH$, $V_{DD}=50V$, $I_D=6.9A$, $R_G=25\Omega$, Starting $T_J=25^\circ C$.

Typical performance characteristics

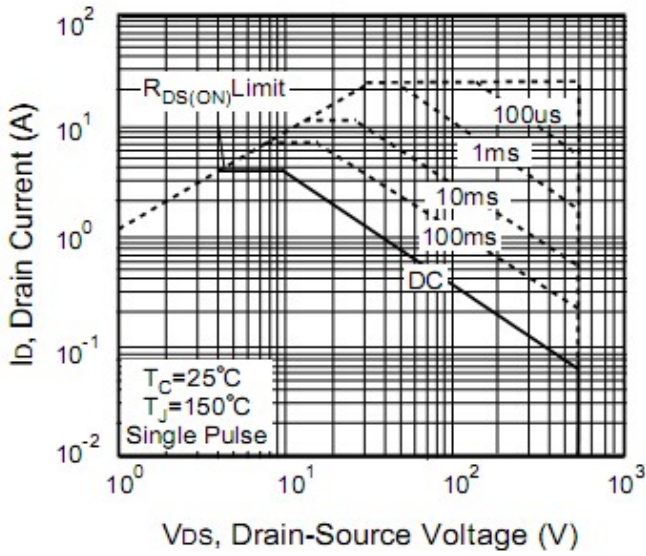


Figure 1 Maximum Safe Operating Area

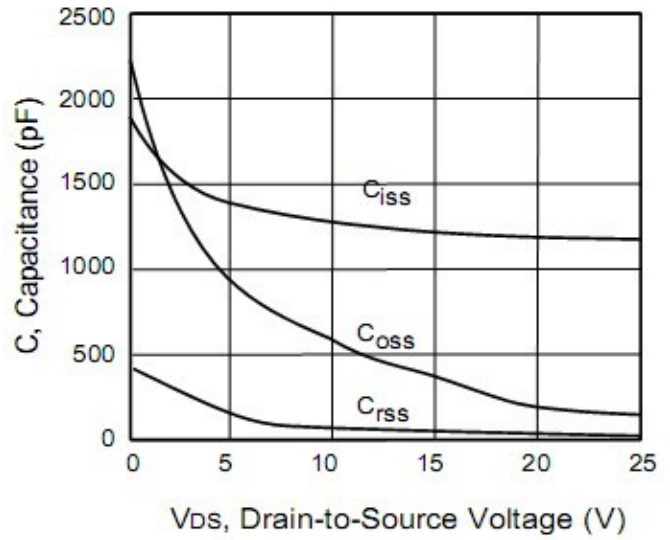


Figure 2. Capacitance Characteristics

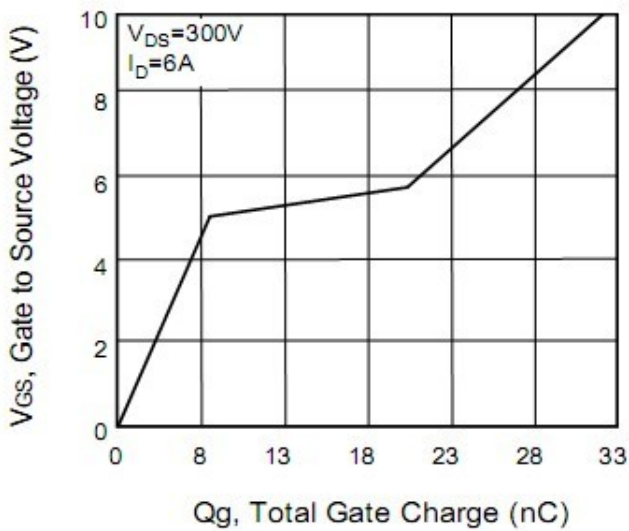


Figure 3. Gate Charge Characteristics

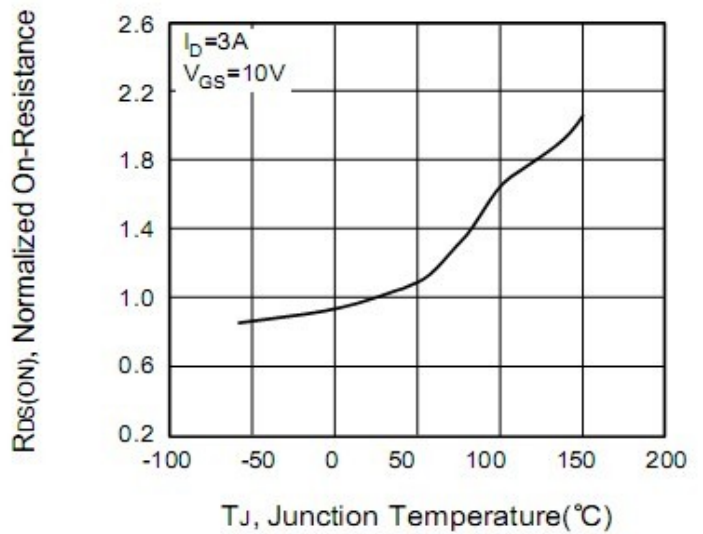
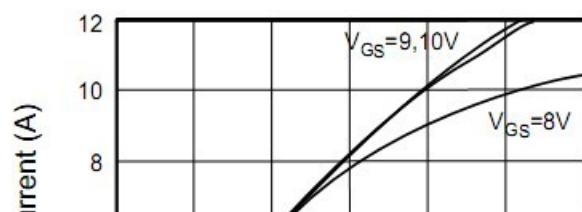


Figure 4. Normalized On-Resistance Variation with Temperature



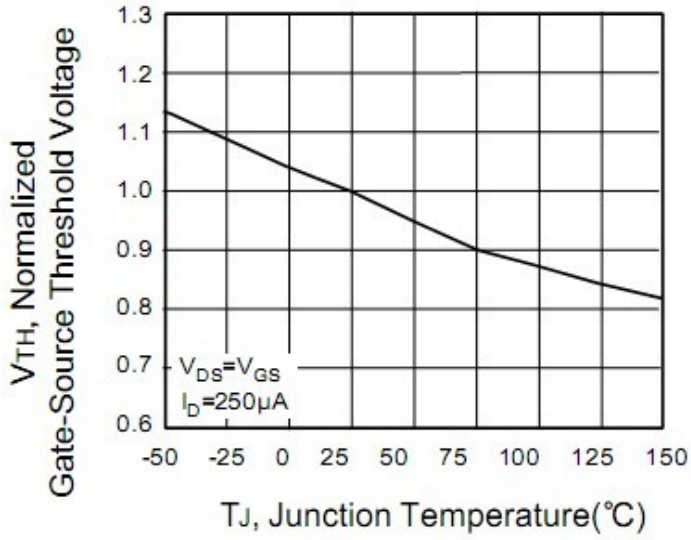


Figure 5. Gate Threshold Variation with Temperature

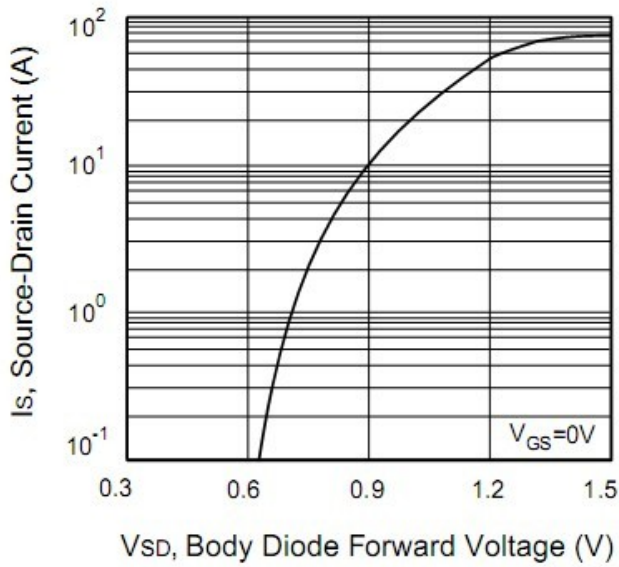


Figure 7. Body Diode Forward Voltage Variation with Source Current

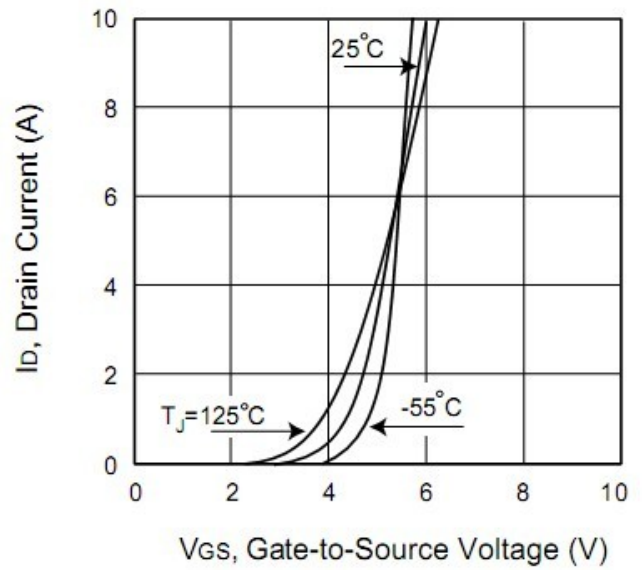


Figure 8. Transfer Characteristics

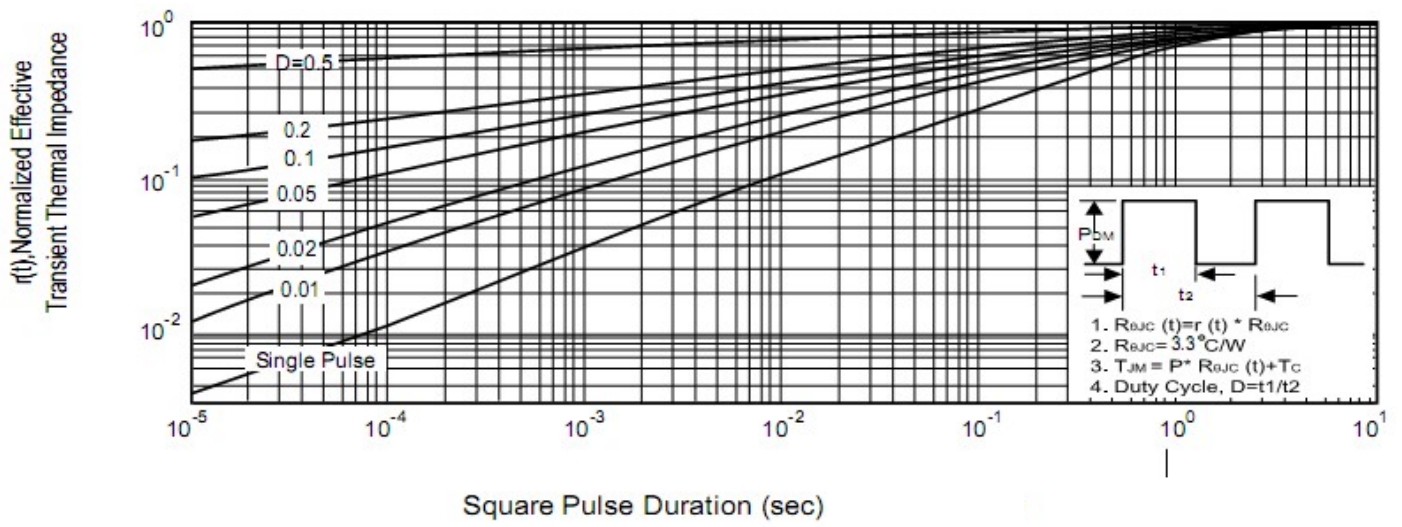
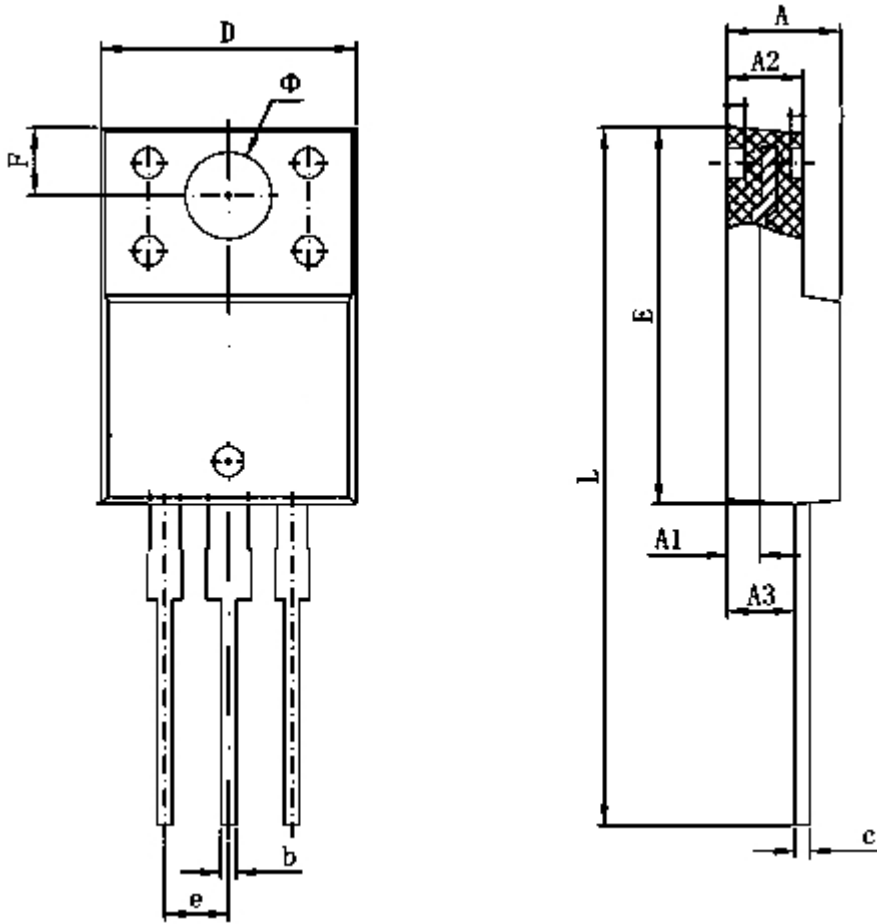


Figure 9 Normalized Effective Transient Thermal Impedance With Pulse Duration

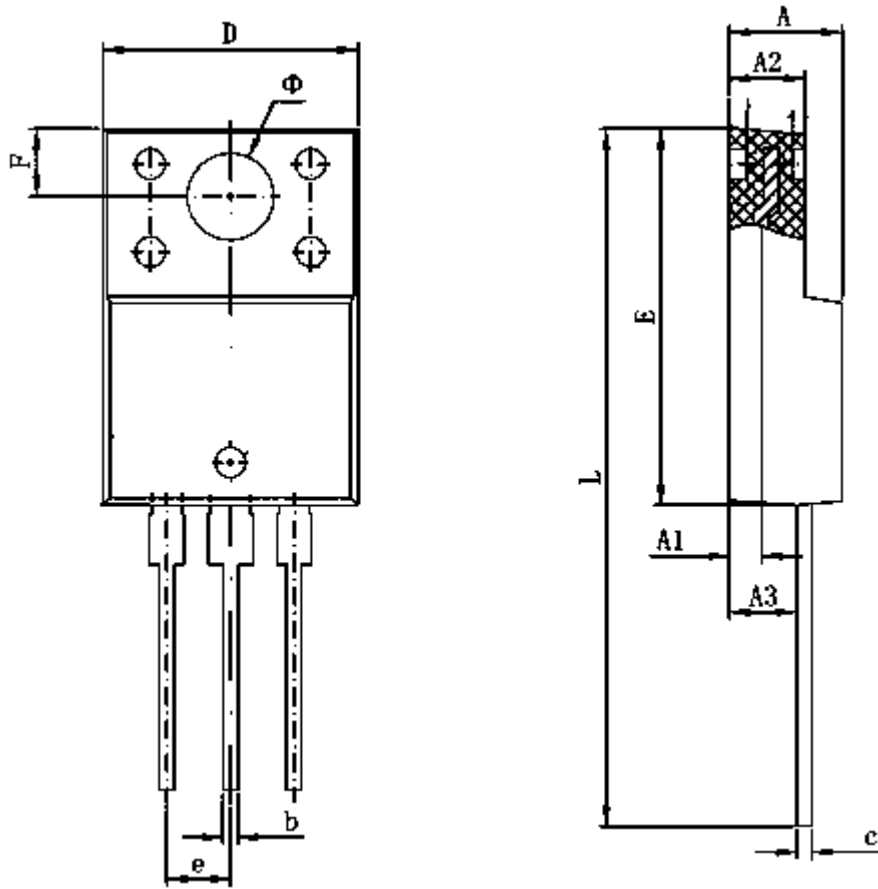
Package Information

- Package Type: TO-220F (A)



DIM	Millimeters		Inches	
	Min	Max	Min	Max
A	4.5	4.9	0.1771	0.1929
A1	0.75	1.05	0.0295	0.0413
A2	2.35	2.75	0.0925	0.1083
A3	2.65	2.85	0.1043	0.1122
b	0.75	0.85	0.0295	0.0334
c	0.45	0.6	0.0177	0.0236
D	10	10.32	0.3937	0.4063
E	15.65	16.05	0.6161	0.6319
e	2.54REF		0.100REF	
F	3.2	3.4	0.1260	0.1338
Φ	3.08	3.28	0.1212	0.1291
L	28.45	29.25	1.1201	1.1516

● Package Type: TO-220F (B)



DIM	Millimeters		Inches	
	Min	Max	Min	Max
A	4.5	4.9	0.1771	0.1929
A1	0.75	1.05	0.0295	0.0413
A2	2.35	2.75	0.0925	0.1083
A3	2.65	2.9	0.1043	0.1142
b	0.75	0.85	0.0295	0.0334
c	0.45	0.6	0.0177	0.0236
D	10	10.32	0.3937	0.4063
E	15.65	16.15	0.6161	0.6358
e	2.54REF		0.100REF	
F	3.2	3.4	0.1260	0.1338
Φ	3.08	3.28	0.1212	0.1291
L	26.2	29.8	1.0315	1.1732

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