

NCE N-Channel Enhancement Mode Power MOSFET

Description

The NCE30H10K uses advanced trench technology and design to provide excellent $R_{\text{DS}(\text{ON})}$ with low gate charge. It can be used in a wide variety of applications.

General Features

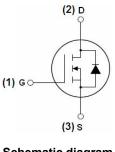
- V_{DS} =30V.I_D =100A
 - $R_{DS(ON)}$ <5.5m Ω @ V_{GS} =10V (Typ:4m Ω)
 - $R_{DS(ON)}$ <6.0m Ω @ V_{GS} =4.5V (Typ:4.2m Ω)
- High density cell design for ultra low Rdson
- Fully characterized avalanche voltage and current
- Good stability and uniformity with high E_{AS}
- Excellent package for good heat dissipation
- Special process technology for high ESD capability

Application

- Power switching application
- Hard switched and high frequency circuits
- Uninterruptible power supply

100% UIS TESTED!

100% AVds TESTED!



Schematic diagram



Marking and pin assignment



TO-252-2L top view

Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
NCE30H10K	NCE30H10K	TO-252-2L	-	-	<u>-</u>

Absolute Maximum Ratings (T_A=25°Cunless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V _{DS}	30	V
Gate-Source Voltage	Vgs	±20	V
Drain Current-Continuous	I _D	100	А
Drain Current-Continuous(Tc=100℃)	I _D (100°ℂ)	70	А
Pulsed Drain Current	I _{DM}	400	А
Maximum Power Dissipation	P _D	110	W
Single pulse avalanche energy (Note 5)	E _{AS}	350	mJ
Operating Junction and Storage Temperature Range	T _J ,T _{STG}	-55 To 175	°C

Thermal Characteristic

Thermal Resistance, Junction-to-Case ^(Note 2)	R _{0JC}	1.36	°C/W
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Electrical Characteristics (T_A=25°C unless otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Max	Unit	
Off Characteristics							
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V I _D =250μA 30		-	-	V	
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =30V,V _{GS} =0V	-	-	1	μA	
Gate-Body Leakage Current	I _{GSS}	V _{GS} =±20V,V _{DS} =0V	-	-	±100	nA	
On Characteristics (Note 3)							
Gate Threshold Voltage	V _{GS(th)}	$V_{DS}=V_{GS},I_{D}=250\mu A$	1	1.6	3	V	
Drain-Source On-State Resistance	В	V _{GS} =10V, I _D =20A	٠ -		5.5	0	
Diain-Source On-State Resistance	R _{DS(ON)}	V _{GS} =4.5V, I _D =20A		4.2	6.0	mΩ	
Forward Transconductance	g FS	V _{DS} =10V,I _D =20A	50	-	-	S	
Dynamic Characteristics (Note4)							
Input Capacitance	C _{lss}	\/ OF\/\/ O\/		3400		PF	
Output Capacitance	Coss	V_{DS} =25V, V_{GS} =0V, F=1.0MHz		356		PF	
Reverse Transfer Capacitance	Crss	r-1.0ivinz		308		PF	
Switching Characteristics (Note 4)							
Turn-on Delay Time	t _{d(on)}		-	11	-	nS	
Turn-on Rise Time	t _r	V_{DD} =15 V , I_D =60 A	-	160	-	nS	
Turn-Off Delay Time	t _{d(off)}	V_{GS} =4.5 V , R_{GEN} =1.8 Ω	-	25	-	nS	
Turn-Off Fall Time	t _f		-	60	-	nS	
Total Gate Charge	Qg	15)/1 004		70		nC	
Gate-Source Charge	Q _{gs}	V _{DS} =15V,I _D =30A,		8.8		nC	
Gate-Drain Charge	Q_{gd}	V _{GS} =10V		16.3		nC	
Drain-Source Diode Characteristics							
Diode Forward Voltage (Note 3)	V _{SD}	V _{GS} =0V,I _S =20A	-	-	1.2	V	
Diode Forward Current (Note 2)	Is	-	-	-	100	Α	
Reverse Recovery Time	t _{rr}	TJ = 25°C, IF = 60A	-	29	-	nS	
Reverse Recovery Charge	Qrr	$di/dt = 100A/\mu s^{(Note3)}$	-	32	-	nC	

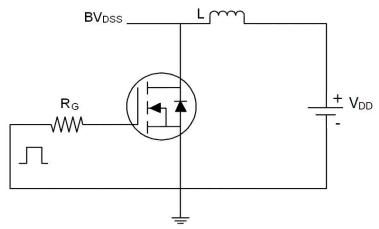
Notes:

- $\textbf{1.} \ \textbf{Repetitive Rating: Pulse width limited by maximum junction temperature.}$
- 2. Surface Mounted on FR4 Board, t ≤ 10 sec.
- **3.** Pulse Test: Pulse Width $\leq 300 \mu s$, Duty Cycle $\leq 2\%$.
- 4. Guaranteed by design, not subject to production
- **5.** EAS condition: Tj=25 $^{\circ}\text{C}$,VDD=15V,VG=10V,L=0.5mH,Rg=25 Ω

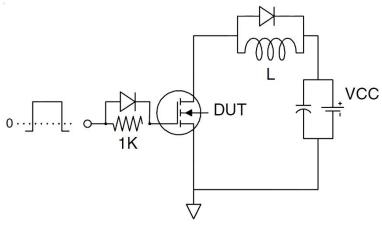


Test circuit

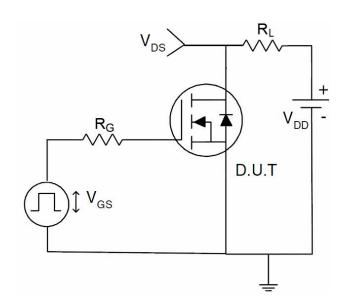
1) E_{AS} test Circuits



2) Gate charge test Circuit:



3) Switch Time Test Circuit:





Typical Electrical and Thermal Characteristics (Curves)

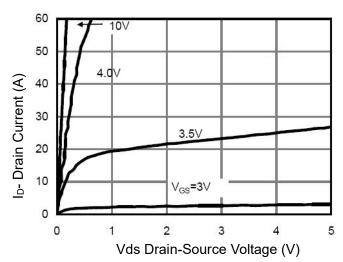


Figure 1 Output Characteristics

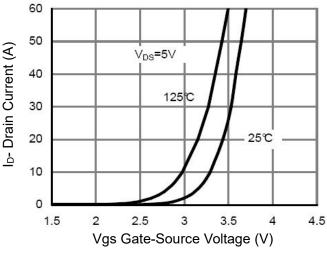


Figure 2 Transfer Characteristics

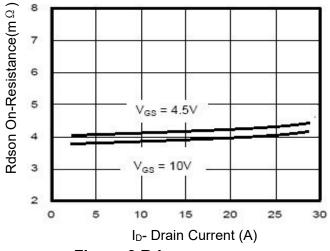


Figure 3 Rdson- Drain Current

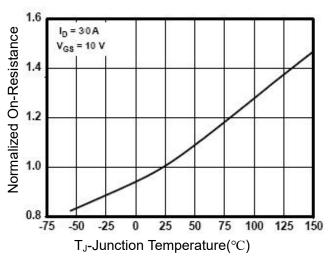


Figure 4 Rdson-JunctionTemperature

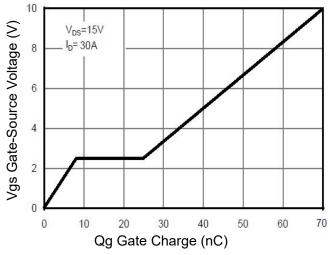


Figure 5 Gate Charge

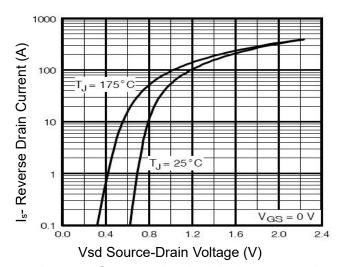


Figure 6 Source- Drain Diode Forward



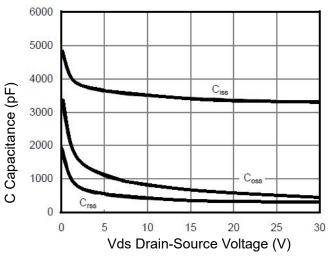


Figure 7 Capacitance vs Vds

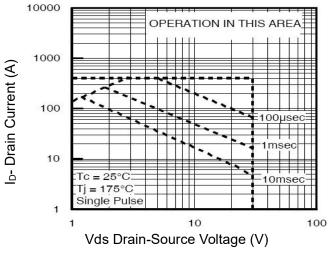


Figure 8 Safe Operation Area

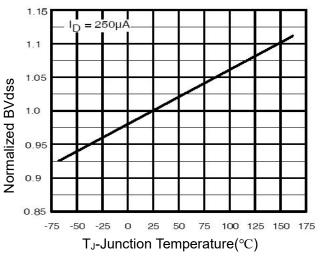


Figure 9 BV_{DSS} vs Junction Temperature

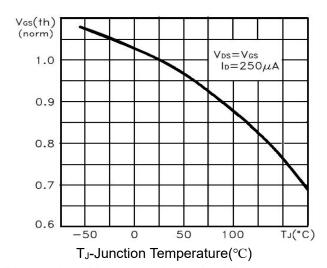
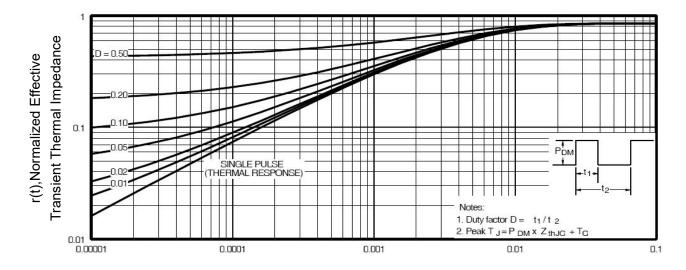


Figure 10 V_{GS(th)} vs Junction Temperature

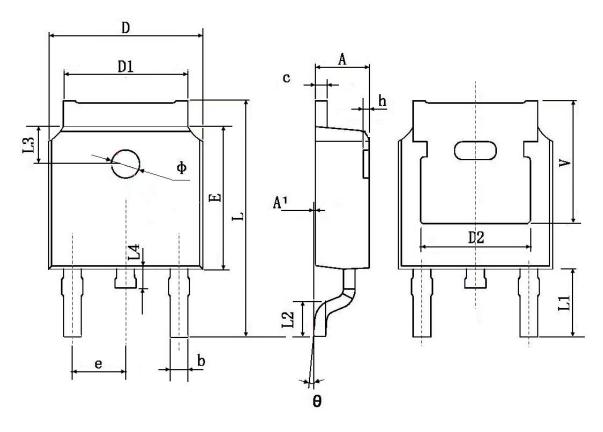


Square Wave Pluse Duration (sec)

Figure 11 Normalized Maximum Transient Thermal Impedance



TO-252 Package Information



Comphal	Dimensions	In Millimeters	Dimensions In Inches		
Symbol	Min.	Max.	Min.	Max.	
А	2.200	2.400	0.087	0.094	
A1	0.000	0.127	0.000	0.005	
b	0.660	0.860	0.026	0.034	
С	0.460	0.580	0.018	0.023	
D	6.500	6.700	0.256	0.264	
D1	5.100	5.460	0.201	0.215	
D2	4.83	4.830TYP.) TYP.	
E	6.000	6.200	0.236	0.244	
е	2.186	2.386	0.086	0.094	
L	9.800	10.400	0.386	0.409	
L1	2.90	2.900 TYP.		TYP.	
L2	1.400	1.700	0.055	0.067	
L3	1.60	0 TYP.	0.063	TYP.	
L4	0.600	1.000	0.024	0.039	
Ф	1.100	1.300	0.043	0.051	
θ	0°	8°	0°	8°	
h	0.000	0.300	0.000	0.012	
V	5.35	0 TYP.	0.211	I1 TYP.	

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