

NCE P-Channel Enhancement Mode Power MOSFET

Description

The NCE3401AY uses advanced trench technology to provide excellent $R_{\rm DS(ON)}$, low gate charge and operation with gate voltages as low as 2.5V. This device is suitable for use as a load switch or in PWM applications.

General Features

• $V_{DS} = -30V, I_{D} = -4.4A$

 $R_{DS(ON)}$ < 120m Ω @ V_{GS} =-2.5V

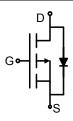
 $R_{DS(ON)}$ < 65m Ω @ V_{GS} =-4.5V

 $R_{DS(ON)}$ < 52m Ω @ V_{GS} =-10V

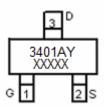
- High power and current handing capability
- Lead free product is acquired
- Surface mount package

Application

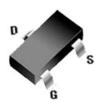
- PWM applications
- Load switch
- Power management



Schematic diagram



Marking and pin Assignment



SOT-23-3L top view

Package Marking And Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
3401AY	NCE3401AY	SOT-23-3L	Ø180mm	8 mm	3000 units

Absolute Maximum Ratings (T_A=25℃unless otherwise noted)

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Parameter	Symbol	Limit	Unit			
Drain-Source Voltage	V _{DS}	-30	V			
Gate-Source Voltage	V _G s	±12	V			
Drain Current-Continuous	I _D	-4.4	А			
Drain Current-Pulsed (Note 1)	I _{DM}	-30	Α			
Maximum Power Dissipation	P _D	1.3	W			
Operating Junction and Storage Temperature Range	T_{J}, T_{STG}	-55 To 150	°C			

Thermal Characteristic

Thermal Resistance, Junction-to-Ambient (Note 2)	$R_{\theta JA}$	95	°C/W
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Electrical Characteristics (T_A=25°C unless otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Max	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V I _D =-250μA	-30	-33	-	V



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NCE3401AY

Parameter	Symbol	Condition	Min	Тур	Max	Unit		
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =-24V,V _{GS} =0V	-	-	-1	μA		
Gate-Body Leakage Current	I _{GSS}	I _{GSS} V _{GS} =±12V,V _{DS} =0V		-	±100	nA		
On Characteristics (Note 3)	On Characteristics (Note 3)							
Gate Threshold Voltage	$V_{GS(th)}$	V _{DS} =V _{GS} ,I _D =-250μA	-0.7	-1	-1.3	V		
		V _{GS} =-10V, I _D =-4.2A	-	41	52	mΩ		
Drain-Source On-State Resistance	R _{DS(ON)}	V _{GS} =-4.5V, I _D =-4A	-	48	65	mΩ		
		V _{GS} =-2.5V, I _D =-1A		64	120	mΩ		
Forward Transconductance	g FS	V _{DS} =-5V,I _D =-2A	-	10	-	S		
Dynamic Characteristics (Note4)								
Input Capacitance	C _{iss}	\/ - 15\/\/ -0\/	-	950	-	PF		
Output Capacitance	C _{oss}	V_{DS} =-15V, V_{GS} =0V, F=1.0MHz	-	115	-	PF		
Reverse Transfer Capacitance	C _{rss}	F-1.UIVITZ	-	75	-	PF		
Switching Characteristics (Note 4)								
Turn-on Delay Time	t _{d(on)}		-	7	-	nS		
Turn-on Rise Time	tr	V _{DD} =-15V,I _D =-3.2A	-	3	-	nS		
Turn-Off Delay Time	t _{d(off)}	V_{GS} =-10V, R_{GEN} =6 Ω	-	30	-	nS		
Turn-Off Fall Time	t _f		-	12	-	nS		
Total Gate Charge	Qg		-	9.5	-	nC		
Gate-Source Charge	Q _{gs}	V _{DS} =-15V,I _D =-4A,V _{GS} =-4.5V	-	2	-	nC		
Gate-Drain Charge	Q_{gd}		-	3	-	nC		
Drain-Source Diode Characteristics								
Diode Forward Voltage (Note 3)	V_{SD}	V _{GS} =0V,I _S =-1A	-	-	-1.2	V		

Notes:

- $\textbf{1.} \ \textbf{Repetitive Rating: Pulse width limited by maximum junction temperature.}$
- **2.** Surface Mounted on FR4 Board, $t \le 10$ sec.
- 3. Pulse Test: Pulse Width ≤ 300µs, Duty Cycle ≤ 2%.
- 4. Guaranteed by design, not subject to production



Typical Electrical and Thermal Characteristics

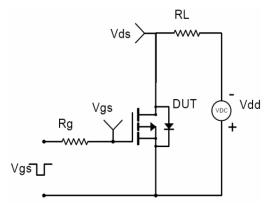
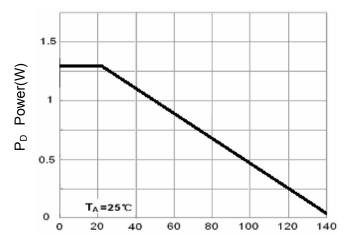


Figure 1:Switching Test Circuit



T_J-Junction Temperature(°C)

Figure 3 Power Dissipation

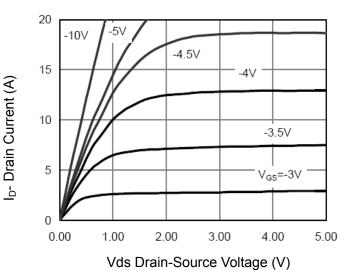


Figure 5 Output Characteristics

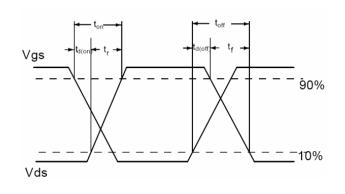


Figure 2:Switching Waveforms

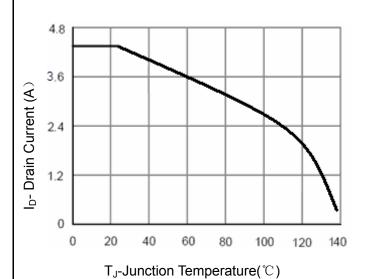


Figure 4 Drain Current

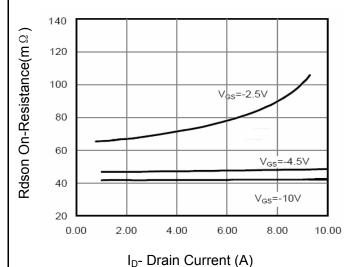


Figure 6 Drain-Source On-Resistance



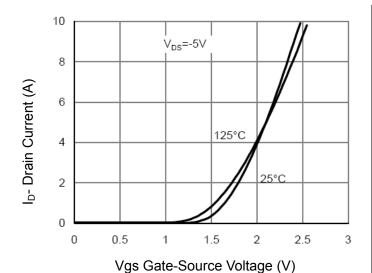
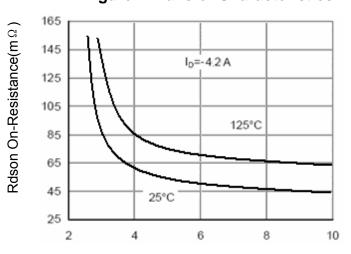


Figure 7 Transfer Characteristics



Vgs Gate-Source Voltage (V)

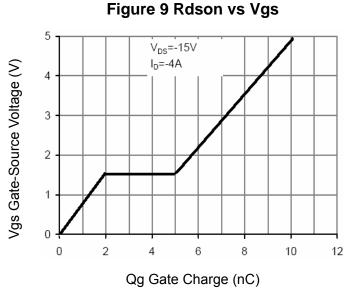
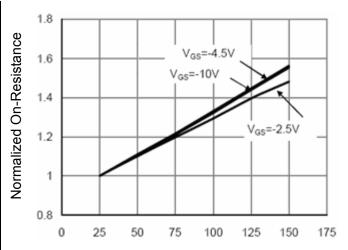
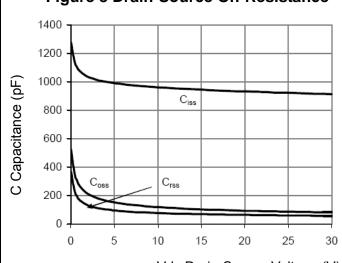


Figure 11 Gate Charge



 T_J -Junction Temperature(${}^{\circ}$ C) Figure 8 Drain-Source On-Resistance



Vds Drain-Source Voltage (V)

Figure 10 Capacitance vs Vds

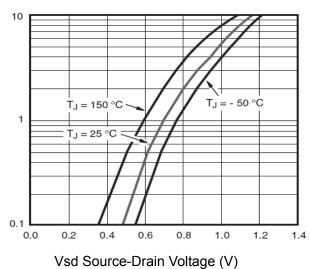


Figure 12 Source- Drain Diode Forward

Is- Reverse Drain Current (A)



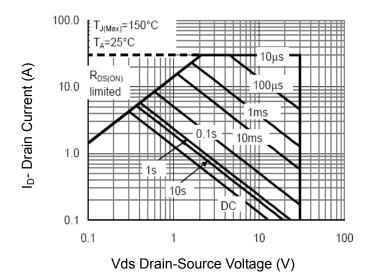


Figure 13 Safe Operation Area

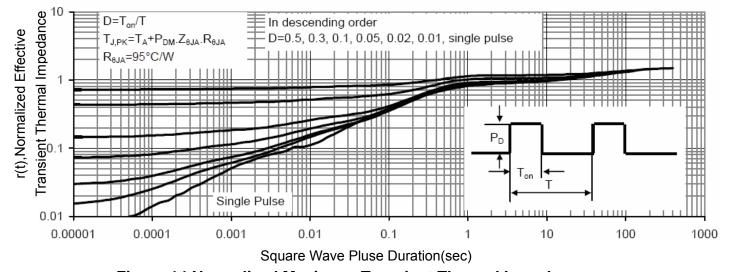
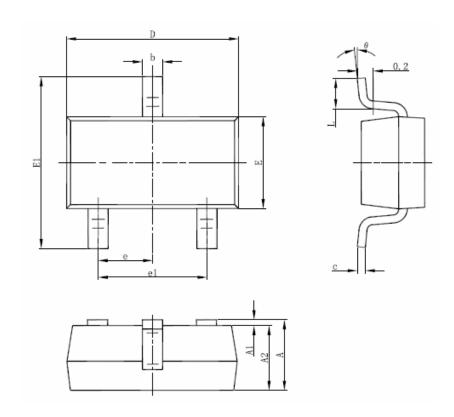


Figure 14 Normalized Maximum Transient Thermal Impedance



SOT-23-3L Package Information



Symbol	Dimensions In	Millimeters	Dimensions In Inches		
Symbol	Min	Max	Min	Max	
Α	1.050	1.250	0.041	0.049	
A1	0.000	0.100	0.000	0.004	
A2	1.050	1.150	0.041	0.045	
b	0.300	0.500	0.012	0.020	
С	0.100	0.200	0.004	0.008	
D	2.820	3.020	0.111	0.119	
E	1.500	1.700	0.059	0.067	
E1	2.650	2.950	0.104	0.116	
е	0.950	(BSC)	0.037(BSC)	
e1	1.800	2.000	0.071	0.079	
L	0.300	0.600	0.012	0.024	
θ	0°	8°	0°	8°	

Notes

- 1. All dimensions are in millimeters.
- 2. Tolerance ±0.10mm (4 mil) unless otherwise specified
- 3. Package body sizes exclude mold flash and gate burrs. Mold flash at the non-lead sides should be less than 5 mils.
- 4. Dimension L is measured in gauge plane.
- 5. Controlling dimension is millimeter, converted inch dimensions are not necessarily exact.



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