

#### **SEIKO EPSON CORPORATION**

### CRYSTAL OSCILLATOR (Programmable) **OUTPUT: CMOS**



**Product Number** 

SG-8018CG: X1G005601xxxx00 SG-8018CE: X1G005591xxxx00 SG-8018CB: X1G005581xxxx00 SG-8018CA: X1G005571xxxx00

## **SG-8018** series

: 0.67 MHz to 170 MHz (1 ppm Step) • Frequency range

 Supply voltage : 1.62 V to 3.63 V

: Output enable (OE) or Standby (ST) Function

• Frequency tolerance: ±50 ppm (-40 °C to +105 °C)

Including frequency aging(+25 °C, 10 years)

• PLL technology to enable short lead time

• Available field oscillator programmer "SG-Writer II"



2.5 × 2.0 mm 3.2 × 2.5 mm





СВ 5.0 × 3.2 mm 7.0 × 5.0 mm

Specifications	(characteristics)
Opcomoduona	( Or larable library

Specifical	lions (chara	iciensiics)										
Ite	m	Symbol	Specifications			Conditions/Remarks						
Supply voltage Vcc		Voc		V Typ.	2.50 V Typ.	3.30 V Typ.						
		1.62 V to 1.98 V			2.70 V to 3.63 V	-						
Output frequen	cy range	f <sub>O</sub>	0.67 MHz to 170 MHz									
Storage tempe		T_stg			-40 °C to +125 °C			Storage as single product.				
Operating temp		T_use			+105 °C		-					
Frequency tolerance*1 f_tol		J: ±50 × 10 <sup>-6</sup>				T_use = -40 °C to +105 °C						
			3.2 mA Max.	3.3 mA Max.	3.4 mA Max.	3.5 mA Max.	T_use = +105 °C	No load, f <sub>0</sub> = 20 MHz				
Current consur	nntion	Icc	2.7 ו	mA Typ.	2.9 mA Typ.	3.0 mA Typ.	T_use = +25 °C	140 10aa, 10 - 20 IVII 12				
Current consul	приоп	100	5.5 mA Max.	5.8 mA Max.	6.7 mA Max.	8.1 mA Max.	T_use = +105 °C	No load, fo = 170 MHz				
			4.7 ı	mA Typ.	5.7 mA Typ.	6.8 mA Typ.	T_use = +25 °C	INU IUAU, IO - 17U IVITIZ				
Output disable	current	l_dis	3.2 mA Max.	3.2 mA Max.	3.3 mA Max.	3.5 mA Max.	OE = GND, f <sub>0</sub> = 170	= GND, f <sub>0</sub> = 170 MHz				
Standby curren	ıt.	I std	0.9 μA Max.	1.0 µA Max.	1.5 μA Max.	2.5 µA Max.	T_use = +105 °C	<u>ST</u> = (	ZNID			
Standby Curren	ıı	i_siu	0.3 μA Typ.	0.4 μA Typ.	0.5 μA Typ.	1.1 μA Typ.	T_use = +25 °C	31 - 0	טווכ			
Symmetry		SYM		45 % 1	o 55 %		50 % V <sub>CC</sub> Level					
							I <sub>OH</sub> /I <sub>OL</sub> Conditions	,	, ,			[mA]
			90 % V <sub>CC</sub> Min.			Rise/Fall time	V <sub>CC</sub>	*A	*B	*C	*D	
						Default (f <sub>O</sub> > 40 MHz), Fast	I <sub>OH</sub>	-2.5 2.5	-3.5 3.5	-4.0 4.0	-5.0 5.0	
Output voltage							lou	-1.5	-2.0		-3.0	
(DC characteris	stics)						Default (f <sub>O</sub> ≤ 40 MHz)	I <sub>OL</sub>	1.5	2.0	2.5	3.0
,			10 % V <sub>CC</sub> Max.			Slow	Іон	-1.0	-1.5	-2.0	-2.5	
		V <sub>OL</sub>					loL	1.0	1.5	2.0	2.5	
						*A: 1.62 V to 1.98 V, *B: 1.98 V to 2.20 V, *C: 2.20 V to 2.80 V, *D: 2.70 V to 3.63 V						
		L CMOS	15 pF Max.				C: 2.20 V to 2.60 V, D. 2.70 V to 3.63 V					
		V <sub>IH</sub>	70 % V <sub>CC</sub> Min.									
Input voltage		VIL					OE or ST					
-		VIL	30 % V <sub>CC</sub> Max.				f . 40 MI					
	Default		3.0 ns Max.			f <sub>O</sub> > 40 MHz						
Rise time /Fall time		tr/tf	6.0 ns Max.			f <sub>O</sub> ≤ 40 MHz 20 % - 80 % V <sub>CC</sub> ,						
	Fast		3.0 ns Max. 10.0 ns Max.			$f_0 = 0.67 \text{ MHz to } 17$	'0 MHz					
	Slow					$f_0 = 0.67 \text{ MHz to } 20$						
Output disable time (OE) tstp_oe tstp_st		1 μs Max.			Measured from the time OE or ST pin crosses 30 % Vcc							
Output enable time (OE) tsta_oe		1 μs Max.			Measured from the time OE pin crosses 70 % V <sub>CC</sub>							
Output enable time (ST) tsta_st		3 ms Max.				Measured from the time ST pin crosses 70 % V <sub>CC</sub>						
Start-up time		t_str	3 ms Max.			Measured from the time V <sub>CC</sub> reaches its rated minimum value, 1.62 V						
Frequency aging f_age		f_age	This is included in frequency tolerance specification.				+25 °C, 10 years					

<sup>\*1</sup> Frequency tolerance includes initial frequency tolerance, frequency / temperature characteristics, frequency / voltage coefficient, frequency / load coefficient and frequency aging (+25 °C, 10 years).

#### Pin description

Pin	Name	I/O type	Function		
	OE Input		Output enable	High*2: Specified frequency output from OUT pin	
			output onablo	Low: Out pin is low (weak pull down), only output driver is disabled.	
1				High <sup>12</sup> : Specified frequency output from OUT pin	
	ST Inpu	Input	Standby	Low: Out pin is low (weak pull down),	
				Device goes to standby mode. Supply current reduces to the least as I_std.	
2	GND	Power	Ground		
3	OUT	Output	Clock output		
4	V <sub>cc</sub>	Power	Power supply		

<sup>\*2</sup> Please do not use the OE/\$\overline{S}\overline{T}\$ terminal in the open state.

#### SEIKO EPSON CORPORATION

### Crystal oscillator

#### Product Name

## $\begin{array}{c|c} \underline{\mathsf{SG-8018CG}} & \underline{\mathsf{25.000000MHz}} & \underline{\mathsf{T}} \, \underline{\mathsf{J}} \, \underline{\mathsf{HPA}} \\ \hline \textcircled{4.5} \, \textcircled{6.78} \end{array}$

- 1) Model 2) Package type 3) Frequency
- (4) Supply voltage (T: 1.8 V to 3.3 V Typ.)
- ⑤Frequency tolerance (J: ±50 × 10<sup>-6</sup>)
- 6Operating temperature (H: -40 °C to +105 °C)
- 7Function 8Rise/Fall time

②Package type			
CG	2.5 mm × 2.0 mm		
CE	3.2 mm × 2.5 mm		
СВ	5.0 mm × 3.2 mm		
CA	7.0 mm × 5.0 mm		

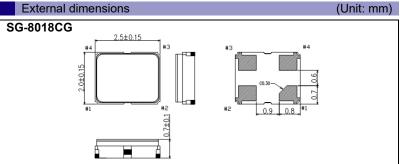
١	⑦Function		
	Р	Output enable	
ı	S	Standby	

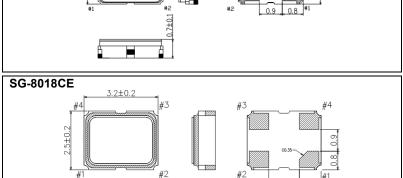
Footprint (Recommended)

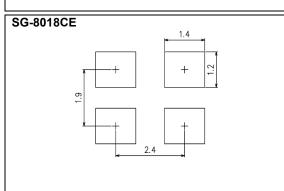
SG-8018CG

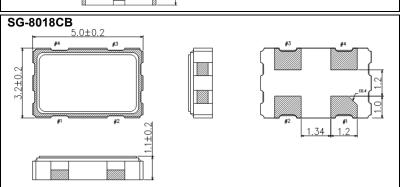
®Rise time/Fall time			
Α	Default		
В	Fast		
С	Slow		

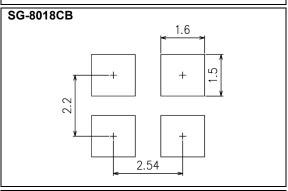
(Unit: mm)

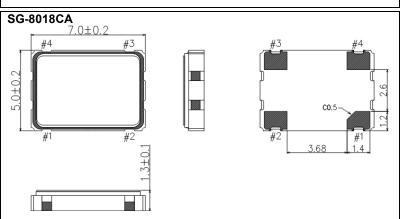


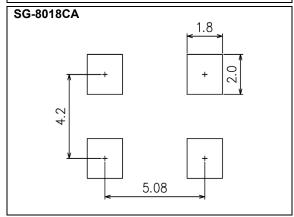












#### ■Notes:

In order to achieve optimum jitter performance, the 0.1  $\mu$ F capacitor between  $V_{CC}$  and GND should be placed. It is also recommended that the capacitors are placed on the device side of the PCB, as close to the device as possible and connected together with short wiring pattern.

# PROMOTION OF ENVIRONMENTAL MANAGEMENT SYSTEM CONFORMING TO INTERNATIONAL STANDARDS

At Seiko Epson, all environmental initiatives operate under the Plan-Do-Check-Action (PDCA) cycle designed to achieve continuous improvements. The environmental management system (EMS) operates under the ISO 14001 environmental management standard.

All of our major manufacturing and non-manufacturing sites, in Japan and overseas, completed the acquisition of ISO 14001 certification.

ISO 14000 is an international standard for environmental management that was established by the International Standards Organization in 1996 against the background of growing concern regarding global warming, destruction of the ozone layer, and global deforestation.

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In order provide high quality and reliable products and services than meet customer needs, Seiko Epson made early efforts towards obtaining ISO9000 series certification and has acquired ISO9001 for all business establishments in Japan and abroad. We have also acquired IATF 16949 certification that is requested strongly by major automotive manufacturers as standard.

IATF 16949 is the international standard that added the sector-specific supplemental requirements for automotive industry based on ISO9001.

Explanation of the mark that are using it for the catalog



►Pb free.



► Complies with EU RoHS directive.

\*About the products without the Pb-free mark.

Contains Pb in products exempted by EU RoHS directive.

(Contains Pb in sealing glass, high melting temperature type solder or other.)



▶ Designed for automotive applications such as Car Multimedia, Body Electronics, Remote Keyless Entry etc.



▶ Designed for automotive applications related to driving safety (Engine Control Unit, Air Bag, ESC etc).

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