

NCE N-Channel Enhancement Mode Power MOSFET

Description

The NCE2302 uses advanced trench technology to provide excellent $R_{DS(ON)}$, low gate charge and operation with gate voltages as low as 2.5V. This device is suitable for use as a Battery protection or in other Switching application.

General Features

V_{DS} = 20V,I_D =4A

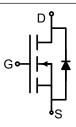
 $R_{DS(ON)}$ < 59m Ω @ V_{GS} =2.5V

 $R_{DS(ON)} < 45 \text{m}\Omega$ @ V_{GS} =4.5V

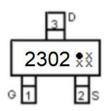
- High power and current handing capability
- Lead free product is acquired
- Surface mount package

Application

- Battery protection
- Load switch
- Power management



Schematic diagram



Marking and pin assignment



SOT-23 top view

Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
2302 *X	NCE2302	SOT-23	Ø180mm	8 mm	3000 units

Absolute Maximum Ratings (T_A=25℃unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V _{DS}	20	V
Gate-Source Voltage	V _G s	±12	V
Drain Current-Continuous	I _D	4	Α
Drain Current-Pulsed (Note 1)	I _{DM}	10	Α
Maximum Power Dissipation	P _D	1	W
Operating Junction and Storage Temperature Range	T_{J}, T_{STG}	-55 To 150	$^{\circ}$ C

Thermal Characteristic

Thermal Resistance, Junction-to-Ambient (Note 2)	ReJA	125	°C/W
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Electrical Characteristics (T_A=25 ℃ unless otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Max	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V I _D =250μA	20	22	-	V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =20V,V _{GS} =0V	-	-	1	μΑ

NCE2302

Parameter	Symbol	Condition	Min	Тур	Max	Unit
Gate-Body Leakage Current	Igss	V _{GS} =±12V,V _{DS} =0V	-	-	±100	nA
On Characteristics (Note 3)	,		'			
Gate Threshold Voltage	V _{GS(th)}	$V_{DS}=V_{GS},I_{D}=250\mu A$	0.5	0.85	1.2	V
Dunin Course On Otata Danistan		V _{GS} =2.5V, I _D =2.5A	-	37	59	mΩ
Drain-Source On-State Resistance	R _{DS(ON)}	V _{GS} =4.5V, I _D =2.9A	-	30	45	mΩ
Forward Transconductance	g FS	V _{DS} =5V,I _D =2.9A	-	8	-	S
Dynamic Characteristics (Note4)						
Input Capacitance	C _{lss}	\/ -40\/\/ -0\/	-	300	-	PF
Output Capacitance	Coss	V_{DS} =10V, V_{GS} =0V, F=1.0MHz	_	120	-	PF
Reverse Transfer Capacitance	C _{rss}	F=1.UIVIHZ	-	80	-	PF
Switching Characteristics (Note 4)						
Turn-on Delay Time	t _{d(on)}		-	10	15	nS
Turn-on Rise Time	t _r	V_{DD} =10 V , I_{D} =2.9 A	-	50	85	nS
Turn-Off Delay Time	t _{d(off)}	V_{GS} =4.5 V , R_{GEN} =6 Ω	_	17	45	nS
Turn-Off Fall Time	t _f		_	10	20	nS
Total Gate Charge	Qg	V 40VI 0.0A	-	4.0	10	nC
Gate-Source Charge	Q _{gs}	$V_{DS}=10V, I_{D}=2.9A,$	-	0.65	-	nC
Gate-Drain Charge	Q _{gd}	V_{GS} =4.5 V	-	1.2	-	nC
Drain-Source Diode Characteristics						
Diode Forward Voltage (Note 3)	V _{SD}	V _{GS} =0V,I _S =2.9A	-	0.75	1.2	V
Diode Forward Current (Note 2)	Is		_	-	4	Α

Notes:

- 1. Repetitive Rating: Pulse width limited by maximum junction temperature.
- **2.** Surface Mounted on FR4 Board, $t \le 10$ sec.
- **3.** Pulse Test: Pulse Width ≤ 300μ s, Duty Cycle ≤ 2%.
- 4. Guaranteed by design, not subject to production



Typical Electrical and Thermal Characteristics

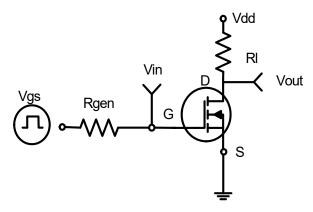


Figure 1:Switching Test Circuit

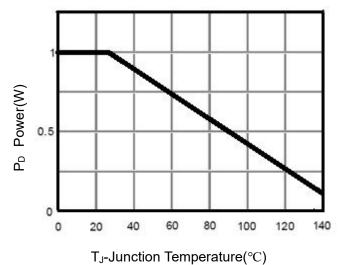


Figure 3 Power Dissipation

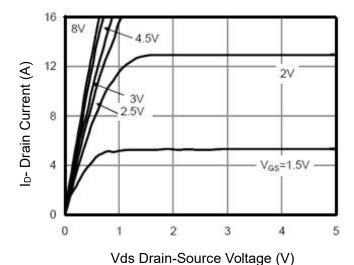


Figure 5 Output Characteristics

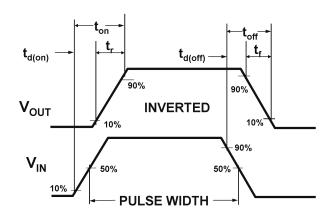


Figure 2:Switching Waveforms

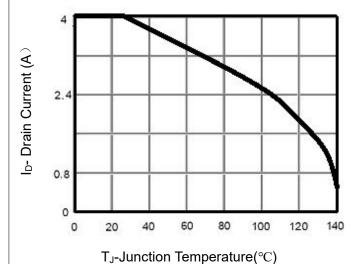


Figure 4 Drain Current

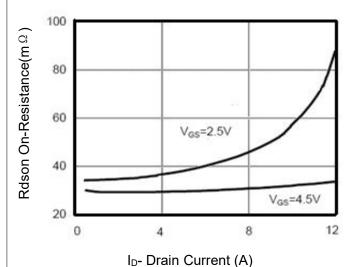
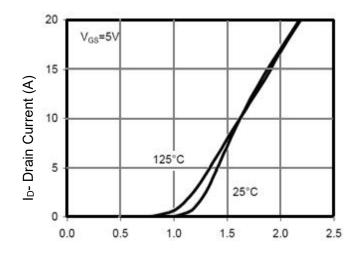
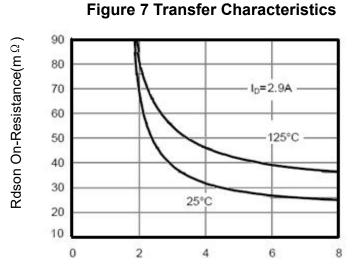


Figure 6 Drain-Source On-Resistance





Vgs Gate-Source Voltage (V)



Vgs Gate-Source Voltage (V)

Figure 9 Rdson vs Vgs

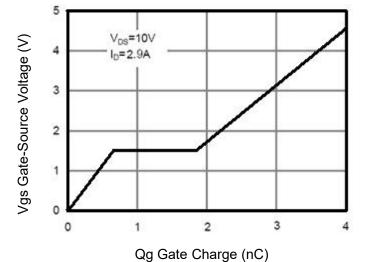
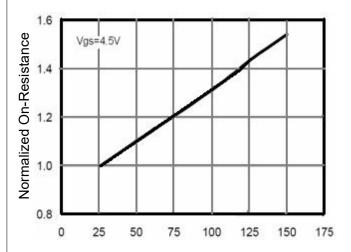
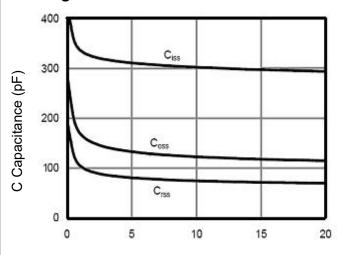


Figure 11 Gate Charge



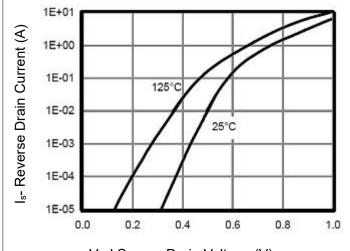
T_J-Junction Temperature(°C)

Figure 8 Drain-Source On-Resistance



Vds Drain-Source Voltage (V)

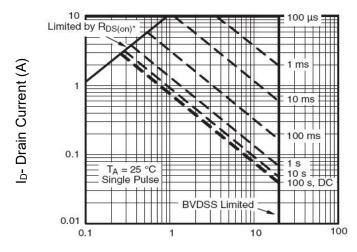
Figure 10 Capacitance vs Vds



Vsd Source-Drain Voltage (V)

Figure 12 Source- Drain Diode Forward





Vds Drain-Source Voltage (V)

Figure 13 Safe Operation Area

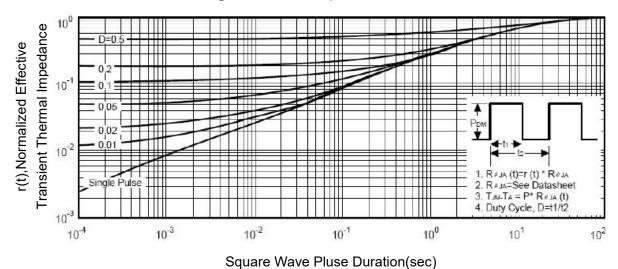
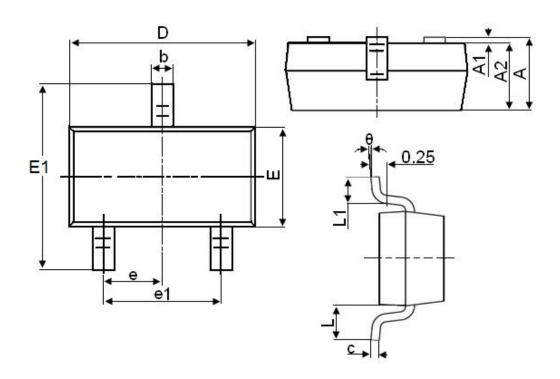


Figure 14 Normalized Maximum Transient Thermal Impedance



SOT-23 (C) Package Information



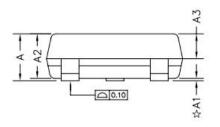
Cymphol	Dimensions in Millimeters				
Symbol	MIN.	MAX.			
А	0.900	1.150			
A1	0.000	0.100			
A2	0.900	1.050			
b	0.300	0.500			
С	0.080	0.150			
D	2.800	3.000			
Е	1.200	1.400			
E1	2.250	2.550			
е		0.950TYP			
e1	1.800	2.000			
L	0.550REF				
L1	0.300	0.500			
θ	0°	8°			

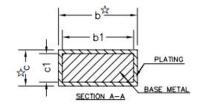
Notes

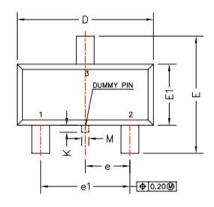
- 1. All dimensions are in millimeters.
- 2. Tolerance ±0.10mm (4 mil) unless otherwise specified
- 3. Package body sizes exclude mold flash and gate burrs. Mold flash at the non-lead sides should be less than 5 mils.
- 4. Dimension L is measured in gauge plane.
- $5. \ Controlling \ dimension \ is \ millimeter, \ converted \ inch \ dimensions \ are \ not \ necessarily \ exact.$

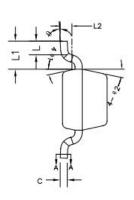


SOT-23 (E) Package Information









Cumbal	Millimeters			
Symbol	Min.	Max.		
Α	0.89	1.12		
A1	0.01	0.10		
A2	0.88	1.02		
A3	0.43	0.63		
b	0.36	0.50		
b1	0.35	0.45		
С	0.14	0.20		
c1	0.14	0.16		
D	2.80	3.00		
E	2.35	2.64		
E1	1.20	1.40		
е	0.90	1.00		
e1	1.80	2.00		
L	0.40	0.60		
L1	0.6REF			
L2	0.25BSC			
M	0.10	0.25		
K	0.00	0.25		
θ	0°	8°		
θ1	10°	14°		
θ2	10°	14°		



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