CRYSTAL OSCILLATOR (Programmable) OUTPUT: CMOS

SG-8018 series

• Frequency range: 0.67 MHz to 170 MHz (1 ppm Step)

• Supply voltage : 1.62 V to 3.63 V

Function : Output enable (OE) or Standby (ST)
 Frequency tolerance : ±50 ppm (-40 °C to +105 °C)
 Include frequency aging(+25 °C, 10 years)

• Package : 2.5 x 2.0, 3.2 x 2.5, 5.0 x 3.2, 7.0 x 5.0 (mm)

• PLL technology to enable short lead time

• Available field oscillator programmer "SG-Writer II"





Product Number (please contact us) SG-8018CA: X1G005571xxxx00 SG-8018CB: X1G005581xxxx00 SG-8018CE: X1G005591xxxx00 SG-8018CG: X1G005601xxxx00









CG CE CB CA 2.5 x 2.0mm 3.2 x 2.5mm 5.0 x 3.2mm 7.0 x 5.0mm

Specifications (characteristics)

Specifications (characteristics)										
Item Symbol			Specifi	cations	Conditions/Remarks					
Supply voltage		Vcc	1.80	V Typ.	2.50 V Typ.	3.30 V Typ.		_		
		VCC	1.62 V to 1.98 V	1.98 V to 2.20 V	2.20 V to 2.80 V	2.70 V to 3.63 V				
Output frequency range fo			0.67 MHz to 170 MHz							
Storage temperature T_stg		T_stg		-40 °C to	+125 °C		Storage as single product.			
Operating temperature T_use		T_use			+105 °C	-				
Frequency tolerance*1		f_tol	J: ±50 × 10 ⁻⁶				T_use = -40 °C to +105 °C			
			3.2 mA Max.	3.3 mA Max.	3.4 mA Max.	3.5 mA Max.	T_use = +105 °C	No load, fo = 20 MHz		
Current consum	ntion	Icc		mA Typ.	2.9 mA Typ.	3.0 mA Typ.	T_use = +25 °C	100 10ad, 10 = 20 WH12		
Current consum	iption	100	5.5 mA Max.	5.8 mA Max.	6.7 mA Max.	8.1 mA Max.	T_use = +105 °C	No load, $f_0 = 170 \text{ MHz}$		
			4.7 r	mA Typ.	5.7 mA Typ.	6.8 mA Typ.	T_use = +25 °C	110 10dd, 10 - 170 Wil 12		
Output disable	current	I_dis	3.2 mA Max.	3.2 mA Max.	3.3 mA Max.	3.5 mA Max.	OE = GND, f _O = 170	MHz		
Standby curren	+	I_std	0.9 μA Max.	1.0 µA Max.	1.5 µA Max.	2.5 µA Max.	T_use = +105 °C	ST = GND		
Standby Current	ι	i_siu	0.3 μA Typ.	0.4 μA Typ.	0.5 μA Typ.	1.1 μA Typ.	T_use = +25 °C	31 - GND		
Symmetry		SYM		45 % 1	0 55 %	50 % V _{CC} Level				
							I _{OH} /I _{OL} Conditions	[mA]		
		.,	90 % V _{CC} Min.				Rise/Fall time	Vcc *A *B *C *D		
		Voh					Default (f _O > 40 MHz), Fast			
Output voltage								I _{OL} 2.5 3.5 4.0 5.0 I _{OH} -1.5 -2.0 -2.5 -3.0		
(DC characteris	stics)						Liperault ($T_0 \le 40$ MHz) F	lo _L 1.5 2.0 2.5 3.0		
·	•						Slow	loн -1.0 -1.5 -2.0 -2.5		
		Vol	10 % V _{CC} Max.			I _{OL} 1.0 1.5 2.0				
							*A: 1.62 V to 1.98 V, *B: 1.98 V to 2.20 V, *C: 2.20 V to 2.80 V, *D: 2.70 V to 3.63 V			
Output load cor	ndition	L CMOS	15 pF Max.				0. 2.20	- D. 2.70 V to 3.00 V		
Catpat load col	idition	V _{IH}			/cc Min.	OE or ST				
Input voltage		VIL		30 % V						
		VIL								
	Default		3.0 ns Max.				f _O > 40 MHz			
Rise and Fall		tr/tf		6.0 ns Max.			f ₀ ≤ 40 MHz	20 % - 80 % V _{CC,}		
time	Fast				ns Max.			0 MHz L_CMOS = 15 pF		
	Slow		10.0 ns Max.				$f_0 = 0.67 \text{ MHz to } 20$			
Disable Time		t_stp	1 μs Max.				Measured from the time OE or $\overline{\text{ST}}$ pin crosses 30 % V_{CC}			
Enable Time		t_sta	1 μs Max.			Measured from the time OE pin crosses 70 % Vcc				
Resume Time		t_res		3 r	ns Max.		Measured from the time \$\overline{ST}\$ pin crosses 70 % Vcc			
Start-up time		t_str	3 ms Max.			Measured from the time V _{CC} reaches its rated minimum value, 1.62 V				
Frequency aging f		f_aging	This is included in frequency tolerance specification.				+25 °C, 10 years			
*4 [1.'6 11.1.'61' (.05.00.40)				

^{*1} Frequency tolerance includes initial frequency tolerance, temperature variation, supply voltage variation, reflow drift, load drift and aging (+25 °C, 10 years).

Pin description

Pin	Name	I/O type	Function					
	OE Input		Output enable	High: Specified frequency output from OUT pin				
)	IIIput	Output enable	Low: Out pin is low (weak pull down), only output driver is disabled.				
1				High: Specified frequency output from OUT pin				
	ST	Input	Standby	Low: Out pin is low (weak pull down),				
				Device goes to standby mode. Supply current reduces to the least as I_std.				
2	GND	Power	Ground					
3	OUT	Output	Clock output					
4	V_{cc}	Power	Power supply					

C: Slow



Product Name

SG-8018CG 170.000000MHz T J H P A

1

3

45678

②Package type CA: 7.0 mm x 5.0 mm

CB: 5.0 mm x 3.2 mm

CE: 3.2 mm x 2.5 mm CG: 2.5 mm x 2.0 mm Supply voltage T: 1.8 V to 3.3 V Typ.

⑤Frequency tolerance

H: -40 °C to +105 °C

⑥Operating temperature

8 Rise/Fall time A: Default B: Fast

①Model, ②Package type,

③Frequency, ④Supply voltage,

⑤Frequency tolerance, ⑥Operating temperature,

⑦Function, ®Rise/Fall time

External dimensions (Unit: mm)

J: 50 x 10⁻⁶

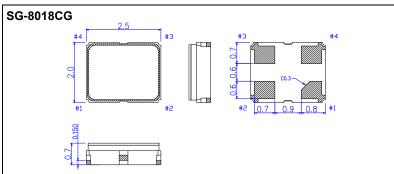


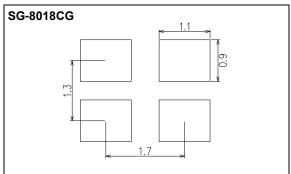
⑦Function

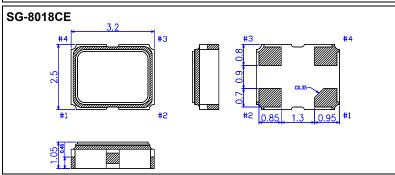
S: Standby

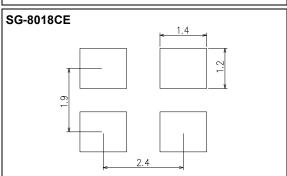
P: Output Enable

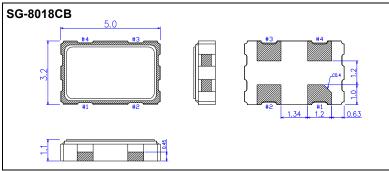
(Unit: mm)

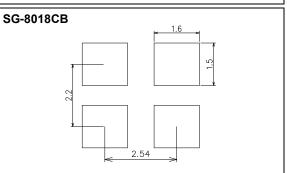


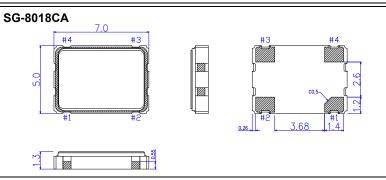


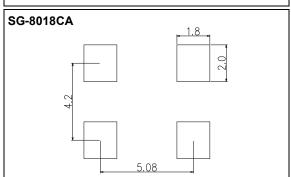










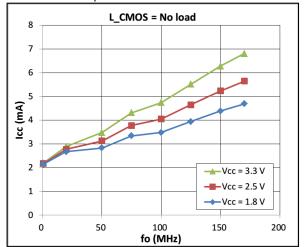


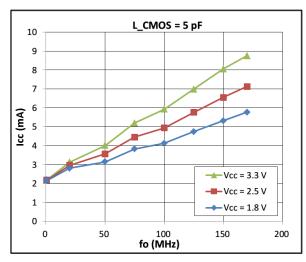
■Notes:

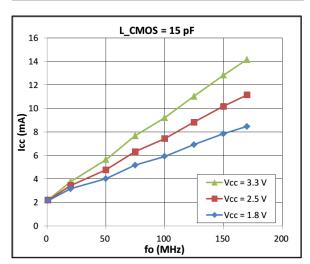
In order to achieve optimum jitter performance, the 0.1 μF capacitor between V_{CC} and GND should be placed. It is also recommended that the capacitors are placed on the device side of the PCB, as close to the device as possible and connected together with short wiring pattern.

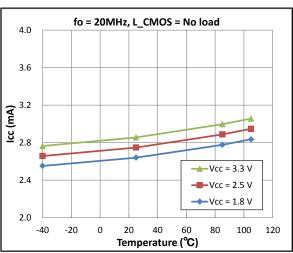
Specification Graph (Typical supplemental specification. Unless otherwise specified T_use = 25 °C, L_CMOS = 15 pF)

Current Consumption

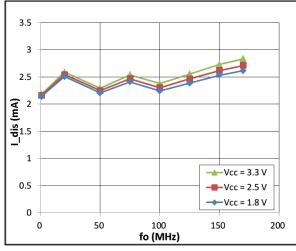


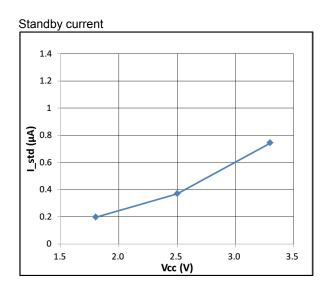






Output disable current



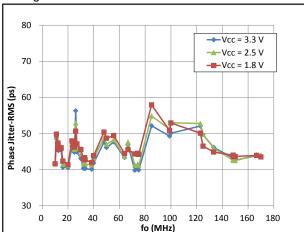


■Notes:

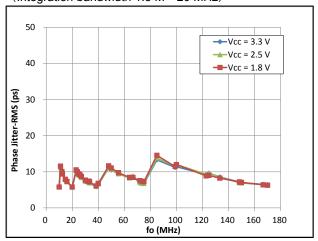
Specification Graph (Typical supplemental specification. Unless otherwise specified T_use = 25 °C, L_CMOS = 15 pF)

Phase Jitter RMS

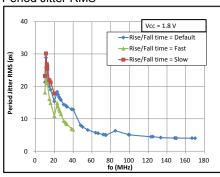
(Integration bandwidth 12 k-20 MHz)

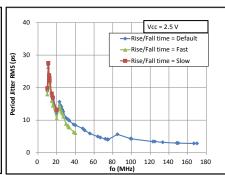


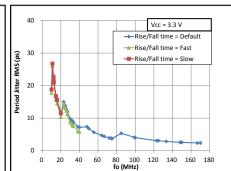
Phase Jitter RMS (Integration bandwidth 1.8 M-20 MHz)



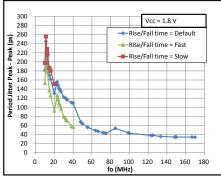
Period Jitter RMS

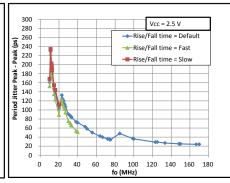


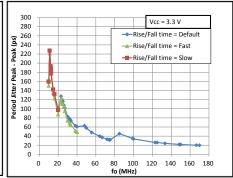




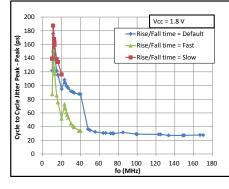
Period Jitter Peak-Peak

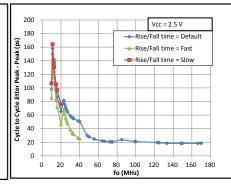


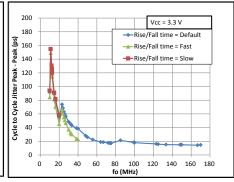




Cycle-to-Cycle Jitter Peak-Peak







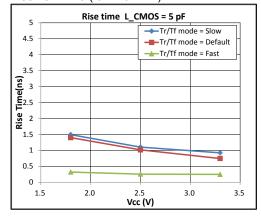
■Notes:

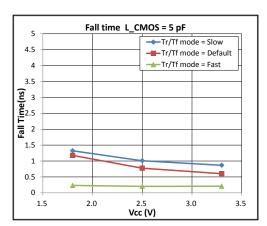


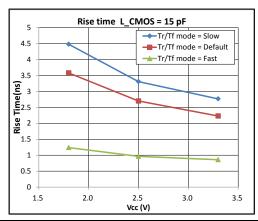
Specification Graph

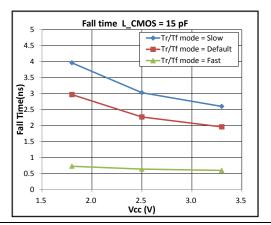
(Typical supplemental specification. Unless otherwise specified T_use = 25 °C, L_CMOS = 15 pF, VCC = 3.3 V)

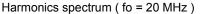
Rise/Fall Time (fo = 20 MHz)

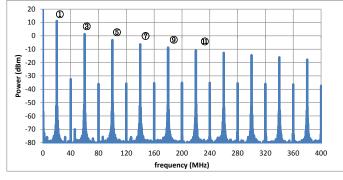




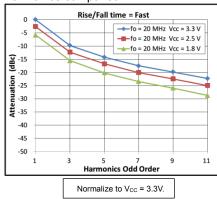


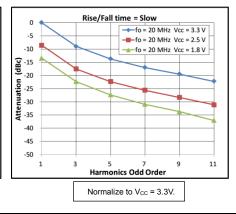


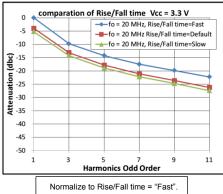




Harmonics comparison







■Notes:

frequency	slow	default	fast	
0.67 M – 20 MHz	See Slow	See Default	See Fast	
20 M – 40 MHz	-	See Default	See Fast	
40 M – 170 MHz	-	See Fast	See Fast	



Crystal oscillator

Test items Breakdown voltage Human Body Model (HBM) Machine Model (MM) Charged Device Model (CDM) Test items Breakdown voltage 2000 V 250 V

Device Marking ((Standard specification)					
Model	Factory Programmed Part Marking	Field Programmable Part Marking (Blank Samples)				
SG-8018CG	Frequency Product code 170. A2 OA23DM 1pin mark Lot No.	A2 OA23DM Lot No.				
SG-8018CE	Frequency 170.0A2 o A23DM Lot No.	A2 Product code A23DM 1pin mark Lot No.				
SG-8018CB	Frequency 170.0 A2 Product code A23DM Lot No.	A2 A23DM Ipin mark Lot No.				
SG-8018CA	Frequency 170.00 A2 Product code A23DM 1pin mark Lot No.	A2 A23DM 1pin mark Lot No.				

Simulation Model

• IBIS Model is available upon request. Please contact us. Information Required: Oscillator operating condition (i.e. Power Supply, Rise/Fall Time, Temperature)

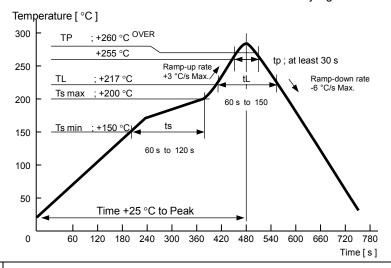


Device Material & Environmental Information

Model	Package	# of	Reference	Terminal	Terminal	Complies	Pb	MSL	Peak
	Dimensions	Pins	Weight	Material	Plating	With EU	Free	Rating	Temp.
			(Typ.)			RoHS			(Max)
SG-8018CG	2.5 x 2.0 x 0.7 mm	4	13 mg	W	Au	Yes	Yes	1	260 °C
SG-8018CE	3.2 x 2.5 x 1.0 mm	4	25 mg	W	Au	Yes	Yes	1	260 °C
SG-8018CB	5.0 x 3.2 x 1.1 mm	4	51 mg	W	Au	Yes	Yes	1	260 °C
SG-8018CA	7.0 x 5.0 x 1.3 mm	4	143 mg	W	Au	Yes	Yes	1	260 °C

SMD products Reflow profile(example)

The availability of the heat resistance for reflow conditions of JEDEC-STD-020D.01 is judged individually. Please inquire.





Pb free.



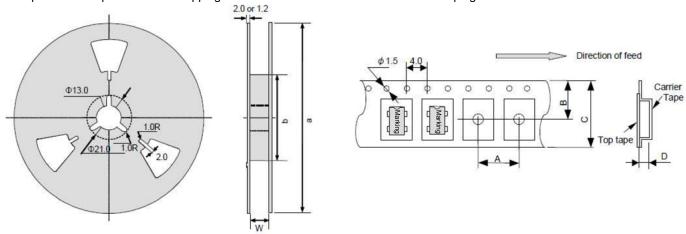
- Complies with EU RoHS directive.
 - About the products without the Pb-free mark.

 Contains Pb in products exempted by EU RoHS directive.

 (Contains Pb in sealing glass, high melting temperature type solder or other.)

Standard Packing Specification

SMD products are packed in the shipping carton as below table in accordance with taping standards EIA-481 and IEC-60286



Standard Packing Quantity & Dimension(Unit mm)

	Quantity	Reel Dimension			Car	Direction of			
Model	(pcs/Reel)	а	b	W	Α	В	С	D	Feed (L= Left Direction)
SG-8018CG	3000	Ф180	Ф60	9	4	5.25	8	1.15	L
SG-8018CE	2000	Ф180	Ф60	9	4	5.25	8	1.4	L
SG-8018CB	1000	Ф180	Ф60	13	8	7.25	12	1.4	L
SG-8018CA	1000	Ф254	Ф100	17.5	8	9.25	16	2.3	L

PROMOTION OF ENVIRONMENTAL MANAGEMENT SYSTEM CONFORMING TO INTERNATIONAL STANDARDS

At Seiko Epson, all environmental initiatives operate under the Plan-Do-Check-Action (PDCA) cycle designed to achieve continuous improvements. The environmental management system (EMS) operates under the ISO 14001 environmental management standard.

All of our major manufacturing and non-manufacturing sites, in Japan and overseas, completed the acquisition of ISO 14001 certification.

ISO 14000 is an international standard for environmental management that was established by the International Standards Organization in 1996 against the background of growing concern regarding global warming, destruction of the ozone layer, and global deforestation.

WORKING FOR HIGH QUALITY

In order provide high quality and reliable products and services than meet customer needs.

Seiko Epson made early efforts towards obtaining ISO9000 series certification and has acquired ISO9001 for all business establishments in Japan and abroad. We have also acquired ISO/TS 16949 certification that is requested strongly by major automotive manufacturers as standard.

ISO/TS16949 is the international standard that added the sector-specific supplemental requirements for automotive industry based on ISO9001.

Explanation of the mark that are using it for the catalog



►Pb free.



- ► Complies with EU RoHS directive.
 - *About the products without the Pb-free mark.

 Contains Pb in products exempted by EU RoHS directive.

 (Contains Pb in sealing glass, high melting temperature type solder or other.)



▶ Designed for automotive applications such as Car Multimedia, Body Electronics, Remote Keyless Entry etc.



 \blacktriangleright Designed for automotive applications related to driving safety (Engine Control Unit, Air Bag, ESC etc).

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