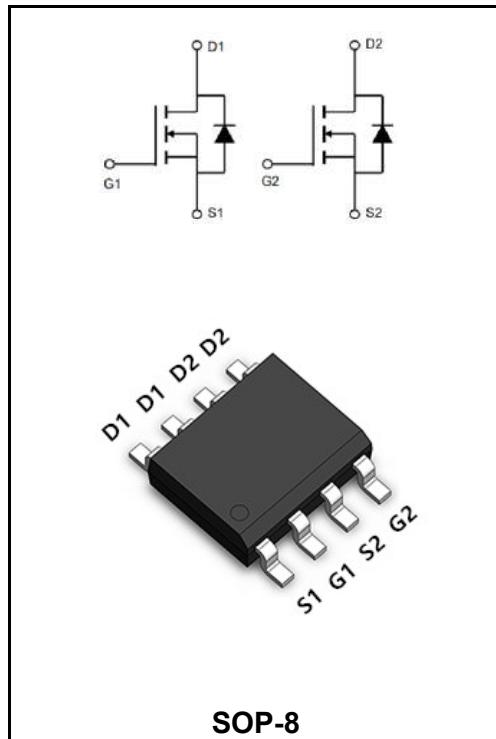


**30V N+N-CHANNEL ENHANCEMENT MODE MOSFET**
**MAIN CHARACTERISTICS**

$I_D$	10A
$V_{DSS}$	30V
$R_{DS(on)-typ}(@V_{GS}=10V)$	< 12mΩ (Type: 9 mΩ)


**Application**

- ◆ Lithium battery protection
- ◆ Wireless impact
- ◆ Mobile phone fast charging

**Product Specification Classification**

Part Number	Package	Marking	Pack
YFW10H03S	SOP-8	S3214 HT4001T	3000PCS/Tape

**Maximum Ratings at  $T_c=25^\circ C$  unless otherwise specified**

Characteristics	Symbols	Value	Units
Drain-Source Voltage	$V_{DS}$	30	V
Gate - Source Voltage	$V_{GS}$	$\pm 20$	V
Continuous Drain Current, $V_{GS} @ 10V^1$ @ $T_c=25^\circ C$	$I_D$	10	A
Continuous Drain Current, $V_{GS} @ 10V^1$ @ $T_c=100^\circ C$	$I_D$	8.2	A
Continuous Drain Current, $V_{GS} @ 10V^1$ @ $T_A=25^\circ C$	$I_D$	9.5	A
Continuous Drain Current, $V_{GS} @ 10V^1$ @ $T_A=70^\circ C$	$I_D$	7.6	A
Pulsed Drain Current <sup>2</sup>	$I_{DM}$	75	A
Single Pulse Avalanche Energy <sup>3</sup>	$E_{AS}$	24.2	mJ
Avalanche Current	$I_{AS}$	22	A
Total Power Dissipation <sup>4</sup> @ $T_c=25^\circ C$	$P_D$	26	W
Total Power Dissipation <sup>4</sup> @ $T_A=25^\circ C$	$P_D$	1.67	W
Storage Temperature Range	$T_{STG}$	-55 to +150	°C
Operating Junction Temperature Range	$T_J$	-55 to +150	°C
Thermal Resistance Junction-ambient <sup>1</sup>	$R_{\theta JA}$	75	°C/W
Thermal Resistance Junction-Case <sup>1</sup>	$R_{\theta JC}$	4.8	°C/W

**Maximum Ratings at T<sub>c</sub>=25°C unless otherwise specified**

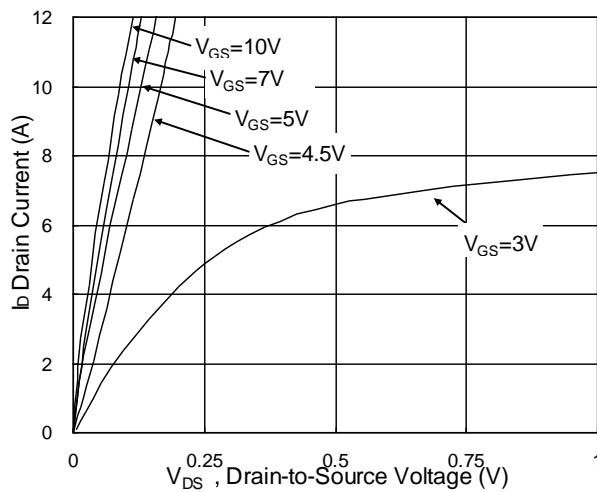
Characteristics	Test Condition	Symbols	Min	Typ	Max	Units
Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V, I <sub>D</sub> =250uA	B <sub>VDSS</sub>	30	33	-	V
BVDSS Temperature Coefficient	Reference to 25°C , I <sub>D</sub> =1mA	ΔB <sub>VDSS/ΔTJ</sub>	-	0.023	-	mV/°C
Static Drain-Source On-Resistance <sup>2</sup>	V <sub>GS</sub> =10V, I <sub>D</sub> =8A	R <sub>DS(ON)</sub>	-	9	12	mΩ
	V <sub>GS</sub> =4.5V, I <sub>D</sub> =6A		-	14	18	mΩ
Gate Threshold Voltage	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250uA	V <sub>GS(th)</sub>	1.0	1.6	2.5	V
V <sub>GS(th)</sub> Temperature Coefficient		ΔV <sub>GS(th)</sub>	-	-5.08	-	mV/°C
Drain-Source Leakage Current	V <sub>DS</sub> =24V, V <sub>GS</sub> =0V T <sub>J</sub> =25°C	I <sub>DSS</sub>	-	-	1	uA
	V <sub>DS</sub> =24V , V <sub>GS</sub> =0V , T <sub>J</sub> =55°C		-	-	5	
Gate-Source Leakage Current	V <sub>GS</sub> =±20V, V <sub>DS</sub> =0V	I <sub>GSS</sub>	-	-	±100	nA
Forward Transconductance	V <sub>DS</sub> = 5V, I <sub>D</sub> = 15A	g <sub>fs</sub>	-	24.4	-	S
Gate Resistance	V <sub>DS</sub> =0V , V <sub>GS</sub> =0V , f=1MHz	R <sub>g</sub>		1.8	-	Ω
Total Gate Charge ( 4.5V )	V <sub>DS</sub> =15V V <sub>GS</sub> =4.5V I <sub>D</sub> =12A	Q <sub>g</sub>	-	9.82	-	nC
Gate-Source Charge		Q <sub>gs</sub>	-	2.24	-	nC
Gate-Drain Charge		Q <sub>gd</sub>	-	5.54	-	nC
Turn-on delay time	V <sub>DD</sub> =15V V <sub>GS</sub> =10V R <sub>G</sub> = 1.5 I <sub>D</sub> = 20A	t <sub>d(on)</sub>	-	6.4	-	ns
Rise Time		T <sub>r</sub>	-	39	-	ns
Turn-Off Delay Time		t <sub>d(OFF)</sub>	-	21	-	ns
Fall Time		t <sub>f</sub>	-	4.7	-	ns
Input Capacitance	V <sub>DS</sub> =15V V <sub>GS</sub> =0V f=1.0MHz	C <sub>iss</sub>	-	896	-	pF
Output Capacitance		C <sub>oss</sub>	-	126	-	pF
Reverse Transfer Capacitance		C <sub>rss</sub>	-	108	-	pF
Continuous Source Current <sup>1,5</sup>	V <sub>G</sub> =V <sub>D</sub> =0V , Force Current	I <sub>s</sub>	-	-	37	A
Pulsed Source Current <sup>2,5</sup>		I <sub>SM</sub>	-	-	75	A
Diode Forward Voltage <sup>2</sup>	V <sub>GS</sub> =0V , I <sub>s</sub> =1A , T <sub>J</sub> =25°C	V <sub>SD</sub>	-	-	1	V

Note :

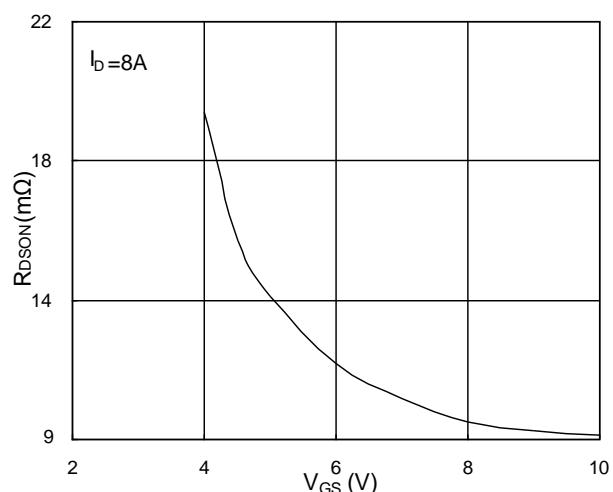
1. The data tested by surface mounted on a 1 inch<sup>2</sup> FR-4 board with 2OZ copper.
2. The data tested by pulsed , pulse width ≤ 300us , duty cycle ≤ 2%
3. The EAS data shows Max. rating . The test condition is V<sub>DD</sub>=25V,V<sub>GS</sub>=10V,L=0.1mH,I<sub>AS</sub>=8A
4. The power dissipation is limited by 175°C junction temperature
5. The data is theoretically the same as I<sub>D</sub> and I<sub>DM</sub> , in real applications , should be limited by total power dissipation.

**Ratings and Characteristic Curves**

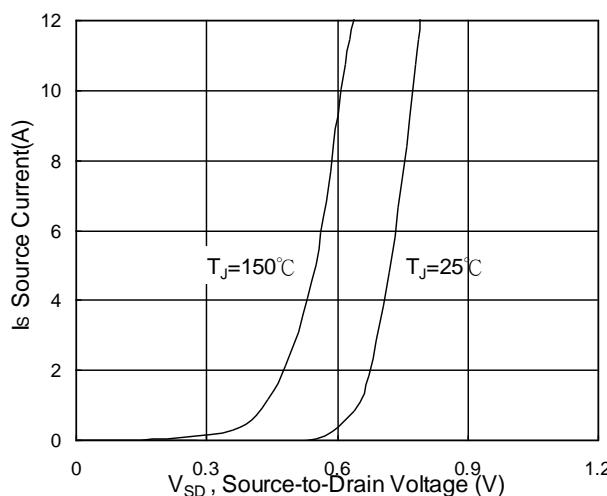
**Typical Characteristics**



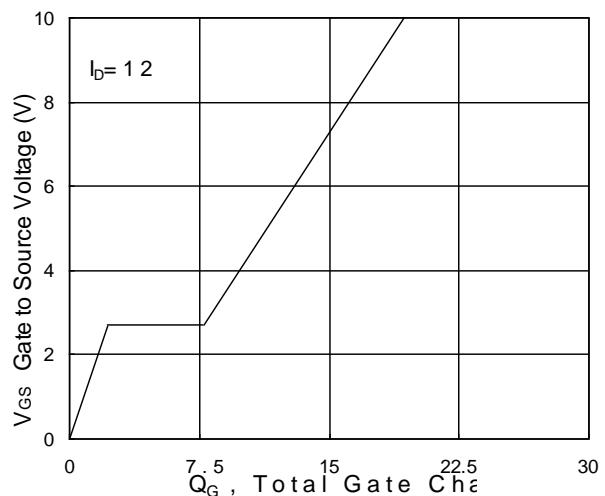
**Fig.1 Typical Output Characteristics**



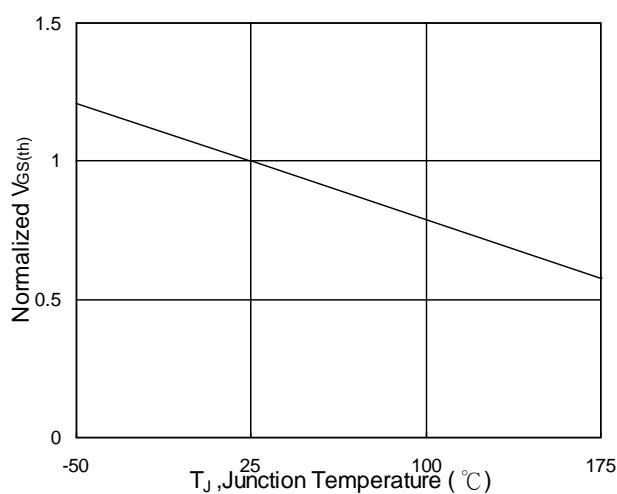
**Fig.2 On-Resistance vs. G-S Voltage**



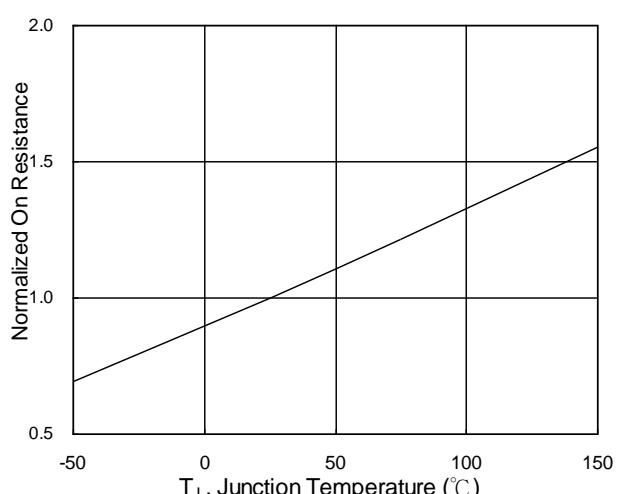
**Fig.3 Forward Characteristics of Reverse**



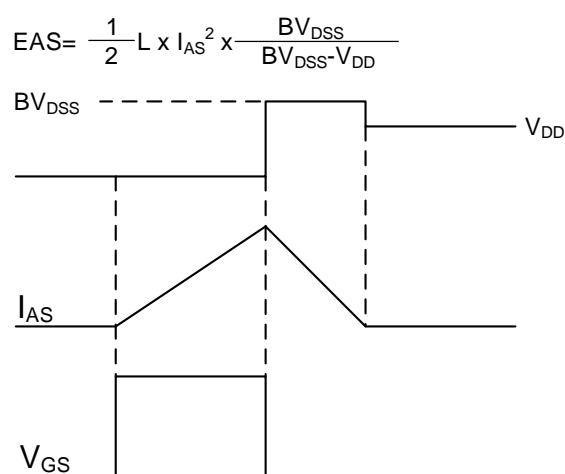
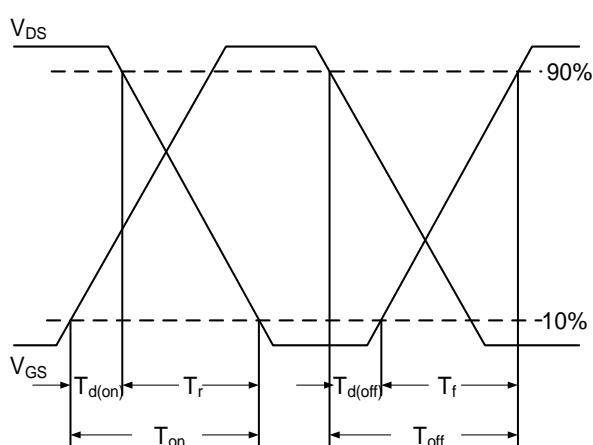
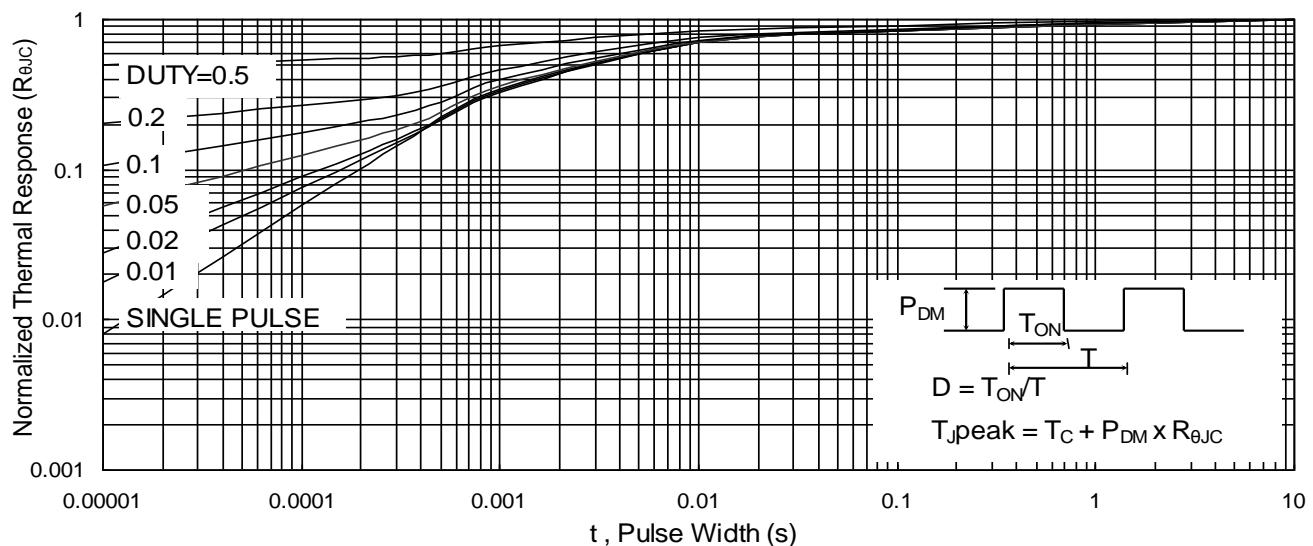
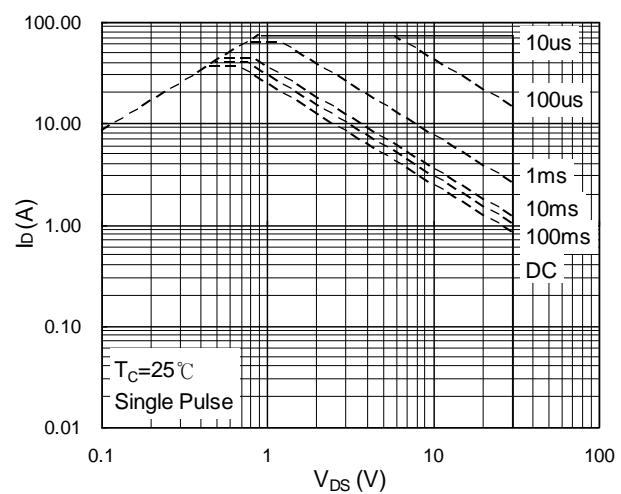
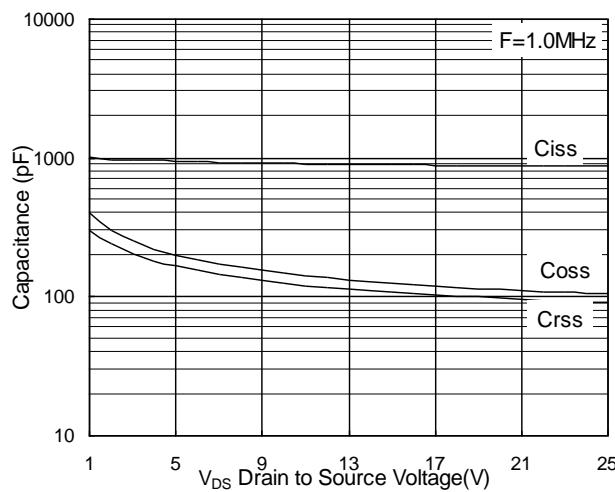
**Fig.4 Gate-charge Characteristics**

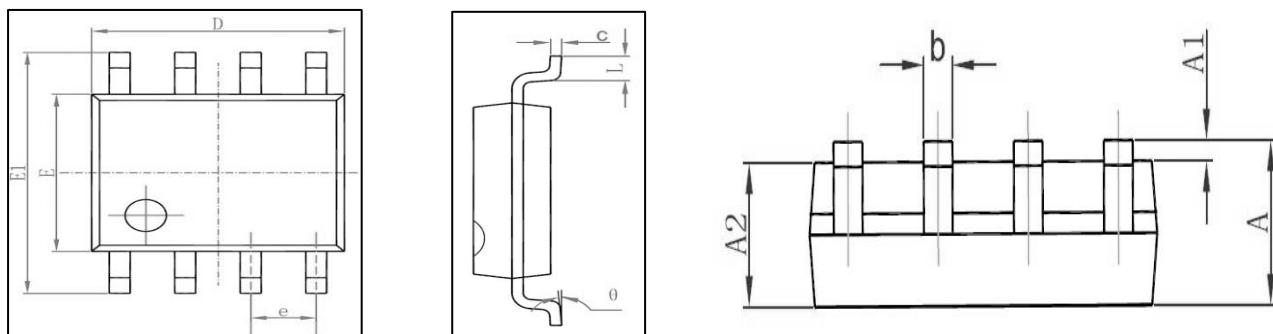


**Fig.5 Normalized  $V_{GS(th)}$  vs.  $T_J$**



**Fig.6 Normalized  $R_{DSON}$  vs.  $T_J$**

**Ratings and Characteristic Curves**


**Package Outline Dimensions Millimeters**
**SOP-8**


Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	1.350	1.750	0.053	0.069
A1	0.100	0.250	0.004	0.010
A2	1.350	1.550	0.053	0.061
b	0.330	0.510	0.013	0.020
c	0.170	0.250	0.006	0.010
D	4.700	5.100	0.185	0.200
E	3.800	4.000	0.150	0.157
E1	5.800	6.200	0.228	0.244
e	1.270 (BSC)		0.050 (BSC)	
L	0.400	1.270	0.016	0.050
$\theta$	0°	8°	0°	8°

