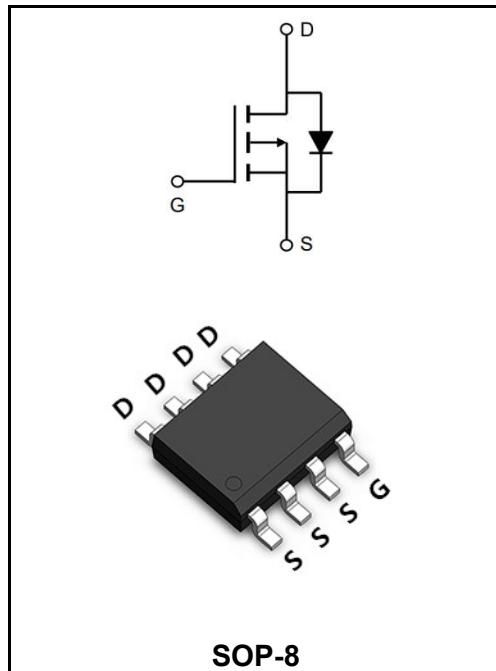


**-60V P-CHANNEL ENHANCEMENT MODE MOSFET**
**MAIN CHARACTERISTICS**

$I_D$	-20A
$V_{DSS}$	-60V
$R_{DS(on)-typ}(@V_{GS}=-10V)$	<-25mΩ (Type: 22 mΩ)


**Application**

- Battery protection
- Load switch
- Uninterruptible power supply

**Product Specification Classification**

Part Number	Package	Marking	Pack
YFW20P06S	SOP-8	YFW 20P06S XXXXX	3000PCS/Tape

**Maximum Ratings at  $T_c=25^\circ\text{C}$  unless otherwise specified**

Characteristics	Symbols	Value	Units
Drain-Source Voltage	$V_{DS}$	-60	V
Gate - Source Voltage	$V_{GS}$	$\pm 20$	V
Continuous Drain Current, $V_{GS} @ -10V^1$ @ $T_c=25^\circ\text{C}$	$I_D$	-20	A
Continuous Drain Current, $V_{GS} @ -10V^1$ @ $T_c=100^\circ\text{C}$	$I_D$	-8.5	A
Pulsed Drain Current <sup>2</sup>	$I_{DM}$	-40	A
Single Pulse Avalanche Energy <sup>3</sup>	$E_{AS}$	113	mJ
Avalanche Current	$I_{AS}$	47.6	A
Total Power Dissipation <sup>4</sup> @ $T_c=25^\circ\text{C}$	$P_D$	52.1	W
Storage Temperature Range	$T_{STG}$	-55 to +150	°C
Operating Junction Temperature Range	$T_J$	-55 to +150	°C
Thermal Resistance Junction-Ambient <sup>1</sup>	$R_{\theta JA}$	62	°C/W
Thermal Resistance Junction to Case <sup>1</sup>	$R_{\theta JC}$	2.4	°C/W

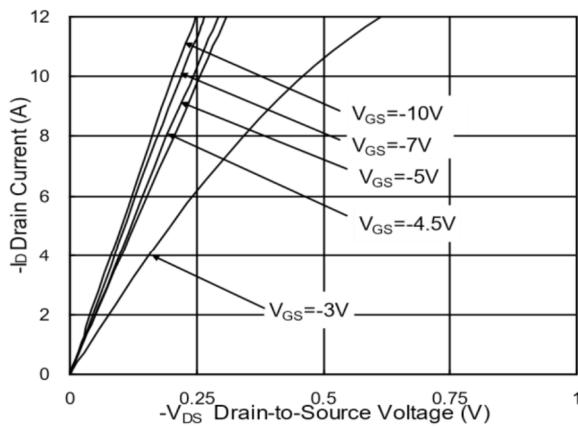
**Maximum Ratings at Tc=25°C unless otherwise specified**

Characteristics	Test Condition	Symbols	Min	Typ	Max	Units
Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V, I <sub>D</sub> =-250μA	BV <sub>DSS</sub>	-60	-	-	V
BVDSS Temperature Coefficient	Reference to 25°C , I <sub>D</sub> =-1mA	ΔBV <sub>DSS/ΔTJ</sub>	-	-0.035	-	V/°C
Static Drain-Source On-Resistance <sup>2</sup>	V <sub>GS</sub> =-10V, I <sub>D</sub> =-18A	R <sub>DS(ON)</sub>	-	22	25	mΩ
	V <sub>GS</sub> =-4.5V, I <sub>D</sub> =-12A		-	28	33	
Gate -Threshold Voltage	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =-250μA	V <sub>GS(th)</sub>	-1.0	-	-2.5	V
V <sub>GS(th)</sub> Temperature Coefficient		ΔV <sub>GS(th)</sub>	-	4.28	-	mV/°C
Drain-Source Leakage Current	V <sub>DS</sub> =-48V , V <sub>GS</sub> =0V , T <sub>J</sub> =25°C	I <sub>DSS</sub>	-	-	1	μA
	V <sub>DS</sub> =-48V , V <sub>GS</sub> =0V , T <sub>J</sub> =55°C		-	-	5	
Gate -Source Leakage Current	V <sub>GS</sub> =±20V, V <sub>DS</sub> =0V	I <sub>GSS</sub>	-	-	±100	nA
Forward Transconductance	V <sub>DS</sub> =-10V , I <sub>D</sub> =-18A	g <sub>fs</sub>	-	23	-	S
Gate Resistance	V <sub>DS</sub> =0V , V <sub>GS</sub> =0V , f=1MHz	R <sub>g</sub>	-	7	-	Ω
Total Gate Charge(-4.5V)	V <sub>DS</sub> =-20V V <sub>GS</sub> =-4.5V I <sub>D</sub> =-12A	Q <sub>g</sub>	-	25	-	nC
Gate-Source Charge		Q <sub>gs</sub>	-	6.7	-	
Gate-Drain Charge		Q <sub>gd</sub>	-	5.5	-	
Turn-on delay time	V <sub>DD</sub> =-15V V <sub>GS</sub> =-10V I <sub>D</sub> = -1A R <sub>G</sub> =3.3	t <sub>d(on)</sub>	-	38	-	ns
Rise Time		T <sub>r</sub>	-	23.6	-	
Turn-Off Delay Time		t <sub>d(OFF)</sub>	-	100	-	
Fall Time		t <sub>f</sub>	-	6.8	-	
Input Capacitance	V <sub>DS</sub> =-15V V <sub>GS</sub> =0V f=1MHz	C <sub>iss</sub>	-	3635	-	pF
Output Capacitance		C <sub>oss</sub>	-	224	-	
Reverse Transfer Capacitance		C <sub>rss</sub>	-	141	-	
Continuous Source Current <sup>1,5</sup>	V <sub>G</sub> =V <sub>D</sub> =0V , Force Current	I <sub>s</sub>	-	-	-35	A
Pulsed Source Current <sup>2,5</sup>		I <sub>SM</sub>	-	-	-70	A
Diode Forward Voltage <sup>2</sup>	V <sub>GS</sub> =0V , I <sub>s</sub> =-1A , T <sub>J</sub> =25°C	V <sub>SD</sub>	-	-	-1	V

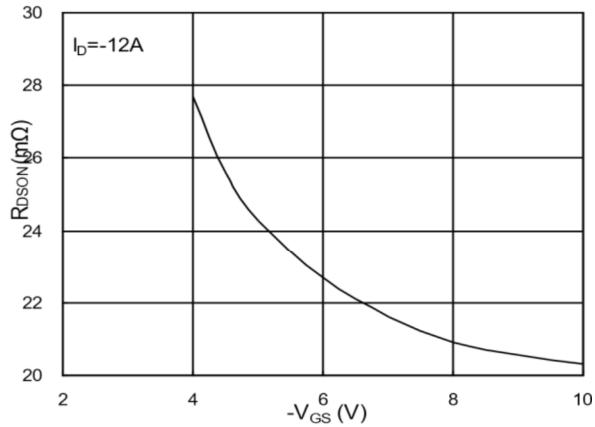
Note :

- 1.The data tested by surface mounted on a 1 inch<sup>2</sup> FR-4 board with 2OZ copper.
- 2.The data tested by pulsed , pulse width ≤ 300us , duty cycle ≤ 2%
- 3.The EAS data shows Max. rating . The test condition is VDD=-25V,VGS=-10V,L=0.1mH,IAS=-47.6A
- 4.The power dissipation is limited by 150°C junction temperature
- 5.The data is theoretically the same as ID and IDM , in real applications , should be limited by total power dissipation.

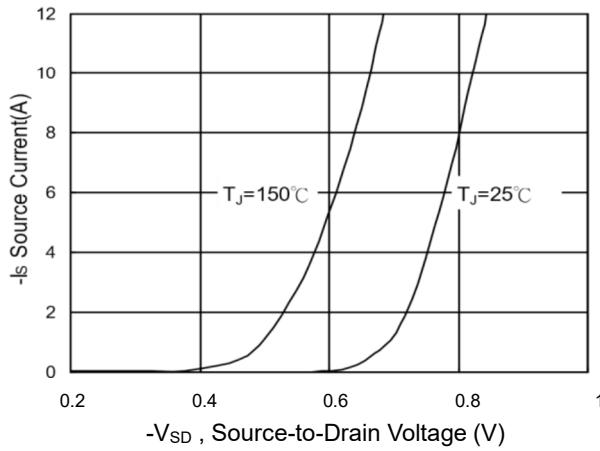
**Ratings and Characteristic Curves**



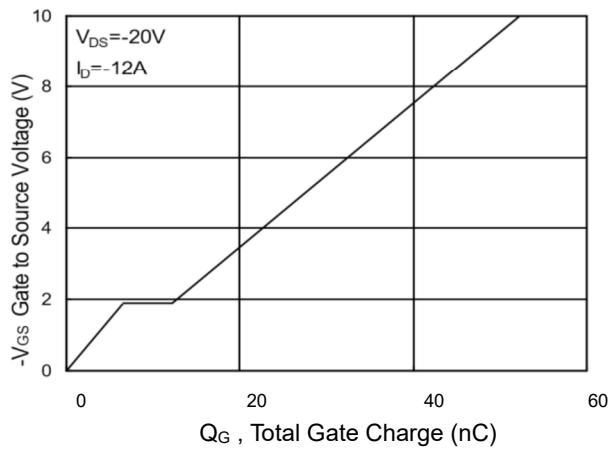
**Fig.1 Typical Output Characteristics**



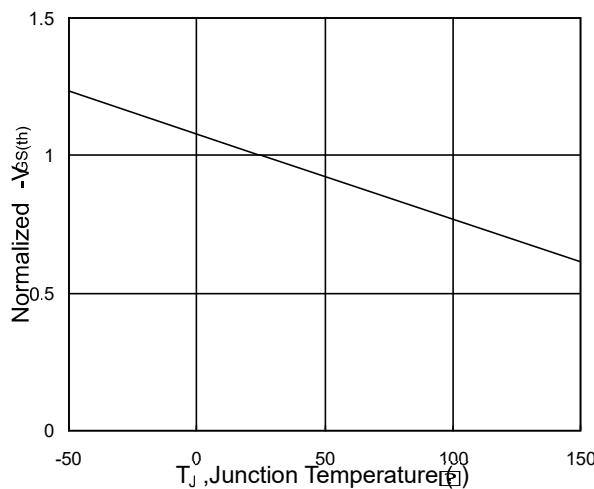
**Fig.2 On-Resistance v.s Gate-Source**



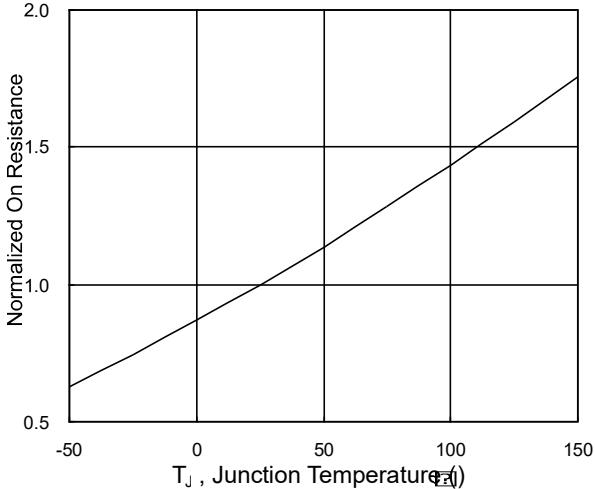
**Fig.3 Forward Characteristics Of Reverse**



**Fig.4 Gate-Charge Characteristics**

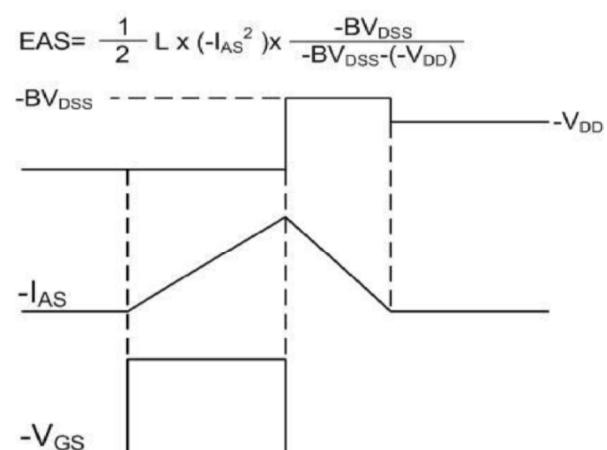
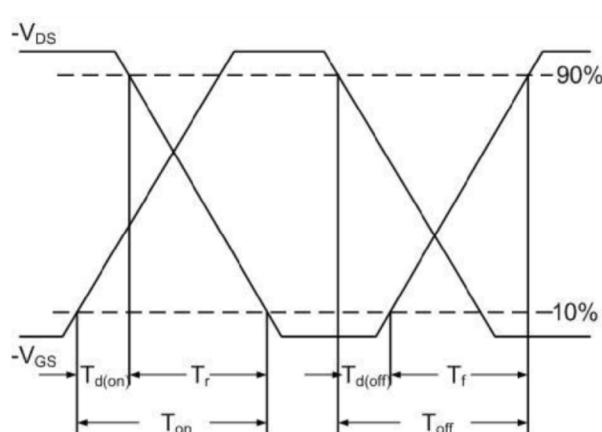
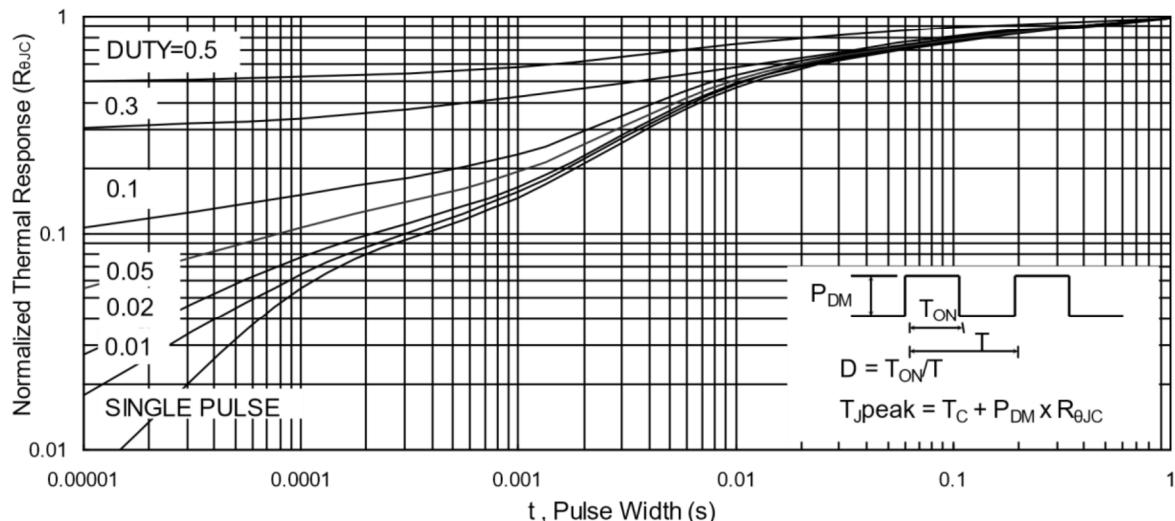
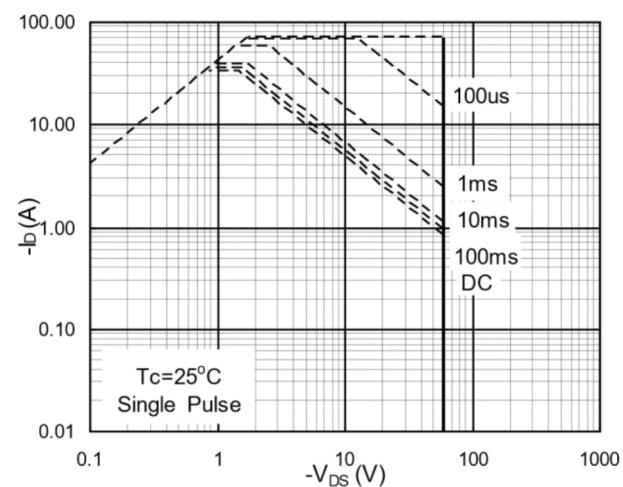
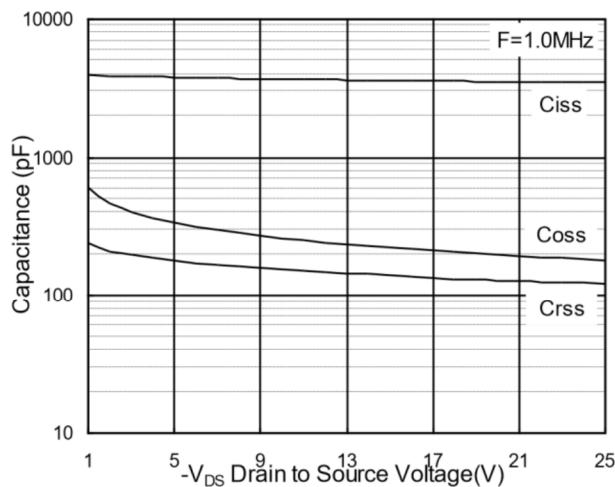


**Fig.5 Normalized  $V_{GS(th)}$  v.s  $T_J$**



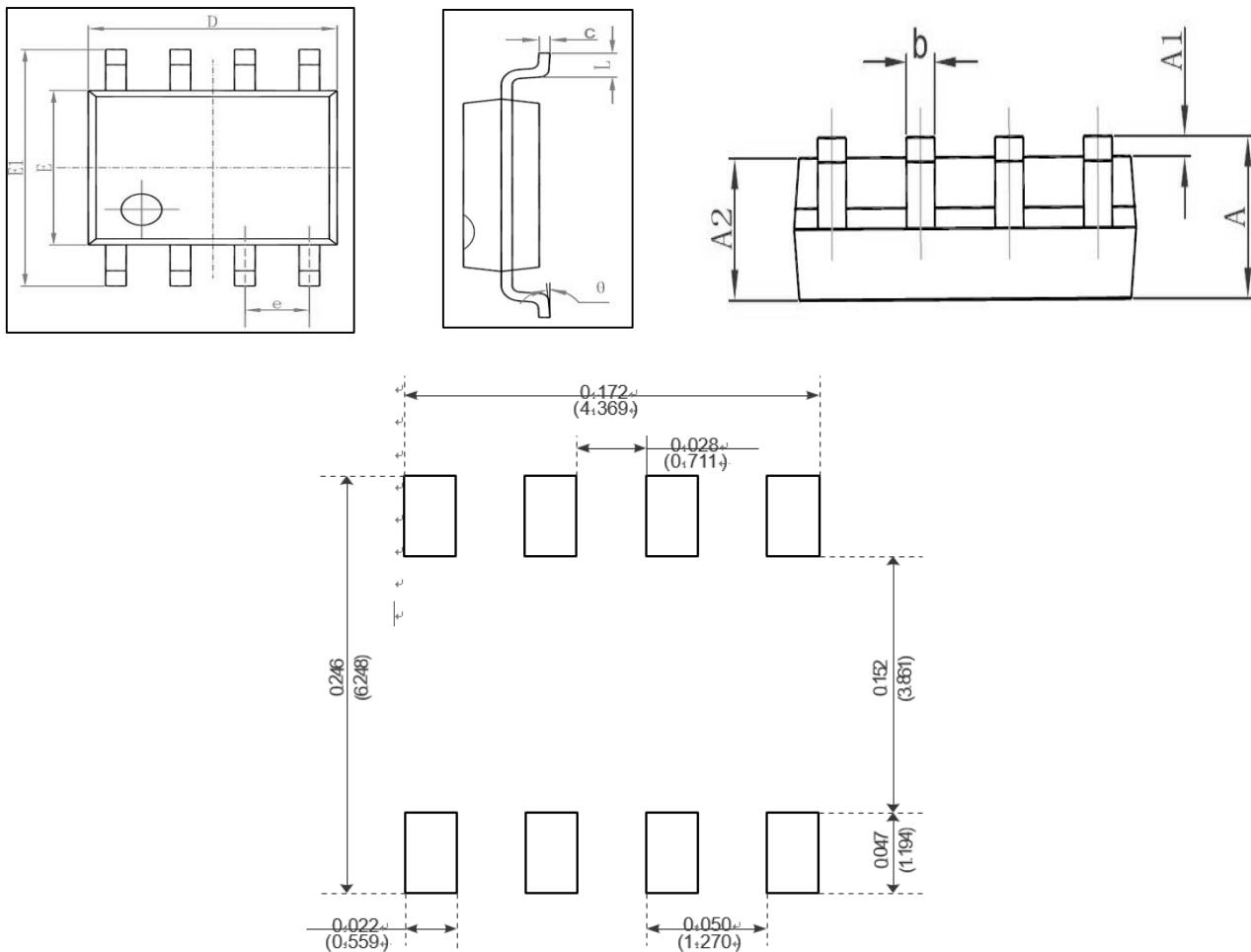
**Fig.6 Normalized  $R_{DS(on)}$  v.s  $T_J$**

**Ratings and Characteristic Curves**



**Package Outline Dimensions Millimeters**

**SOP-8**



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	1.350	1.750	0.053	0.069
A1	0.100	0.250	0.004	0.010
A2	1.350	1.550	0.053	0.061
b	0.330	0.510	0.013	0.020
c	0.170	0.250	0.006	0.010
D	4.700	5.100	0.185	0.200
E	3.800	4.000	0.150	0.157
E1	5.800	6.200	0.228	0.244
e	1.270 (BSC)		0.050 (BSC)	
L	0.400	1.270	0.016	0.050
$\theta$	0°	8°	0°	8°