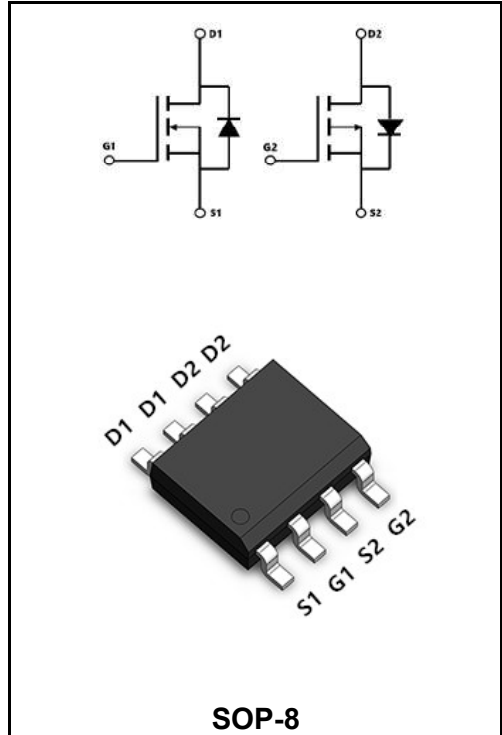


**30V N+P-CHANNEL ENHANCEMENT MODE MOSFET**
**MAIN CHARACTERISTICS**

$I_D$	6.2A
$V_{DSS}$	30V
$R_{DS(on)-typ}(@V_{GS}=10V)$	< 25mΩ (Type:18 mΩ)
$I_D$	-4.8A
$V_{DSS}$	-30V
$R_{DS(on)-typ}(@V_{GS}=-10V)$	< 60mΩ (Type:48 mΩ)


**Application**

- ◆ Lithium battery protection
- ◆ Wireless impact
- ◆ Mobile phone fast charging

**Product Specification Classification**

Part Number	Package	Marking	Pack
YFW4606S	SOP-8	YFW 4606S XXXXX	3000PCS/Tape

**Maximum Ratings at Tc=25°C unless otherwise specified**

Characteristics	Symbols	Value		Units
		N-Ch	P-Ch	
Drain-Source Voltage	$V_{DS}$	30	-30	V
Gate - Source Voltage	$V_{GS}$	±20	±20	V
Continuous Drain Current, $V_{GS} @ 10V^1 @ T_A=25^{\circ}C$	$I_D$	6.2	-4.8	A
Continuous Drain Current, $V_{GS} @ 10V^1 @ T_A=70^{\circ}C$	$I_D$	5	-3.8	A
Pulsed Drain Current <sup>2</sup>	$I_{DM}$	24	-24	A
Single Pulse Avalanche Energy <sup>3</sup>	$E_{AS}$	26.6	37	mJ
Avalanche Current	$I_{AS}$	12.7	15	A
Total Power Dissipation <sup>4</sup> @ $T_A=25^{\circ}C$	$P_D$	1.5	1.5	W
Storage Temperature Range	$T_{STG}$	-55 to +150		°C
Operating Junction Temperature Range	$T_J$	-55 to +150		°C
Thermal Resistance Junction-Ambient <sup>1</sup>	$R_{\theta JA}$	85		°C/W
Thermal Resistance Junction-Case <sup>1</sup>	$R_{\theta JC}$	60		°C/W

**N-Channel Electrical Characteristics (T<sub>J</sub>=25 °C, unless otherwise noted)**

Characteristics	Test Condition	Symbols	Min	Typ	Max	Units
Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V, I <sub>D</sub> =250uA	<b>BV<sub>DSS</sub></b>	30	31.5	-	<b>V</b>
BVDSS Temperature Coefficient	Reference to 25 °C , I <sub>D</sub> =1mA	<b>ΔBV<sub>DSS</sub>/ΔT<sub>J</sub></b>	-	0.023	-	<b>V/°C</b>
Static Drain-Source On-Resistance <sup>2</sup>	V <sub>GS</sub> =10V, I <sub>D</sub> =5A	<b>R<sub>DS(ON)</sub></b>	-	18	25	<b>mΩ</b>
	V <sub>GS</sub> =4.5V, I <sub>D</sub> =3A		-	24	40	
Gate Threshold Voltage	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250uA	<b>V<sub>GS(th)</sub></b>	1.0	1.6	2.5	<b>V</b>
V <sub>GS(th)</sub> Temperature Coefficient		<b>ΔV<sub>GS(th)</sub></b>	-	-4.2	-	<b>mV/°C</b>
Drain-Source Leakage Current	V <sub>DS</sub> =24V, V <sub>GS</sub> =0V T <sub>J</sub> =25°C	<b>I<sub>DSS</sub></b>	-	-	1	<b>uA</b>
	V <sub>DS</sub> =24V, V <sub>GS</sub> =0V, T <sub>J</sub> =55°C		-	-	5	
Gate-Source Leakage Current	V <sub>GS</sub> =±20V, V <sub>DS</sub> =0V	<b>I<sub>GSS</sub></b>	-	-	±100	<b>nA</b>
Forward Transconductance	V <sub>DS</sub> = 5V, I <sub>D</sub> = 6A	<b>g<sub>fs</sub></b>	-	5.8	-	<b>S</b>
Gate Resistance	V <sub>DS</sub> =0V, V <sub>GS</sub> =0V, f=1MHz	<b>R<sub>g</sub></b>	-	2.3	-	<b>Ω</b>
Total Gate Charge(4.5V)	V <sub>DS</sub> =20V V <sub>GS</sub> =4.5V I <sub>D</sub> =6A	<b>Q<sub>g</sub></b>	-	5	-	<b>nC</b>
Gate-Source Charge		<b>Q<sub>gs</sub></b>	-	1.11	-	
Gate-Drain Charge		<b>Q<sub>gd</sub></b>	-	2.61	-	
Turn-on delay time	V <sub>DD</sub> =12V V <sub>GS</sub> =10V R <sub>G</sub> = 3.3Ω I <sub>D</sub> = 6A	<b>t<sub>d(on)</sub></b>	-	7.7	-	<b>ns</b>
Rise Time		<b>T<sub>r</sub></b>	-	46	-	
Turn-Off Delay Time		<b>t<sub>d(OFF)</sub></b>	-	11	-	
Fall Time		<b>t<sub>f</sub></b>	-	3.6	-	
Input Capacitance	V <sub>DS</sub> =15V V <sub>GS</sub> =0V f=1MHz	<b>C<sub>iss</sub></b>	-	416	-	<b>pF</b>
Output Capacitance		<b>C<sub>oss</sub></b>	-	62	-	
Reverse Transfer Capacitance		<b>C<sub>rss</sub></b>	-	51	-	
Continuous Source Current <sup>1,5</sup>	V <sub>G</sub> =V <sub>D</sub> =0V, Force Current	<b>I<sub>S</sub></b>	-	-	6.2	<b>A</b>
Pulsed Source Current <sup>2,5</sup>		<b>I<sub>SM</sub></b>	-	-	24	<b>A</b>
Diode Forward Voltage <sup>2</sup>	V <sub>GS</sub> =0V, I <sub>S</sub> =1A, T <sub>J</sub> =25°C	<b>V<sub>SD</sub></b>	-	-	1.2	<b>V</b>

Note :

- 1、 The data tested by surface mounted on a 1 inch<sup>2</sup> FR-4 board with 2OZ copper.
- 2、 The data tested by pulsed , pulse width ≅ 300us , duty cycle ≅ 2%
- 3、 The power dissipation is limited by 150°C junction temperature
- 4、 The data is theoretically the same as I<sub>D</sub> and I<sub>DM</sub> , in real applications , should be limited by total power dissipation.

**P-Channel Electrical Characteristics (T<sub>J</sub>=25 °C, unless otherwise noted)**

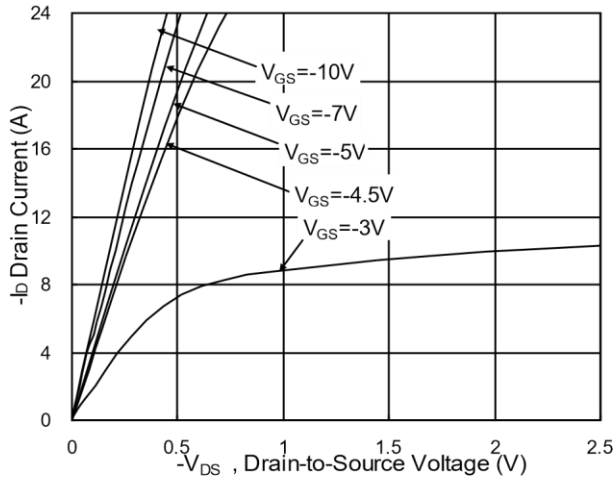
Characteristics	Test Condition	Symbols	Min	Typ	Max	Units
Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V, I <sub>D</sub> =-250uA	<b>BV<sub>DSS</sub></b>	-30	-32	-	<b>V</b>
BVDSS Temperature Coefficient	Reference to 25°C, I <sub>D</sub> =-1mA	<b>ΔBV<sub>DSS</sub>/ΔT<sub>J</sub></b>	-	-0.02	-	<b>V/°C</b>
Static Drain-Source On-Resistance <sup>2</sup>	V <sub>GS</sub> =-10V, I <sub>D</sub> =-4.1A	<b>R<sub>DS(ON)</sub></b>	-	42	55	<b>mΩ</b>
	V <sub>GS</sub> =-4.5V, I <sub>D</sub> =-3.5A		-	68	85	<b>mΩ</b>
Gate Threshold Voltage	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =-250uA	<b>V<sub>GS(th)</sub></b>	-1.0	-1.7	-2.5	<b>V</b>
V <sub>GS(th)</sub> Temperature Coefficient		<b>ΔV<sub>GS(th)</sub></b>	-	4.32	-	<b>mV/°C</b>
Drain-Source Leakage Current	V <sub>DS</sub> =-24V, V <sub>GS</sub> =0V, T <sub>J</sub> =25°C	<b>I<sub>DSS</sub></b>	-	-	-1	<b>uA</b>
	V <sub>DS</sub> =-24V, V <sub>GS</sub> =0V, T <sub>J</sub> =55°C		-	-	-5	
Gate-Source Leakage Current	V <sub>GS</sub> =±20V, V <sub>DS</sub> =0V	<b>I<sub>GSS</sub></b>	-	-	±100	<b>nA</b>
Forward Transconductance	V <sub>DS</sub> = -5V, I <sub>D</sub> = -3A	<b>g<sub>fs</sub></b>	-	4.7	-	<b>S</b>
Gate Resistance	V <sub>DS</sub> =0V, V <sub>GS</sub> =0V, f=1MHz	<b>R<sub>g</sub></b>	-	24	-	<b>Ω</b>
Total Gate Charge(-4.5V)	V <sub>DS</sub> =-20V V <sub>GS</sub> =-4.5V I <sub>D</sub> =-5A	<b>Q<sub>g</sub></b>	-	5.22	-	<b>nC</b>
Gate-Source Charge		<b>Q<sub>gs</sub></b>	-	1.25	-	
Gate-Drain Charge		<b>Q<sub>gd</sub></b>	-	2.3	-	
Turn-on delay time	V <sub>DD</sub> =-15V V <sub>GS</sub> =-10V R <sub>G</sub> = 3.3Ω I <sub>D</sub> =-1A	<b>t<sub>d(on)</sub></b>	-	18.4	-	<b>ns</b>
Rise Time		<b>T<sub>r</sub></b>	-	11.4	-	
Turn-Off Delay Time		<b>t<sub>d(OFF)</sub></b>	-	39.4	-	
Fall Time		<b>t<sub>f</sub></b>	-	5.2	-	
Input Capacitance	V <sub>DS</sub> =-15V V <sub>GS</sub> =0V f=1MHz	<b>C<sub>iss</sub></b>	-	463	-	<b>pF</b>
Output Capacitance		<b>C<sub>oss</sub></b>	-	82	-	
Reverse Transfer Capacitance		<b>C<sub>rss</sub></b>	-	68	-	
Continuous Source Current <sup>1,5</sup>	V <sub>G</sub> =V <sub>D</sub> =0V, Force Current	<b>I<sub>S</sub></b>	-	-	-4	<b>A</b>
Pulsed Source Current <sup>2,5</sup>		<b>I<sub>SM</sub></b>	-	-	-24	<b>A</b>
Diode Forward Voltage <sup>2</sup>	V <sub>GS</sub> =0V, I <sub>S</sub> =-1A, T <sub>J</sub> =25°C	<b>V<sub>SD</sub></b>	-	-	-1	<b>V</b>

Note :

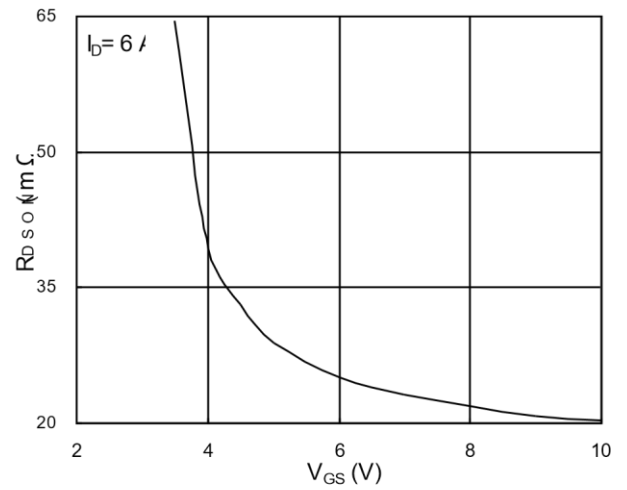
- 1、 The data tested by surface mounted on a 1 inch FR-4 board with 2OZ copper.
- 2、 The data tested by pulsed , pulse width ≅ 300us , duty cycle ≅ 2%
- 3、 The power dissipation is limited by 150°C junction temperature
- 4、 The data is theoretically the same as I<sub>D</sub> and I<sub>DM</sub> , in real applications , should be limited by total power dissipation.

**Ratings and Characteristic Curves**

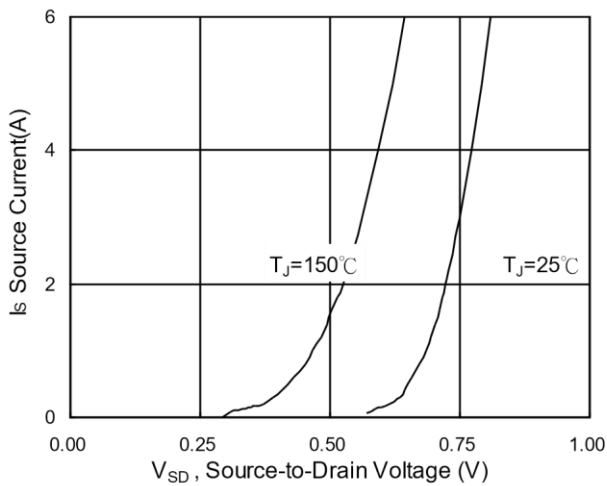
**N-Channel Typical Characteristics**



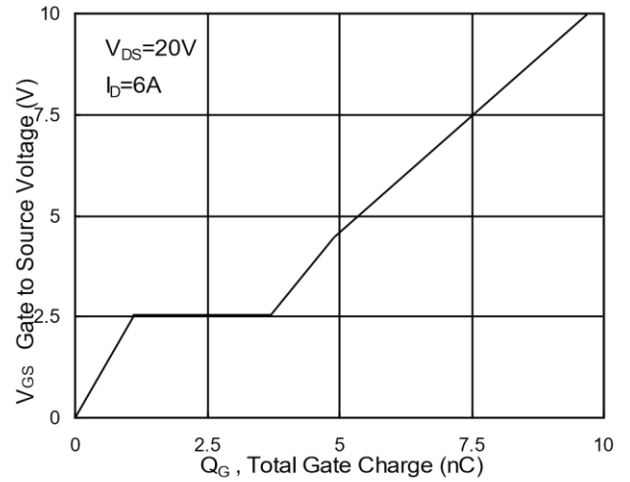
**Fig.1 Typical Output Characteristics**



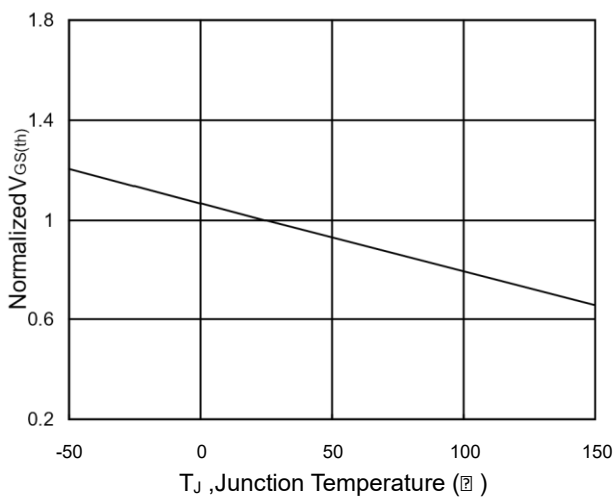
**Fig.2 On-Resistance vs. Gate-Source**



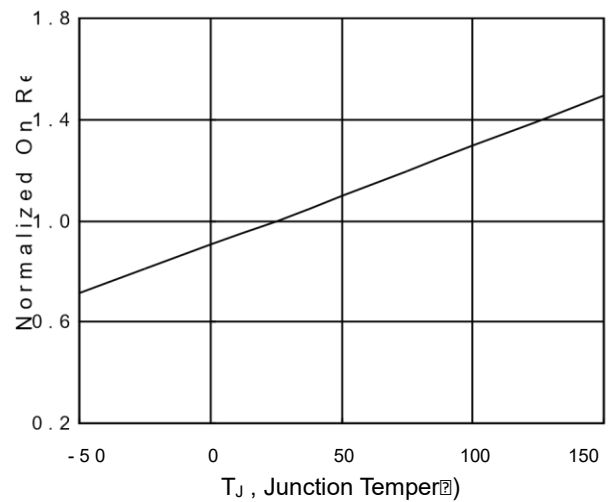
**Fig.3 Forward Characteristics Of Reverse**



**Fig.4 Gate-Charge Characteristics**

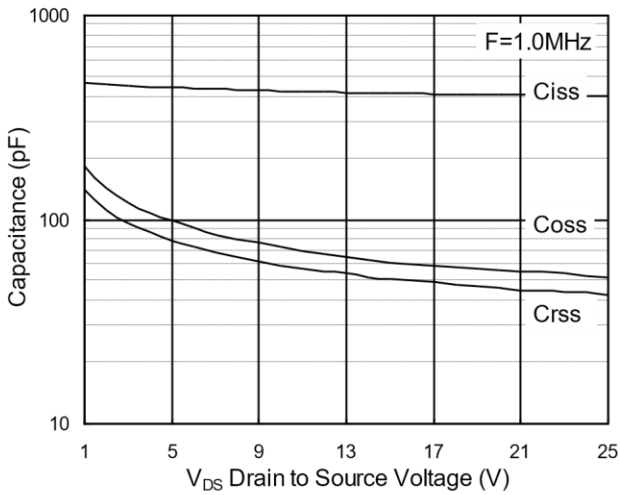


**Fig.5 Normalized  $V_{GS(th)}$  vs.  $T_J$**

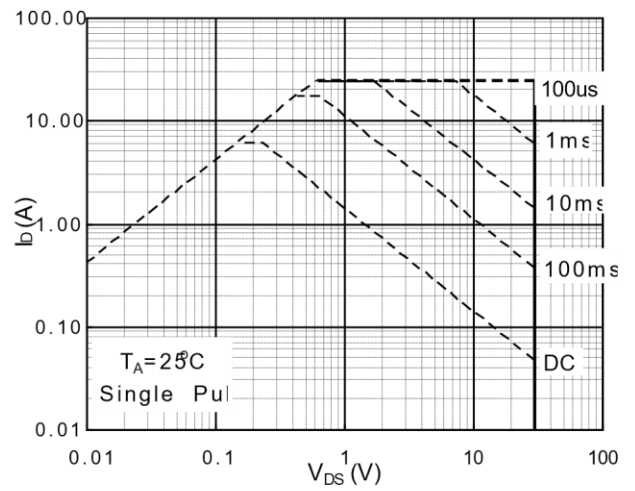


**Fig.6 Normalized  $R_{DS(on)}$  vs.  $T_J$**

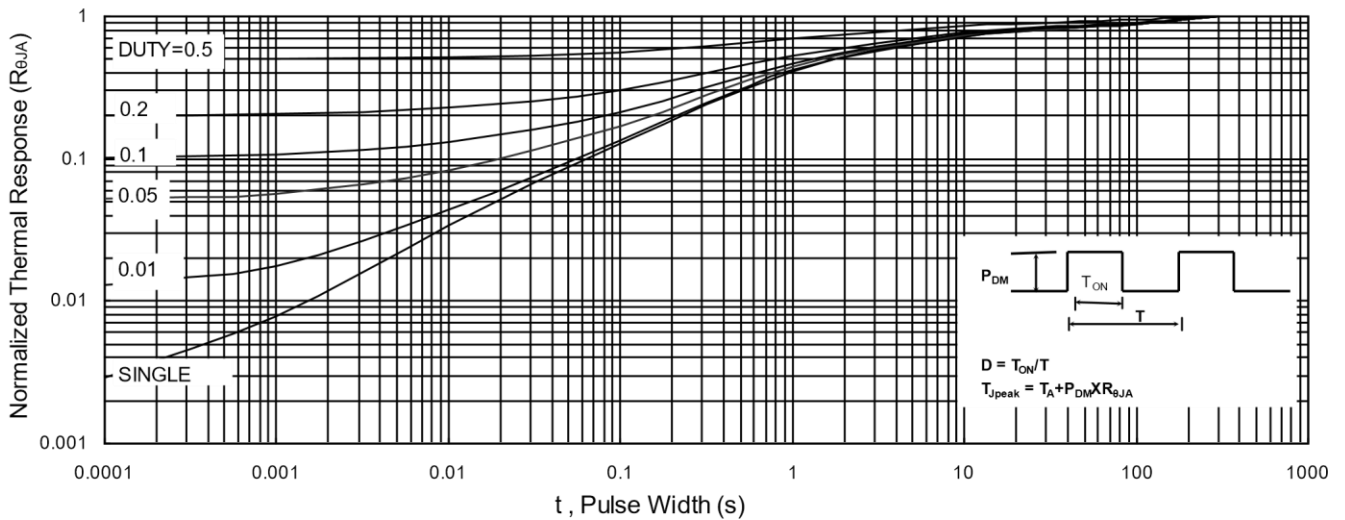
**Ratings and Characteristic Curves**



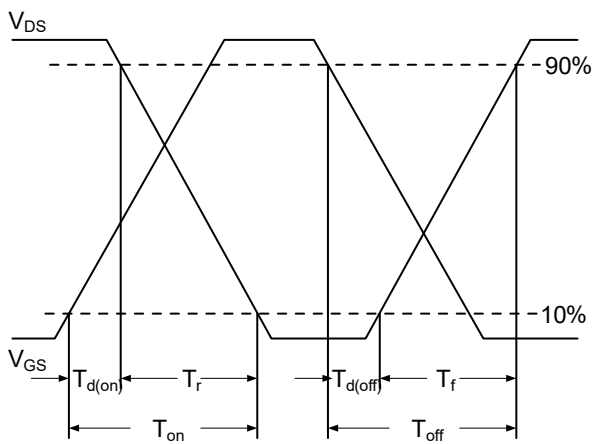
**Fig.7 Capacitance**



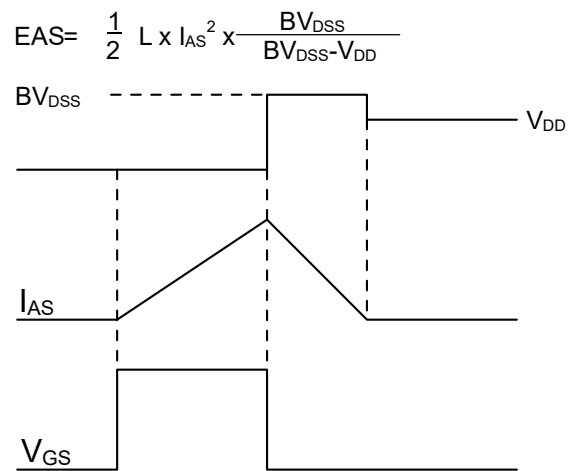
**Fig.8 Safe Operating Area**



**Fig.9 Normalized Maximum Transient Thermal Impedance**

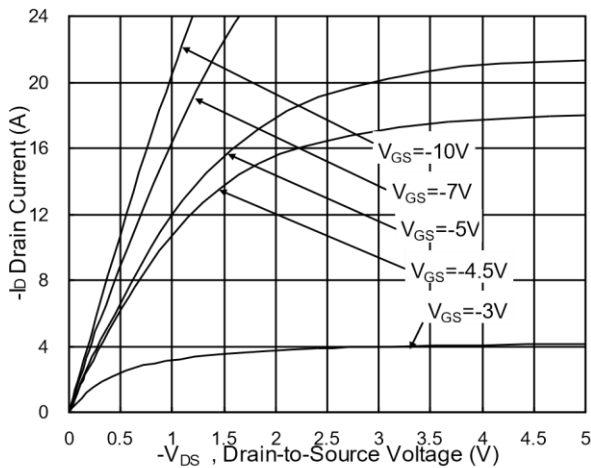


**Fig.10 Switching Time Waveform**

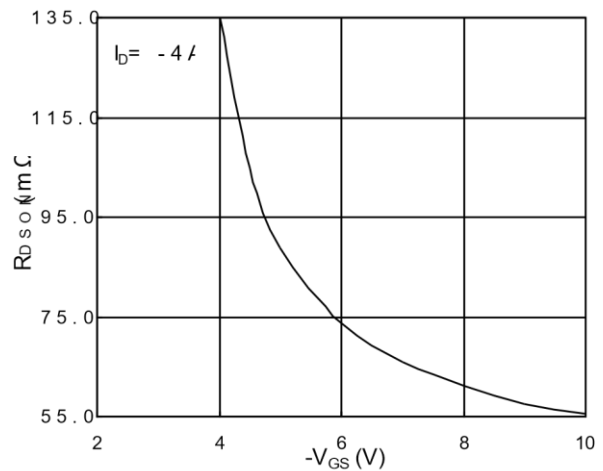


**Fig.11 Unclamped Inductive Switching Waveform**

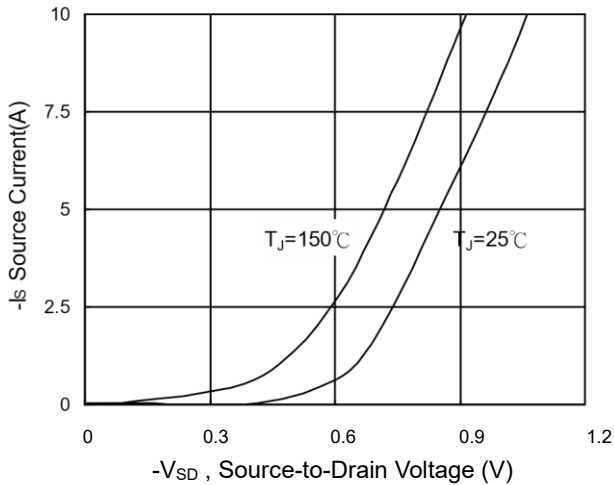
**P-Channel Typical Characteristics**



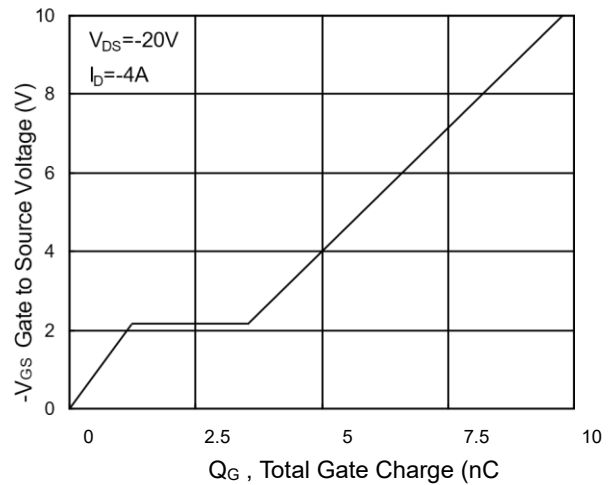
**Fig.1 Typical Output Characteristics**



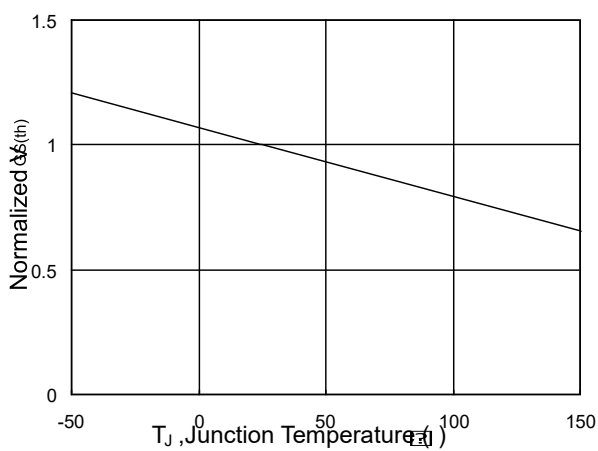
**Fig.2 On-Resistance vs. G-S Voltage**



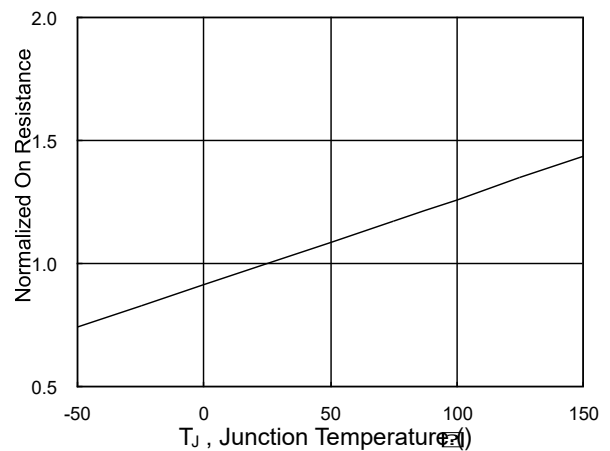
**Fig.3 Forward Characteristics of Reverse**



**Fig.4 Gate-Charge Characteristics**

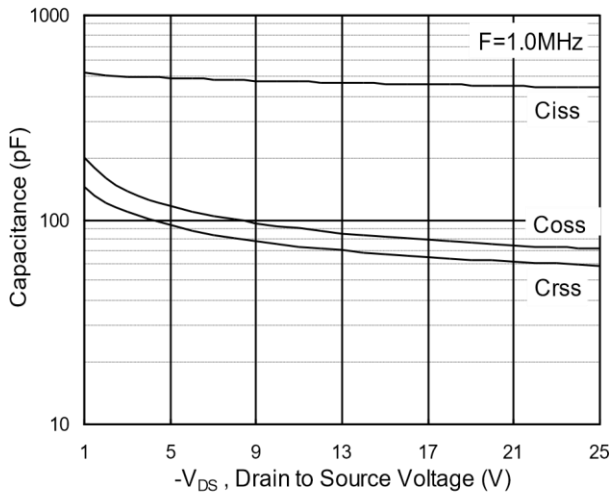


**Fig.5 Normalized  $V_{GS(th)}$  vs.  $T_J$**

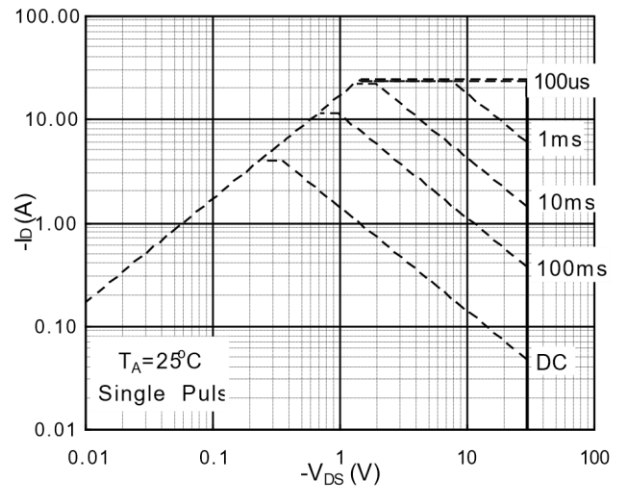


**Fig.6 Normalized  $R_{DS(on)}$  vs.  $T_J$**

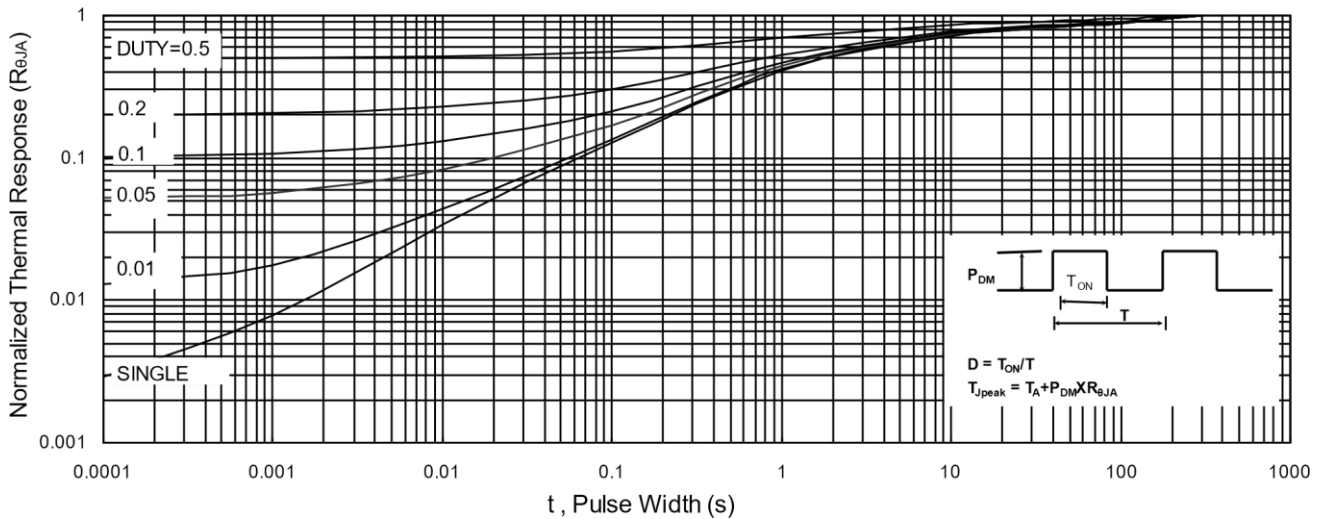
**Ratings and Characteristic Curves**



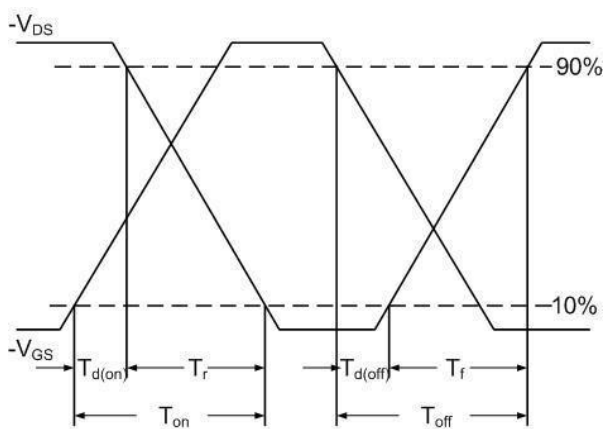
**Fig.7 Capacitance**



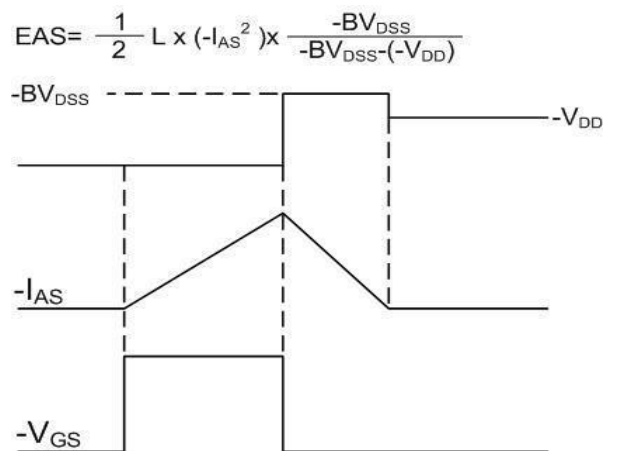
**Fig.8 Safe Operating Area**



**Fig.9 Normalized Maximum Transient Thermal Impedance**

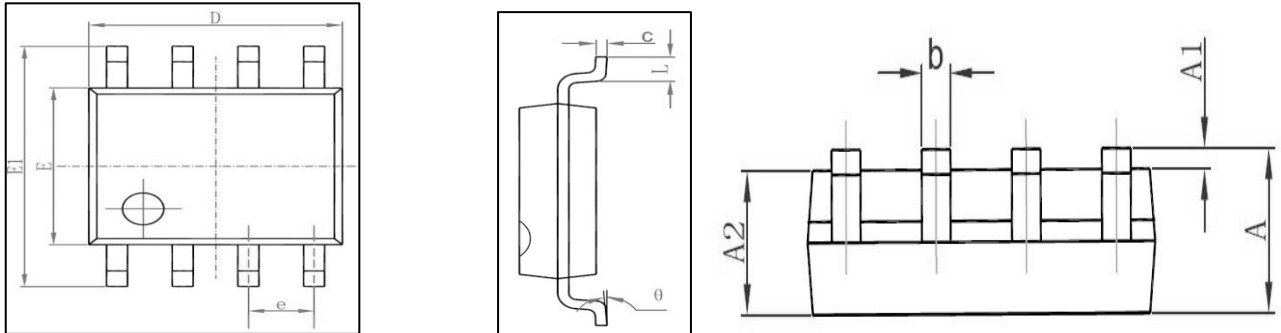


**Fig.10 Switching Time Waveform**

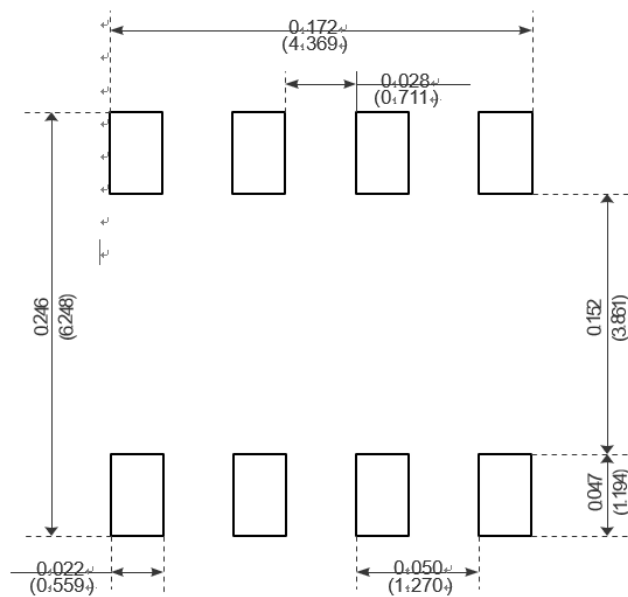


**Fig.11 Unclamped Inductive Switching Waveform**

**SOP-8**



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	1.350	1.750	0.053	0.069
A1	0.100	0.250	0.004	0.010
A2	1.350	1.550	0.053	0.061
b	0.330	0.510	0.013	0.020
c	0.170	0.250	0.006	0.010
D	4.700	5.100	0.185	0.200
E	3.800	4.000	0.150	0.157
E1	5.800	6.200	0.228	0.244
e	1.270 (BSC)		0.050 (BSC)	
L	0.400	1.270	0.016	0.050
θ	0°	8°	0°	8°



Recommended Minimum Pads