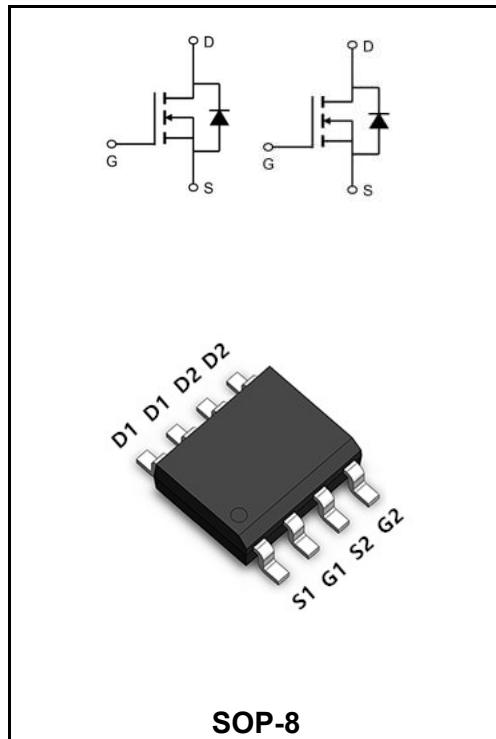


60V N+N-CHANNEL ENHANCEMENT MODE MOSFET
MAIN CHARACTERISTICS

I_D	8A
V_{DSS}	60V
$R_{DS(on)-typ}(@V_{GS}=10V)$	< 32mΩ (Type: 23 mΩ)


Application

- Battery protection
- Load switch
- Uninterruptible power supply

Product Specification Classification

Part Number	Package	Marking	Pack
YFW8H06S	SOP-8	YFW 8H06S XXXXX	3000PCS/Tape

Maximum Ratings at $T_c=25^\circ\text{C}$ unless otherwise specified

Characteristics	Symbols	Value	Units
Drain-Source Voltage	V_{DS}	60	V
Gate - Source Voltage	V_{GS}	± 20	V
Continuous Drain Current, $V_{GS} @ 10V^1 @ T_A=25^\circ\text{C}$	I_D	8.2	A
Continuous Drain Current, $V_{GS} @ 10V^1 @ T_A=70^\circ\text{C}$	I_D	5.8	A
Pulsed Drain Current ²	I_{DM}	16.6	A
Single Pulse Avalanche Energy ³	E_{AS}	28.5	mJ
Avalanche Current	I_{AS}	22.6	A
Total Power Dissipation ⁴ @ $T_A=25^\circ\text{C}$	P_D	1.5	W
Storage Temperature Range	T_{STG}	-55 to +150	°C
Operating Junction Temperature Range	T_J	-55 to +150	°C
Thermal Resistance Junction-Ambient ¹	$R_{\theta JA}$	85	°C/W
Thermal Resistance Junction-Case ¹	$R_{\theta JC}$	36	°C/W

Maximum Ratings at Tc=25°C unless otherwise specified

Characteristics	Test Condition	Symbols	Min	Typ	Max	Units
Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =250uA	B _{VDSS}	60	66	-	V
BVDSS Temperature Coefficient	Reference to 25°C , I _D =1mA	ΔB _{VDSS/ΔTJ}	-	0.063	-	V/°C
Static Drain-Source On-Resistance ²	V _{GS} =10V, I _D =8A	R _{DS(ON)}	-	23	32	mΩ
	V _{GS} =4.5V, I _D =6A		-	28	38	
Gate Threshold Voltage	V _{DS} =V _{GS} , I _D =250uA	V _{GS(th)}	1.2	1.6	2.5	V
V _{GS(th)} Temperature Coefficient		ΔV _{GS(th)}	-	-5.24	-	mV/°C
Drain-Source Leakage Current	V _{DS} =48V, V _{GS} =0V T _J =25°C	I _{DSS}	-	-	1	uA
	V _{DS} =48V , V _{GS} =0V , T _J =55°C		-	-	5	
Gate-Source Leakage Current	V _{GS} =±20V, V _{DS} =0V	I _{GSS}	-	-	±100	nA
Forward Transconductance	V _{DS} = 5V, I _D = 4A	g _{fs}	-	21	-	S
Gate Resistance	V _{DS} =0V , V _{GS} =0V , f=1MHz	R _g		3.2	6.4	Ω
Total Gate Charge (4.5V)	V _{DS} =48V V _{GS} =4.5V I _D =4A	Q _g	-	12.6	-	nC
Gate-Source Charge		Q _{gs}	-	3.2	-	
Gate-Drain Charge		Q _{gd}	-	6.3	-	
Turn-on delay time	V _{DD} =30V V _{GS} =10V R _G = 3.3Ω I _D = 4A	t _{d(on)}	-	8	-	ns
Rise Time		T _r	-	14.2	-	
Turn-Off Delay Time		t _{d(OFF)}	-	24.4	-	
Fall Time		t _f	-	4.6	-	
Input Capacitance	V _{DS} =15V V _{GS} =0V f=1.0MHz	C _{iss}	-	1378	-	pF
Output Capacitance		C _{oss}	-	86	-	
Reverse Transfer Capacitance		C _{rss}	-	64	-	
Continuous Source Current ^{1,5}	V _G =V _D =0V , Force Current	I _s	-	-	4.8	A
Pulsed Source Current ^{2,5}		I _{SM}	-	-	9.6	A
Diode Forward Voltage ²	V _{GS} =0V , I _s =1A , T _J =25°C	V _{SD}	-	0.746	1.2	V

Note :

- 1.The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper.
- 2.The data tested by pulsed , pulse width ≤ 300us , duty cycle ≤ 2%
- 3.The EAS data shows Max. rating . The test condition is V_{DD}=25V,V_{GS}=10V,L=0.1mH,I_{AS}=22.6A
- 4.The power dissipation is limited by 150°C junction temperature
- 5.The data is theoretically the same as I_D and I_{DM} , in real applications , should be limited by total power dissipation.

Ratings and Characteristic Curves

Typical Characteristics

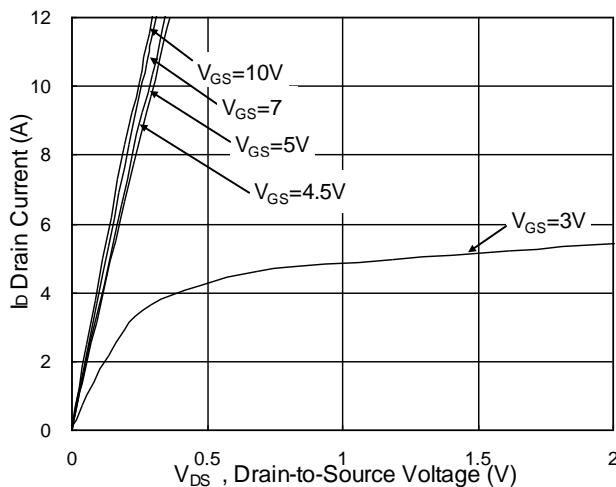


Fig.1 Typical Output Characteristics

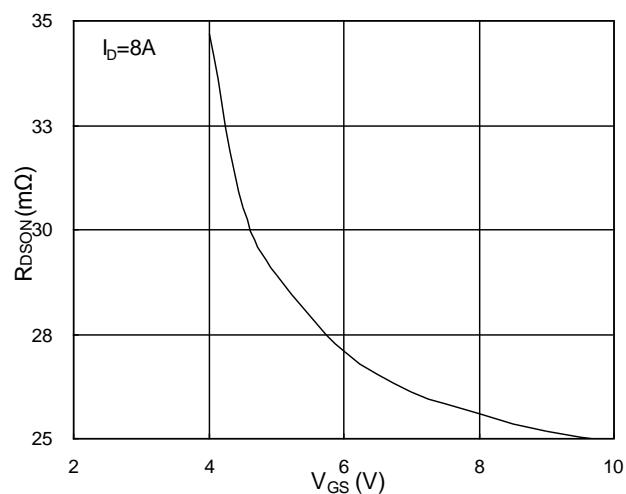


Fig.2 On-Resistance v.s Gate-Source

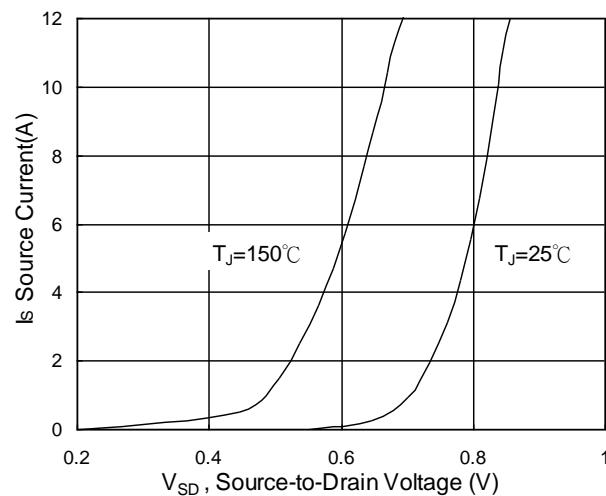


Fig.3 Forward Characteristics of Reverse

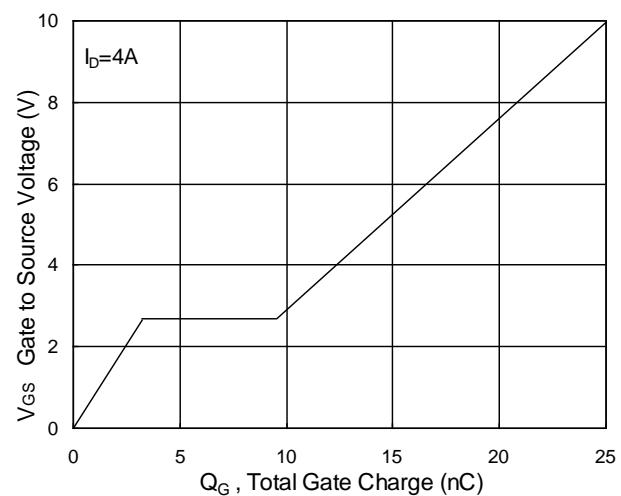


Fig.4 Gate-Charge Characteristics

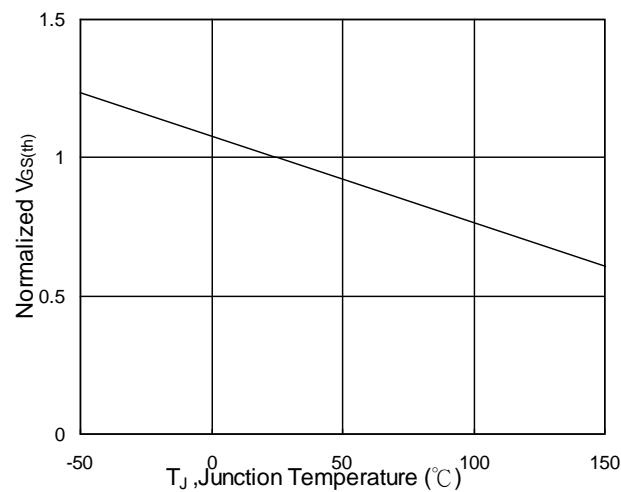


Fig.5 Normalized V_{GS(th)} v.s T_J

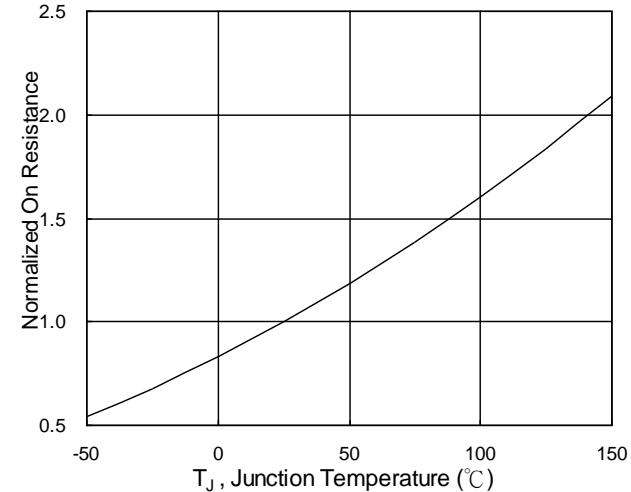
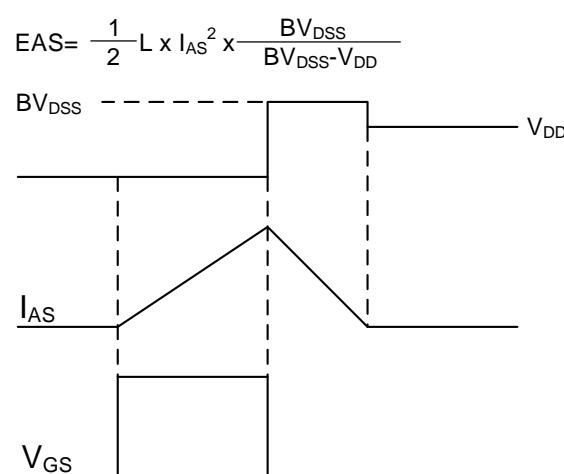
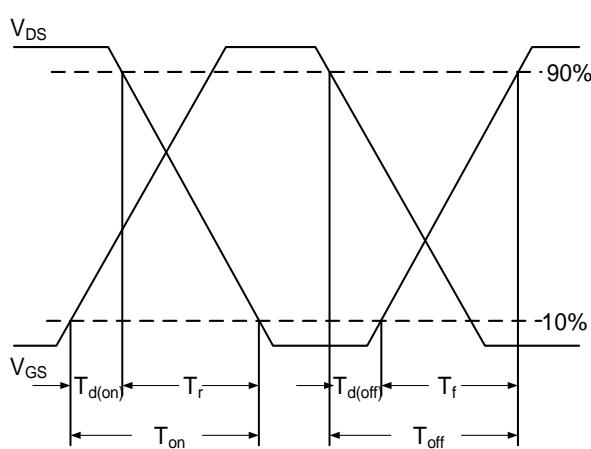
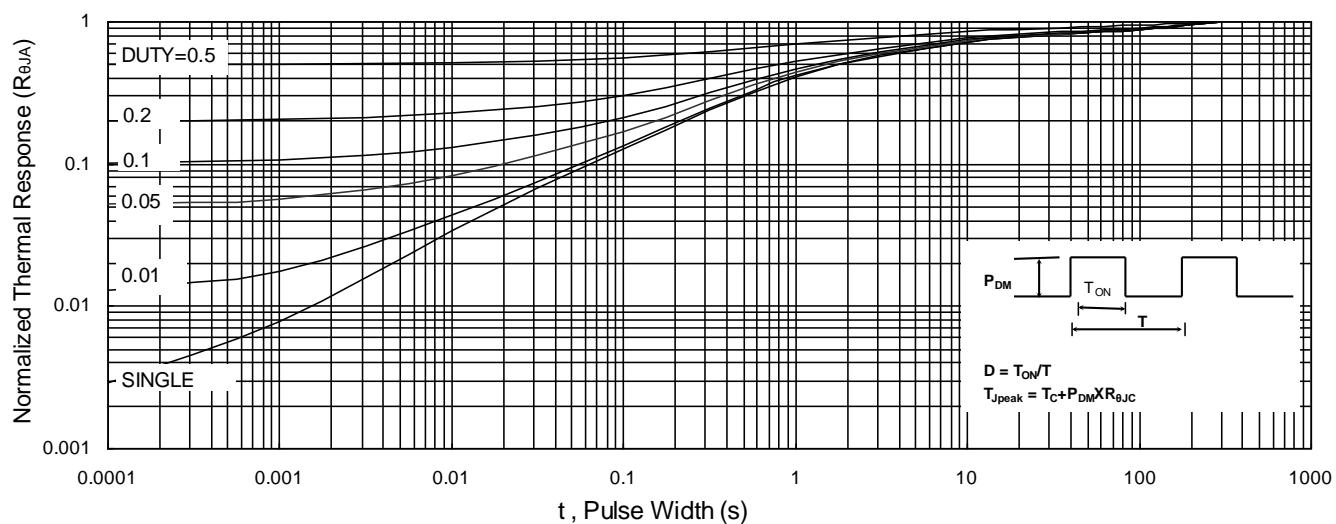
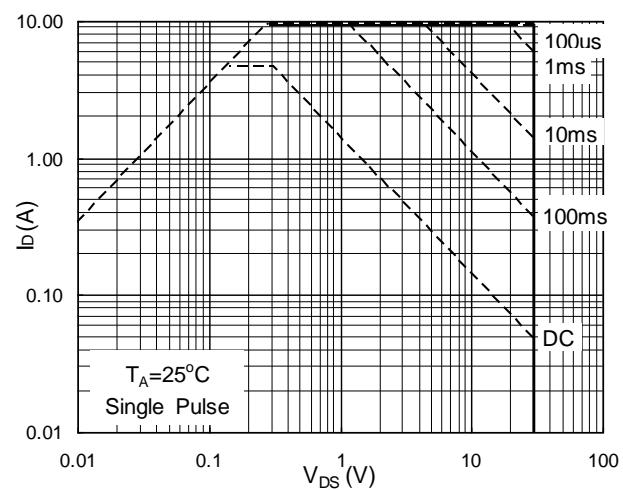
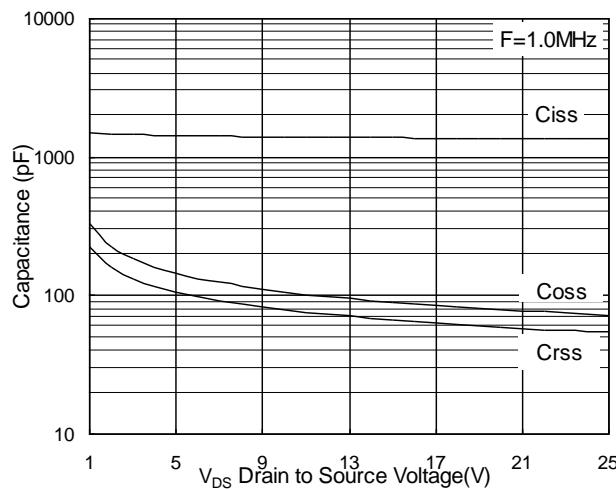


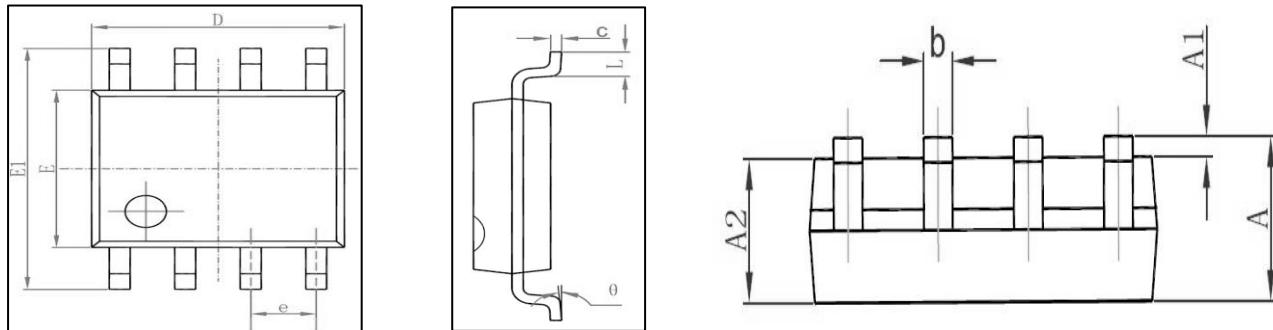
Fig.6 Normalized R_{DS(on)} v.s T_J

Ratings and Characteristic Curves



Package Outline Dimensions Millimeters

SOP-8



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	1.350	1.750	0.053	0.069
A1	0.100	0.250	0.004	0.010
A2	1.350	1.550	0.053	0.061
b	0.330	0.510	0.013	0.020
c	0.170	0.250	0.006	0.010
D	4.700	5.100	0.185	0.200
E	3.800	4.000	0.150	0.157
E1	5.800	6.200	0.228	0.244
e	1.270 (BSC)		0.050 (BSC)	
L	0.400	1.270	0.016	0.050
θ	0°	8°	0°	8°

