

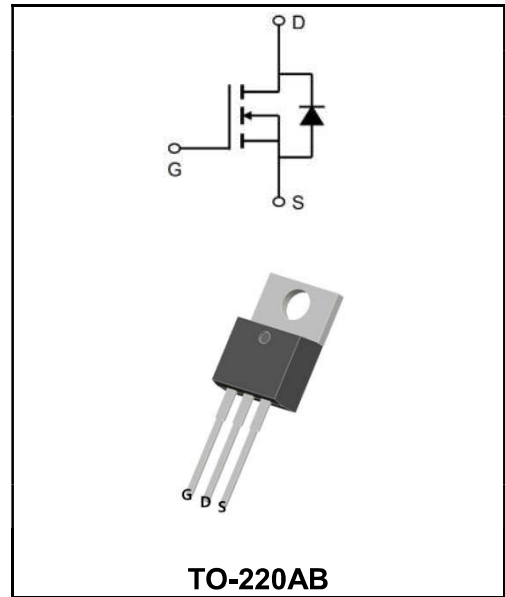
**200V N-CHANNEL ENHANCEMENT MODE MOSFET**

**MAIN CHARACTERISTICS**

<b>I<sub>D</sub></b>	18A
<b>V<sub>DSS</sub></b>	200V
<b>R<sub>DS(on)-typ(@V<sub>GS</sub>=10V)</sub></b>	< 150mΩ( <b>Type:120mΩ</b> )

**Application**

- ◆Uninterruptible Power Supply(UPS)
- ◆Power Factor Correction (PFC)



**Product Specification Classification**

Part Number	Package	Marking	Pack
YFW640AT	TO-220AB	YFW 640AT XXXXX	1000PCS/Tape

**Maximum Ratings at T<sub>c</sub>=25°C unless otherwise specified**

Characteristics	Symbols	Value	Units
Drain-Source Voltage(V <sub>GS</sub> =0V)	<b>V<sub>DS</sub></b>	200	<b>V</b>
Continuous Drain Current	<b>I<sub>D</sub></b>	18	<b>A</b>
Pulsed Drain Current (Note1)	<b>I<sub>DM</sub></b>	72	<b>A</b>
Gate - Source Voltage	<b>V<sub>GS</sub></b>	±20	<b>V</b>
Single Pulse Avalanche Energy (Note2)	<b>E<sub>AS</sub></b>	340	<b>mJ</b>
Avalanche Current (Note1)	<b>I<sub>AR</sub></b>	15	<b>A</b>
Repetitive Avalanche Energy (Note1)	<b>E<sub>AR</sub></b>	8.3	<b>mJ</b>
Power Dissipation (T <sub>c</sub> =25°C)	<b>P<sub>D</sub></b>	104	<b>W</b>
Operating Junction and Storage Temperature Range	<b>T<sub>J</sub> ,T<sub>STG</sub></b>	-55 to +150	<b>°C</b>
Thermal Resistance, Junction-case	<b>R<sub>θJC</sub></b>	1.2	<b>°C/W</b>
Thermal Resistance, Junction ambient	<b>R<sub>θJA</sub></b>	62.5	<b>°C/W</b>

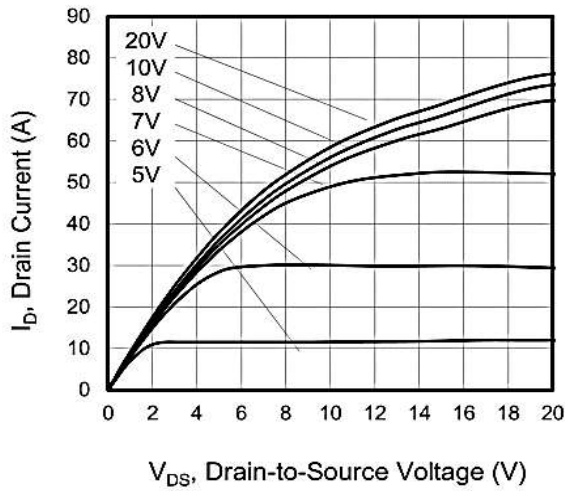
**Maximum Ratings at Tc=25°C unless otherwise specified**

Characteristics	Test Condition	Symbols	Min	Typ	Max	Units
Drain-Source Breakdown Voltage	$V_{GS}=0V, I_D=250\mu A$	<b>V(BR)DSS</b>	200	220	-	<b>V</b>
Zero Gate Voltage Drain Current	$V_{DS}=200V, V_{GS}=0V, T_J=25^\circ C$	<b>I<sub>DSS</sub></b>	-	-	5	<b>μA</b>
	$V_{DS}=160V, V_{GS}=0V, T_J=125^\circ C$		-	-	100	
Gate- Source Leakage	$V_{GS}=\pm 20V$	<b>I<sub>GSS</sub></b>	-	-	±100	<b>nA</b>
Gate Source Threshold Voltage	$V_{DS}=V_{GS}, I_D=250\mu A$	<b>V<sub>GS(th)</sub></b>	2.0	3.0	4.0	<b>V</b>
Drain-Source On-Resistance (Note3)	$V_{GS}=10V, I_D=9A$	<b>R<sub>DS(ON)</sub></b>	-	120	150	<b>mΩ</b>
Input Capacitance	$V_{DS}=25V$ $V_{GS}=0V$ $f=1MHz$	<b>C<sub>iss</sub></b>	-	1318	-	<b>pF</b>
Output Capacitance		<b>C<sub>oss</sub></b>	-	180	-	
Reverse Transfer Capacitance		<b>C<sub>rss</sub></b>	-	75	-	
Total Gate Charge	$V_{DD}=160V$ $I_D=18A$ $V_{GS}=10V$	<b>Q<sub>g</sub></b>	-	41	-	<b>nC</b>
Gate-Source Charge		<b>Q<sub>gs</sub></b>	-	5.5	-	
Gate-Drain Charge		<b>Q<sub>gd</sub></b>	-	19.5	-	
Turn-on delay time	$V_{DD}=100V$ $I_D=18A$ $R_G=25\Omega$	<b>t<sub>d(on)</sub></b>	-	24	-	<b>ns</b>
Turn-on Rise Time		<b>T<sub>r</sub></b>	-	45	-	
Turn-Off Delay Time		<b>t<sub>d(OFF)</sub></b>	-	101	-	
Turn-on Fall Time		<b>t<sub>f</sub></b>	-	95	-	
Continuous Body Diode Current	$T_C=25^\circ C$	<b>I<sub>S</sub></b>	-	-	18	<b>A</b>
Pulsed Diode Forward Current		<b>I<sub>SM</sub></b>	-	-	72	<b>A</b>
Body Diode Voltage	$V_{GS}=0V, I_{SD}=18A, T_J=25^\circ C$	<b>V<sub>SD</sub></b>	-	-	1.4	<b>V</b>
Reverse Recovery Time	$V_{GS}=0V, I_S=18A,$ $di_{SD}/dt=100A/\mu s$	<b>t<sub>rr</sub></b>	-	230	-	<b>ns</b>
Reverse Recovery Charge		<b>Q<sub>rr</sub></b>	-	1.8	-	<b>nC</b>

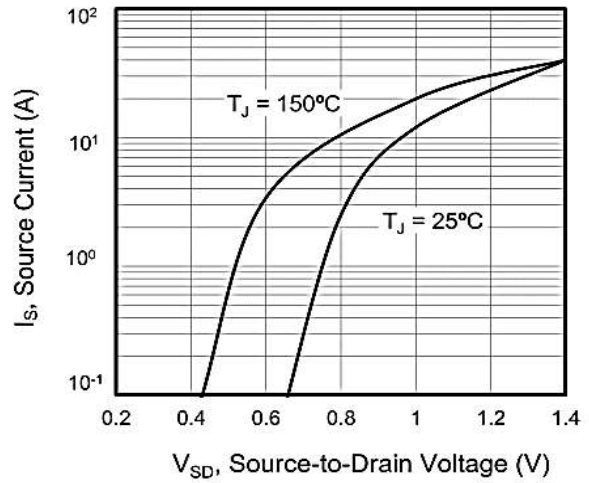
Note :

- 1、 The data tested by surface mounted on a 1 inch2 FR-4 board with 2OZ copper.
- 2、 The EAS data shows Max. rating . IAS = 15A, VDD = 50V, RG = 25 Ω, Starting TJ = 25 °C
- 3、 The test condition is Pulse Test: Pulse width ≤ 300μs, Duty Cycle ≤ 1%
- 4、 The power dissipation is limited by 150°C junction temperature
- 5、 The data is theoretically the same as ID and IDM , in real applications , should be limited by total power dissipation.

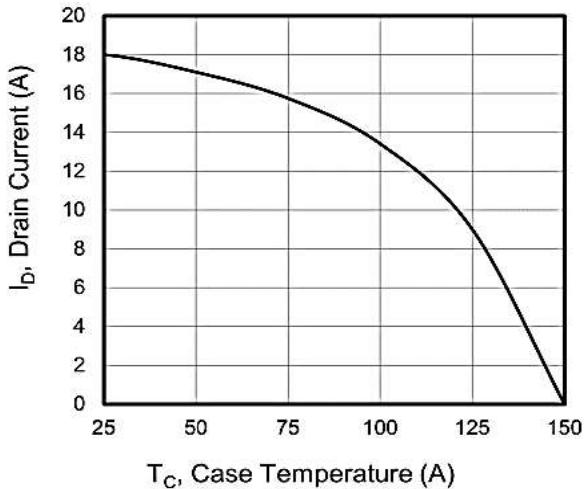
**Typical Characteristics**



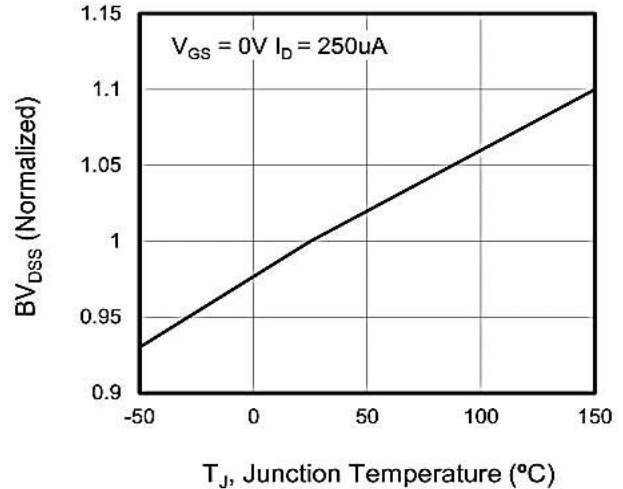
**Figure 1. Output Characteristics ( $T_J = 25^\circ\text{C}$ )**



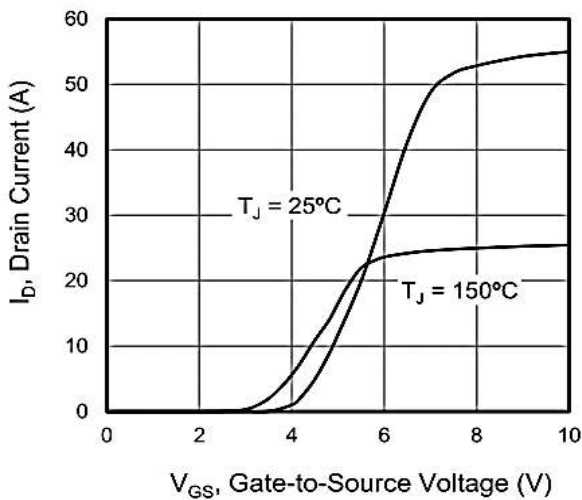
**Figure 2. Body Diode Forward Voltage**



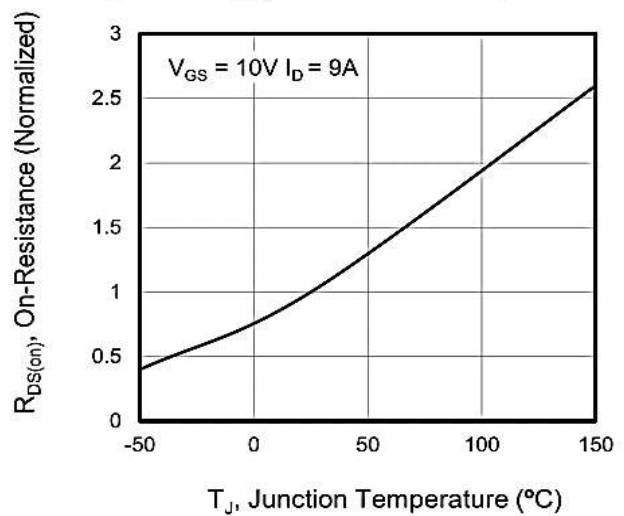
**Figure 3. Drain Current vs. Temperature**



**Figure 4.  $BV_{DSS}$  Variation vs. Temperature**



**Figure 5. Transfer Characteristics**



**Figure 6. On-Resistance vs. Temperature**

Ratings and Characteristic Curves

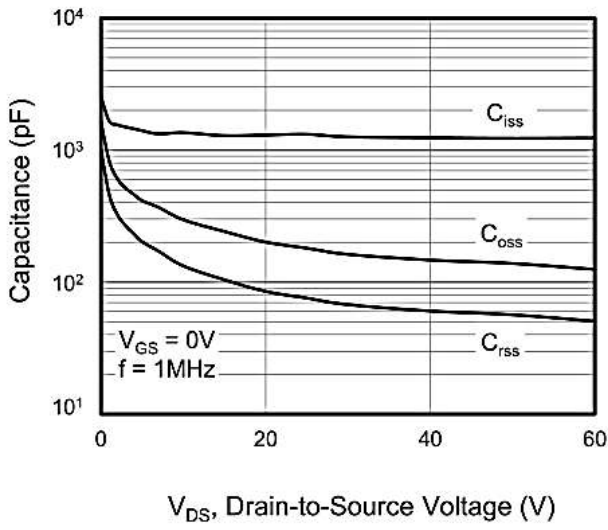


Figure 7. Capacitance

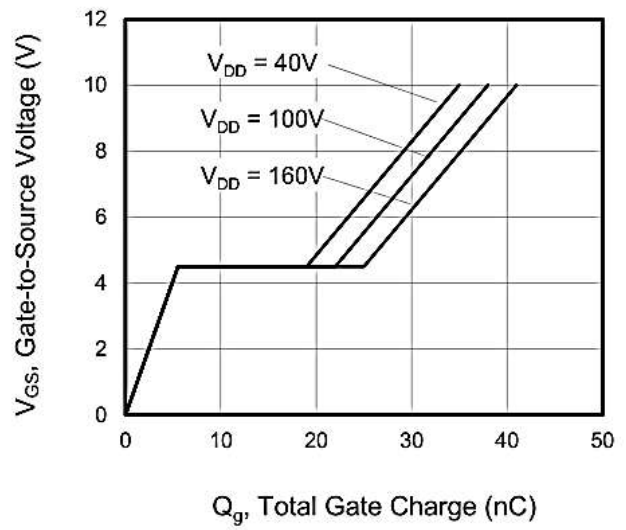


Figure 8. Gate Charge

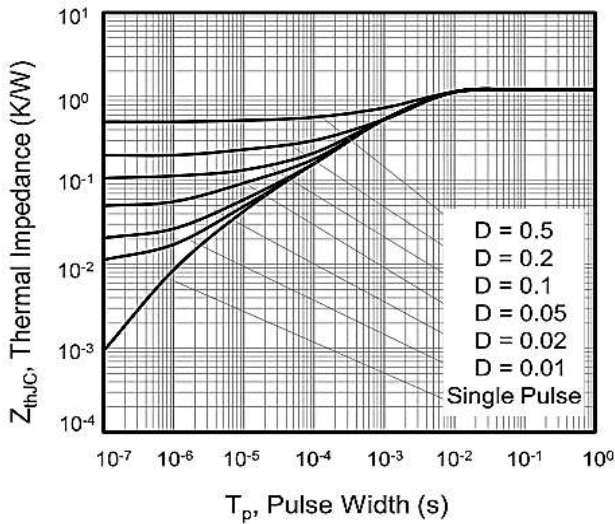


Figure 10. Transient Thermal Impedance

Package Outline Dimensions Millimeters

TO-220AB

Dim.	Min.	Max.
A	10.15	10.35
B	2.65	2.95
C	3.70	3.90
D	28.5	29.5
E	1.30	1.45
F	6.35	6.55
G	2.9	3.3
H	15.0	16.0
I	0.38	0.42
J	4.45	4.55
K	1.25	1.35
L	Typ 5.08	
M	Typ 2.54	
N	3.1	3.3
O	0.76	0.84
All Dimensions in millimeter		