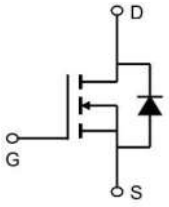
700V N-SJ ENHANCEMENT MODE MOSFET

MAIN CHARACTERISTICS

I_D	15A
V_{DSS}	700V
R_{DSON}-typ(@V_{GS}=10V)	< 0.22Ω(Type:0.18Ω)



TO-220AB

TO-220F

TO-263

Features

- Low RDS(on) & FOM
- Extremely low switching loss
- Excellent stability and uniformity
- Easy to drive

Application

- Lighting
- Server power supply
- Telecom
- Solar invertor

Product Specification Classification

Part Number	Package	Marking	Pack
YFWJ15N70AT	TO-220AB	YFW 15N70AT XXXXX	1000PCS/Box
YFWJ15N70AF	TO-220F	YFW 15N70AF XXXXX	1000PCS/Box
YFWJ15N70AS	TO-263	YFW 15N70AS XXXXX	800PCS/Reel

Maximum Ratings at Tc=25°C unless otherwise specified

Characteristics	Symbols	Value	Units
Drain-Source Voltage	V_{DS}	700	V
Gate - Source Voltage	V_{GS}	±30	V
Continuous Drain Current [1] T_C=25℃	I_D	15	A
Continuous Drain Current [1] T_C=100℃		11	A
Pulsed Drain Current[2] T_C=25℃	$I_{DM\ (pluse)}$	54	A
Power dissipation[3] for TO220, TO262, TO263, TO247, T_C=25 ℃	P_D	151	W
Power dissipation[3] for TO220F , T_C=25 ℃		34	
Single Pulse Avalanche Energy[5]	E_{AS}	272	mJ
MOSFET dv/dt ruggedness, V_{DS}=0…480 V	dv/dt	100	V/ns
Reverse diode dv/dt, V_{DS}=0…480 V, I_{SD}≤I_D	dv/dt	50	V/ns
Operating and Storage Temperature	T_J ,T_{STG}	-55 to 150	°C
Thermal Resistance， Junction-to-Case	$R_{\theta JC}$	3.68	°C/W
Thermal Resistance, Junction –to-ambient[4]	$R_{\theta JA}$	62.5	°C/W

Maximum Ratings at Tc=25°C unless otherwise specified

Characteristics	Test Condition	Symbols	Min	Typ	Max	Units
Drain-Source Breakdown Voltage	V_{GS}=0V, I_D=250uA	BV_{DSS}	650	-	-	V
	V_{GS}=0V, I_D=250uA, T_j= 150℃		700	770		
Gate Threshold Voltage	V_{DS}= V_{GS}, I_D=250μA	$V_{GS(th)}$	3.5	-	4.5	V
Drain-Source On-State Resistance	V_{GS}=10V, I_D=9A	$R_{DS(ON)}$	-	0.18	0.22	Ω
	V_{GS}=10V, I_D=9A, T_j= 150℃		-	0.45	-	
Gate Source Leakage Current	V_{GS}=30V	I_{GSS}	-	-	100	nA
	V_{GS}=-30V		-	-	-100	
Drain-source leakage current	V_{DS}=650V , V_{GS}=0V	I_{DSS}	-	-	10	μA
Input Capacitance	V_{DS}=50V V_{GS}=0V f=1MHz	C_{iss}	-	1493	-	pF
Output Capacitance		C_{oss}	-	101	-	
Reverse Transfer Capacitance		C_{rss}	-	2.05	-	
Turn-on delay time	V_{GS}=10V V_{DS}=400V R_G=20Ω I_D=18A	$t_{d(on)}$	-	45.28	-	ns
Rise Time		T_r	-	82.64	-	
Turn-Off Delay Time		$t_{d(OFF)}$	-	42.2	-	
Fall Time		t_f	-	32.56	-	
Total Gate Charge	I_D=18A V_{DS}=400V V_{GS}=10V	Q_g	-	21.7	-	nC
Gate-Source Charge		Q_{gs}	-	8.04	-	
Gate-Drain Charge		Q_{gd}	-	7.4	-	
Gate plateau voltage		$V_{plateau}$	-	7.2	-	V
Diode forward current	V_{GS}<Vth	I_S	-	-	18	A
Pulsed source current		I_{SP}	-	-	54	
Diode forward voltage	V_{GS}=0V , I_S=18A	V_{SD}	-	-	1.2	V
Reverse Recovery Time	I_S=18A, V_R=400V dI_{SD}/dt=100A/μs,	t_{rr}	-	143.3	-	ns
Reverse Recovery Charge		Q_{rr}	-	767	-	nC
Peak reverse recovery current		I_{rrm}	-	10.7	-	A

1) Calculated continuous current based on maximum allowable junction temperature.
2) Repetitive rating, pulse width limited by max. junction temperature.
3) Pd is based on max. junction temperature, using junction-case thermal resistance.
4) The value of RθJA is measured with the device mounted on 1 in 2 FR-4 board with 2oz. Copper, in a still air environment with Ta=25 ℃.
5) VDD=100 V, RG=25 Ω, L=10 mH, starting Tj=25 ℃.

Ratings and Characteristic Curves

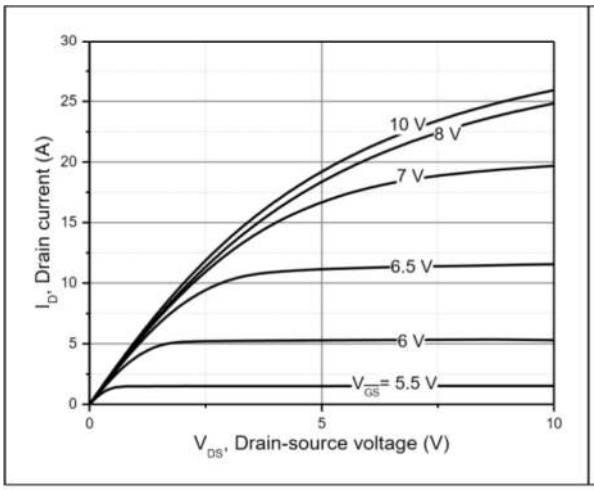


Figure 1, Typ. output characteristics

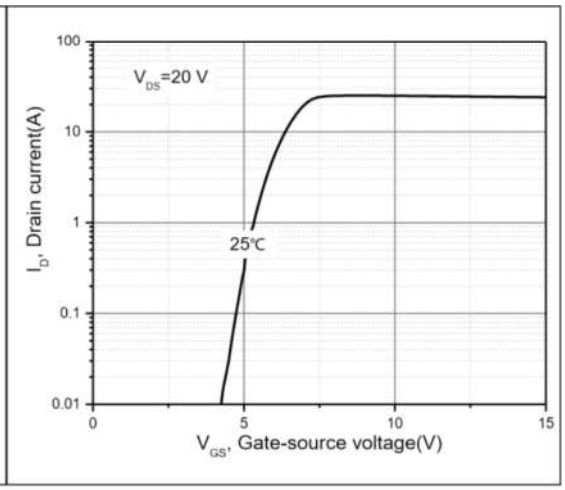


Figure 2, Typ. transfer characteristics

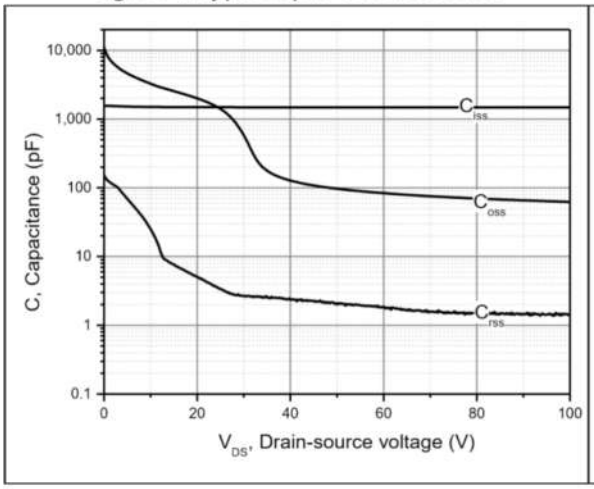


Figure 3, Typ. capacitances

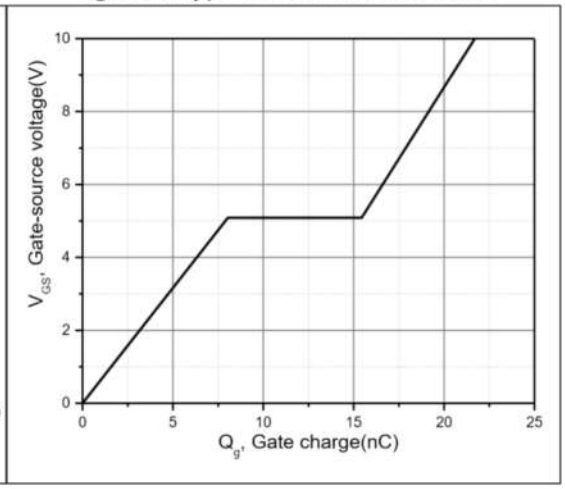


Figure 4, Typ. gate charge

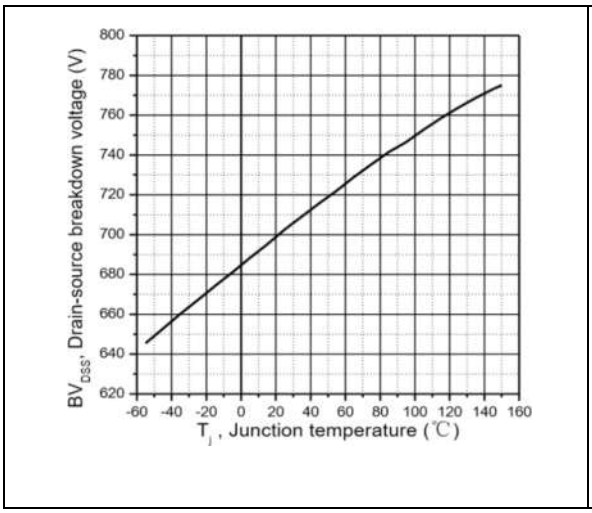


Figure 5, Drain-source breakdown voltage

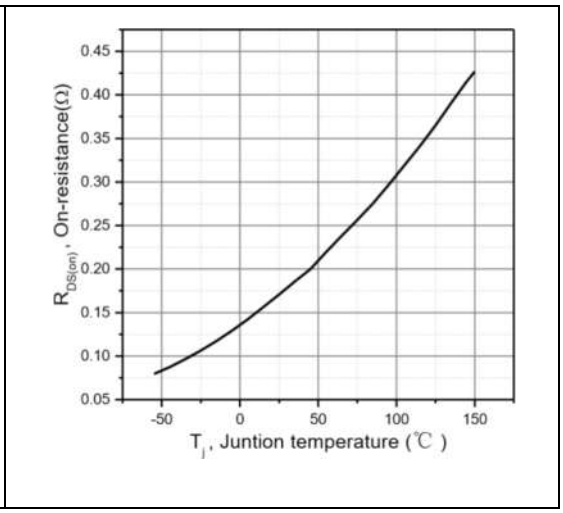


Figure 6, Drain-source on-state resistance

Ratings and Characteristic Curves

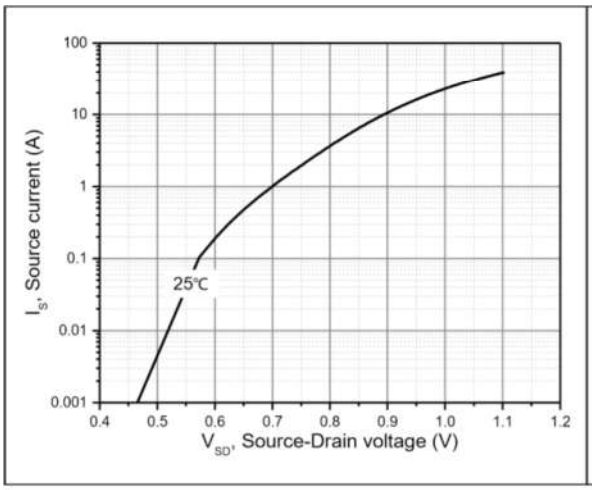


Figure 7, Forward characteristic of body diode

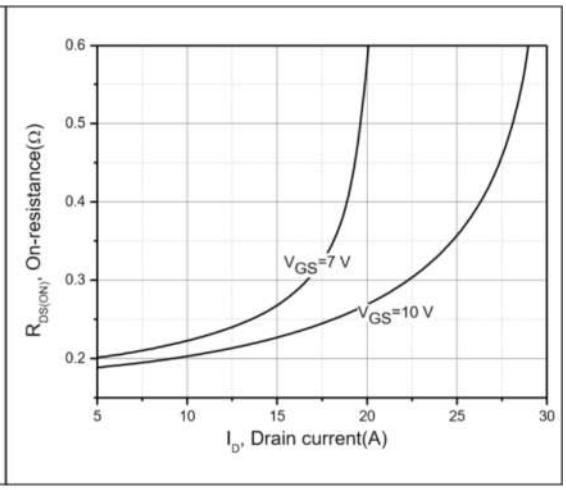


Figure 8, Drain-source on-state resistance

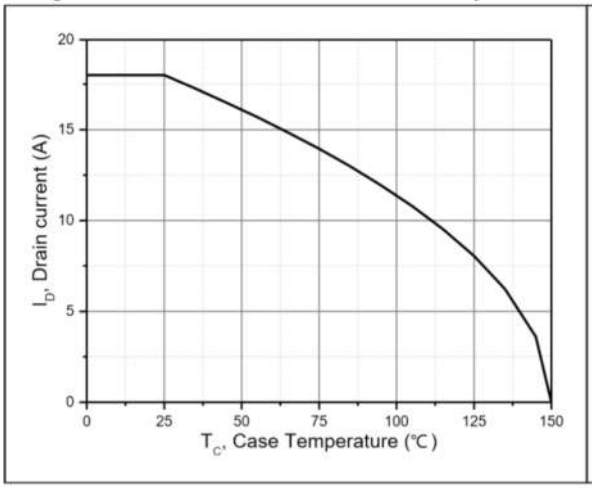


Figure 9, Drain current

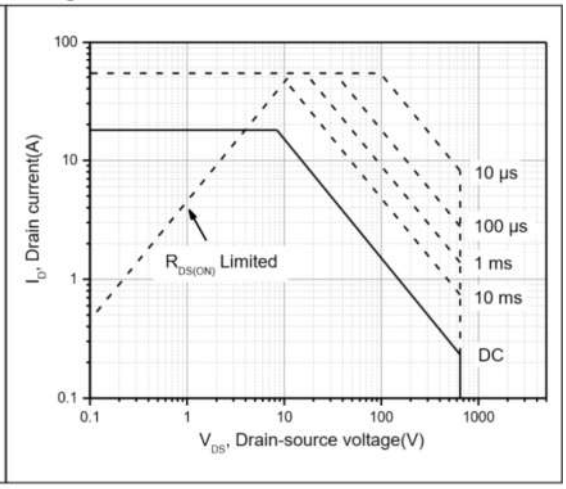


Figure 10, Safe operation area for TO220/TO262/TO263/TO247 T_C=25 ℃

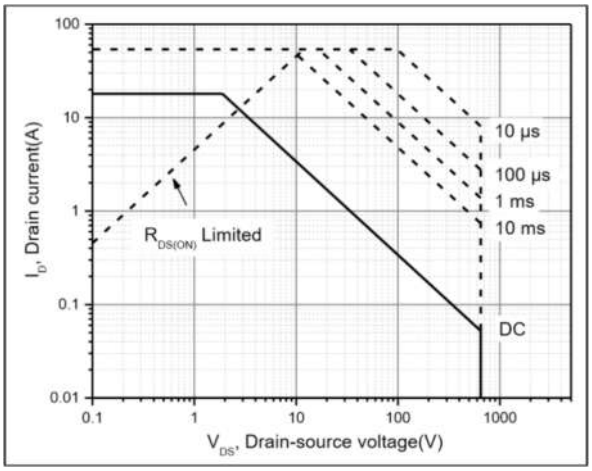


Figure 11, Safe operation area for TO220F T_C=25 ℃

Ratings and Characteristic Curves

Test circuits and waveforms

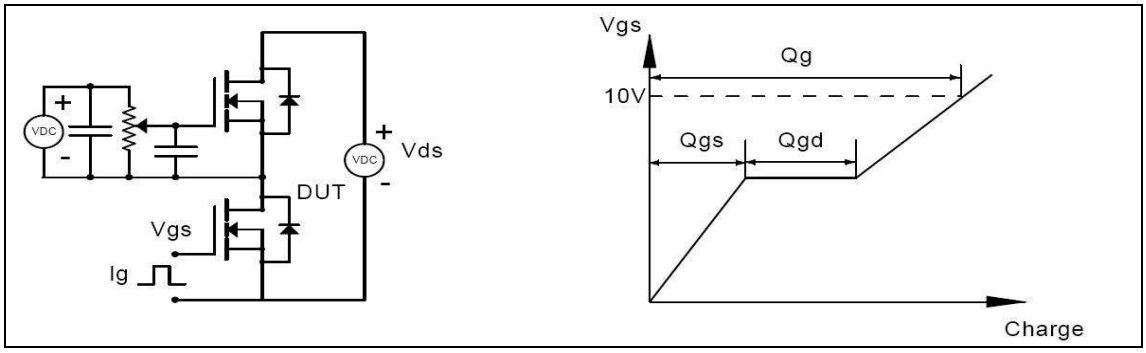


Figure 1, Gate charge test circuit & waveform

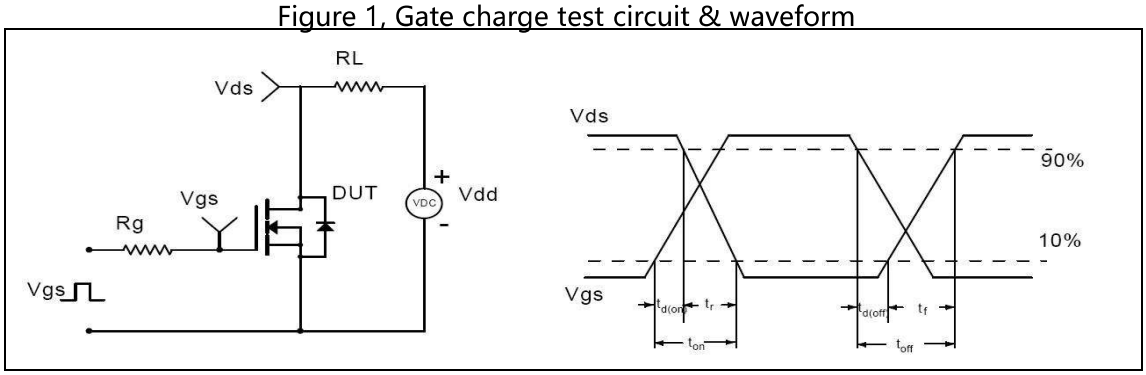


Figure 2, Switching time test circuit & waveforms

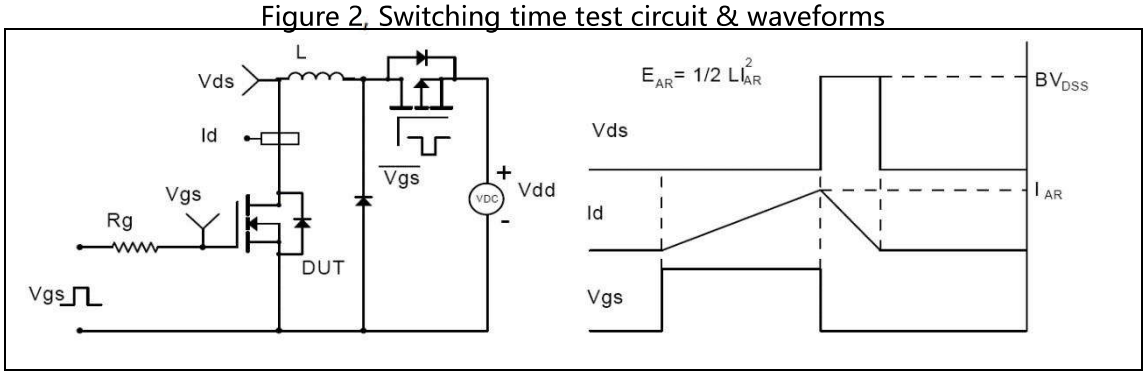


Figure 3, Unclamped inductive switching (UIS) test circuit & waveforms

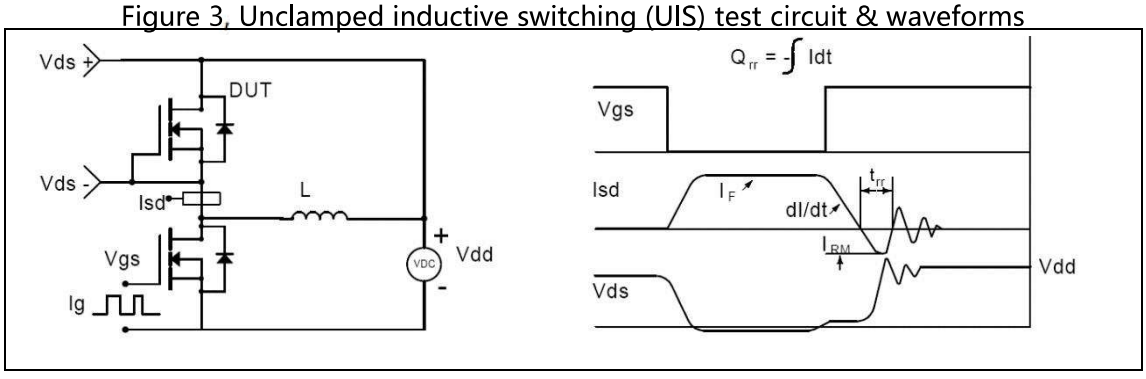
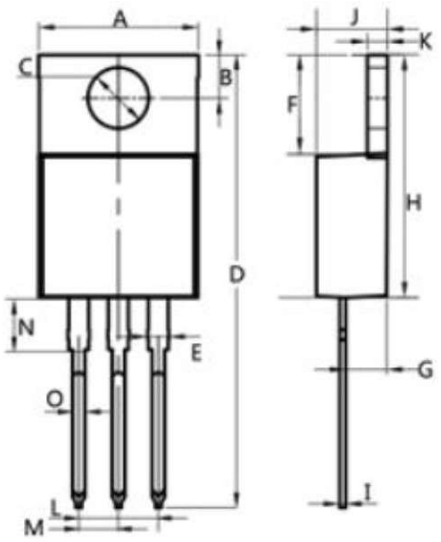


Figure 4, Diode reverse recovery test circuit & waveforms

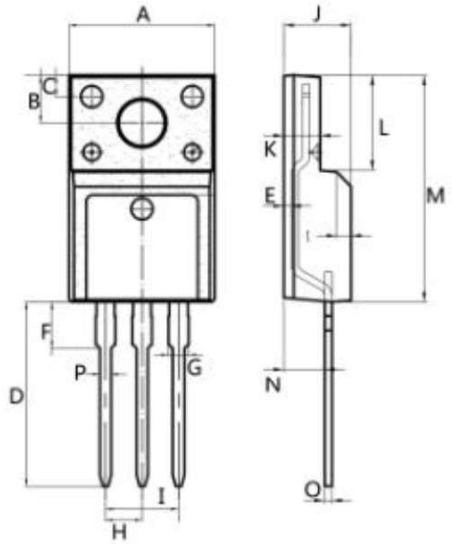
Package Outline Dimensions Millimeters

TO-220AB



Dim.	Min.	Max.
A	10.15	10.35
B	2.65	2.95
C	3.70	3.90
D	28.5	29.5
E	1.30	1.45
F	6.35	6.55
G	2.9	3.3
H	15.0	16.0
I	0.38	0.42
J	4.45	4.55
K	1.25	1.35
L	Typ 5.08	
M	Typ 2.54	
N	3.1	3.3
O	0.76	0.84
All Dimensions in millimeter		

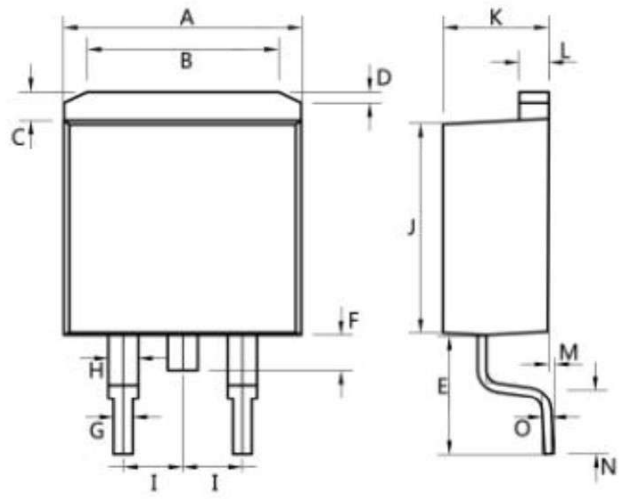
TO-220F



Dim.	Min.	Max.
A	9.95	10.25
B	2.95	3.25
C	1.25	1.45
D	12.95	13.25
E	0.50	0.65
F	3.1	3.3
G	1.30	1.45
H	Typ 2.54	
I	Typ 5.08	
J	4.60	4.75
K	2.50	2.65
L	6.35	6.55
M	15.4	16.0
N	2.75	3.05
O	0.48	0.52
P	0.76	0.84
All Dimensions in millimeter		

Package Outline Dimensions Millimeters

TO-263



Dim.	Min.	Max.
A	10.1	10.2
B	7.4	7.6
C	1.3	1.5
D	0.55	0.75
E	5.0	6.0
F	1.4	1.6
G	0.78	0.86
H	1.2	1.3
I	Typ2.54	
J	8.4	8.6
K	4.45	4.55
L	1.25	1.35
M	0.02	0.1
N	2.4	2.8
O	0.36	0.40
All Dimensions in millimeter		