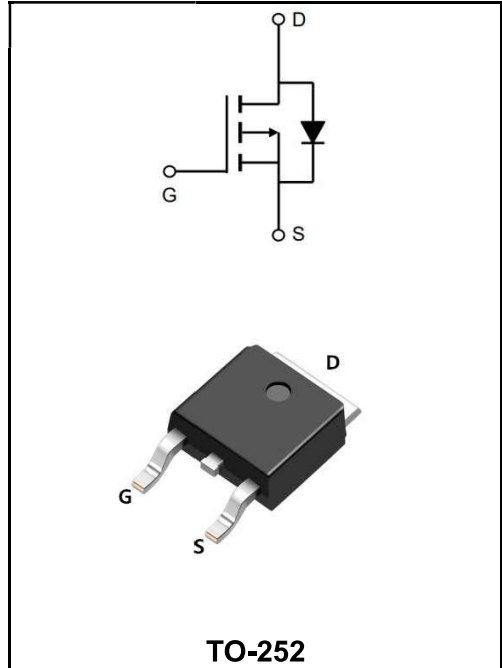


**-100V P-CHANNEL ENHANCEMENT MODE MOSFET**

**MAIN CHARACTERISTICS**

<b>I<sub>D</sub></b>	-50A
<b>V<sub>DSS</sub></b>	-100V
<b>R<sub>DS(on)-typ(@V<sub>GS</sub>=-10V)</sub></b>	< 52mΩ( <b>Type:40 mΩ</b> )



**Application**

- ◆Brushless motor
- ◆Load switch
- ◆Uninterruptible power supply

**Product Specification Classification**

Part Number	Package	Marking	Pack
YFW50P10AD	TO-252	YFW 50P10AD XXXXX	2500PCS/Tape

**Maximum Ratings at T<sub>c</sub>=25°C unless otherwise specified**

Characteristics	Symbols	Value	Units
Drain-Source Voltage	<b>V<sub>DS</sub></b>	-100	<b>V</b>
Gate - Source Voltage	<b>V<sub>GS</sub></b>	±20	<b>V</b>
Continuous Drain Current, V <sub>GS</sub> @ -10V <sup>1</sup> @T <sub>C</sub> =25°C	<b>I<sub>D</sub></b>	-50	<b>A</b>
Continuous Drain Current, V <sub>GS</sub> @ -10V <sup>1</sup> @T <sub>C</sub> =100°C	<b>I<sub>D</sub></b>	-28	<b>A</b>
Pulsed Drain Current <sup>2</sup>	<b>I<sub>DM</sub></b>	-150	<b>A</b>
Single Pulse Avalanche Energy <sup>3</sup>	<b>E<sub>AS</sub></b>	87	<b>mJ</b>
Avalanche Current	<b>I<sub>AS</sub></b>	-35	<b>A</b>
Total Power Dissipation <sup>4</sup> @T <sub>C</sub> =25°C	<b>P<sub>D</sub></b>	140	<b>W</b>
Storage Temperature Range	<b>T<sub>STG</sub></b>	-55 to +150	<b>°C</b>
Operating Junction Temperature Range	<b>T<sub>J</sub></b>	-55 to +150	<b>°C</b>
Thermal Resistance Junction-Ambient <sup>1</sup>	<b>R<sub>θJA</sub></b>	62.5	<b>°C/W</b>
Thermal Resistance Junction-Case <sup>1</sup>	<b>R<sub>θJC</sub></b>	1.1	<b>°C/W</b>

**Maximum Ratings at Tc=25°C unless otherwise specified**

Characteristics	Test Condition	Symbols	Min	Typ	Max	Units
Drain-Source Breakdown Voltage	$V_{GS}=0V, I_D=-250\mu A$	<b>V(BR)DSS</b>	-100	-	-	<b>V</b>
Zero Gate Voltage Drain Current	$V_{DS}=-100V, V_{GS}=0V$	<b>I<sub>DSS</sub></b>	-	-	-1.0	<b>μA</b>
Gate to Body Leakage Current	$V_{GS}=\pm 20V, V_{DS}=0V$	<b>I<sub>GSS</sub></b>	-	-	±100	<b>nA</b>
Gate -Threshold Voltage	$V_{DS}=V_{GS}, I_D=-250\mu A$	<b>V<sub>GS(th)</sub></b>	-1.0	-1.6	-2.5	<b>V</b>
Static Drain-Source on-Resistance	$V_{GS}=-10V, I_D=-20A$	<b>R<sub>DS(ON)</sub></b>	-	40	52	<b>mΩ</b>
	$V_{GS}=-4.5V, I_D=-10A$		-	44	62	
Input Capacitance	$V_{DS}=-50V$ $V_{GS}=0V$ $f=1MHz$	<b>C<sub>iss</sub></b>	-	2120	-	<b>μF</b>
Output Capacitance		<b>C<sub>oss</sub></b>	-	194	-	
Reverse Transfer Capacitance		<b>C<sub>rss</sub></b>	-	13	-	
Total Gate Charge	$V_{DS}=-50V$ $V_{GS}=-10V$ $I_D=-5A$	<b>Q<sub>g</sub></b>	-	40	-	<b>nC</b>
Gate-Source Charge		<b>Q<sub>gs</sub></b>	-	7.8	-	
Gate-Drain("Miller") Charge		<b>Q<sub>gd</sub></b>	-	8.6	-	
Turn-on delay time	$V_{DD}=-50V$ $V_{GS}=-10V$ $I_D=-5A$ $R_G=6\Omega$	<b>t<sub>d(on)</sub></b>	-	13	-	<b>ns</b>
Turn-on Rise Time		<b>T<sub>r</sub></b>	-	39	-	
Turn-Off Delay Time		<b>t<sub>d(OFF)</sub></b>	-	100.1	-	
Turn-Off Fall Time		<b>t<sub>f</sub></b>	-	105.3	-	
Maximum Continuous Drain to Source Diode Forward Current		<b>I<sub>S</sub></b>	-	-	-35	<b>A</b>
Maximum Pulsed Drain to Source Diode Forward Current		<b>I<sub>SM</sub></b>	-	-	-140	<b>A</b>
Drain to Source Diode Forward Voltage	$V_{GS}=0V, I_S=-30A$	<b>V<sub>SD</sub></b>	-	-	-1.2	<b>V</b>
Body Diode Reverse Recovery Time	$I_F=-5A, dI/dt=100A/\mu s, T_J=25^\circ C$	<b>t<sub>rr</sub></b>	-	104	-	<b>ns</b>
Body Diode Reverse Recovery Charge		<b>Q<sub>rr</sub></b>	-	280	-	<b>nC</b>

Note :

- 1、 The data tested by surface mounted on a 1 inch 2 FR-4 board with 2OZ copper.
- 2、 The data tested by pulsed , pulse width  $\leq 300\mu s$  , duty cycle  $\leq 2\%$
- 3、 The EAS data shows Max. rating . The test condition is  $V_{DD}=-25V, V_{GS}=-10V, L=0.1mH, I_{AS}=-24A$
- 4、 The power dissipation is limited by 150°C junction temperature
- 5、 The data is theoretically the same as I D and I DM , in real applications , should be limited by total power dissipation.

Ratings and Characteristic Curves

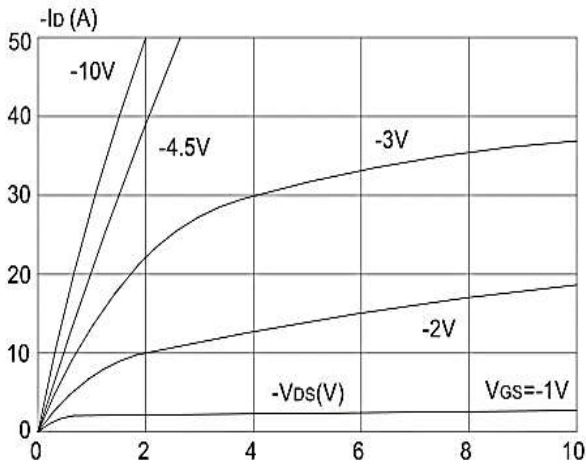


Figure 1: Output Characteristics

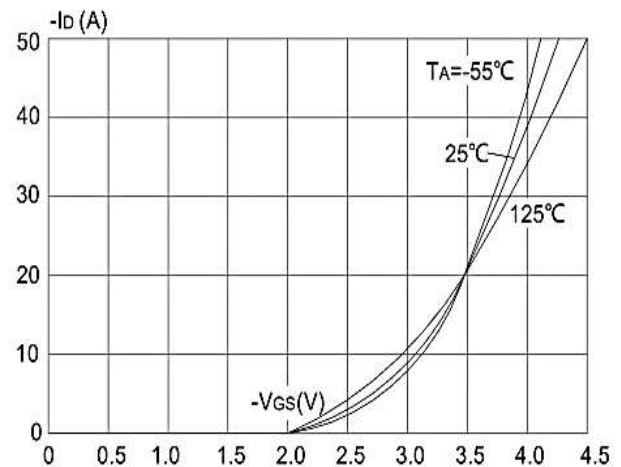


Figure 2: Typical Transfer Characteristics

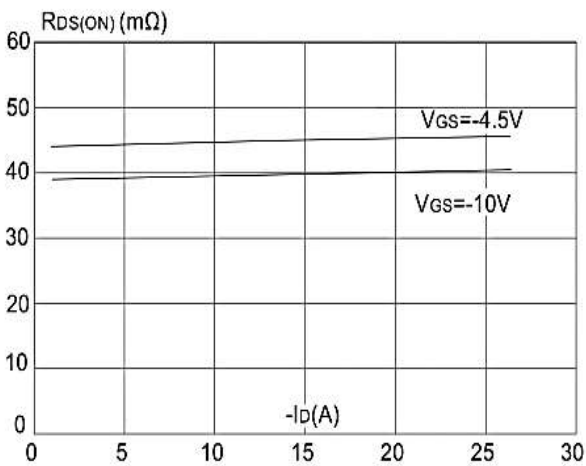


Figure 3: On-resistance vs. Drain Current

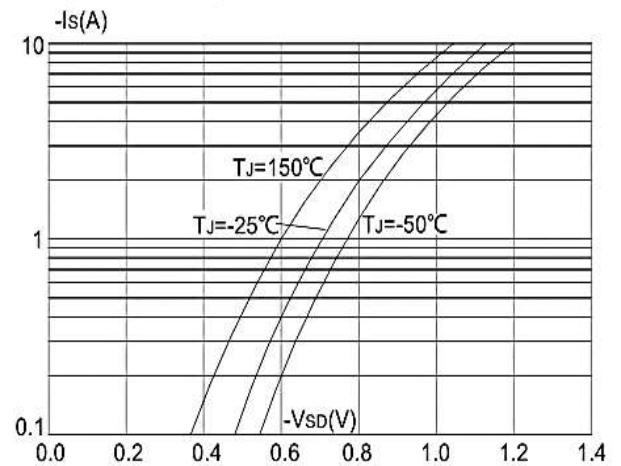


Figure 4: Body Diode Characteristics

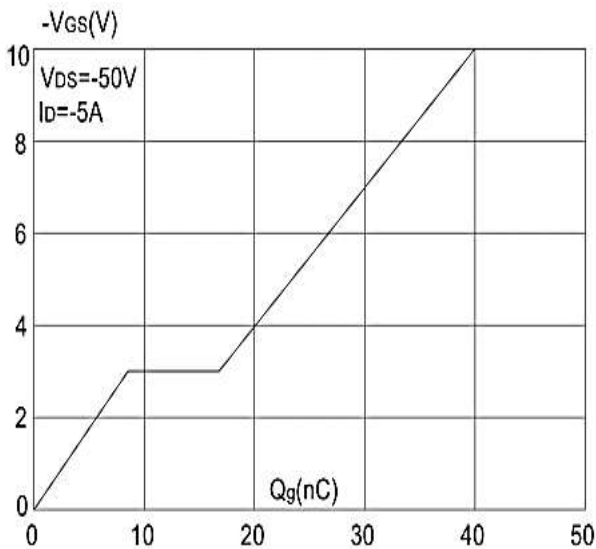


Figure 5: Gate Charge Characteristics

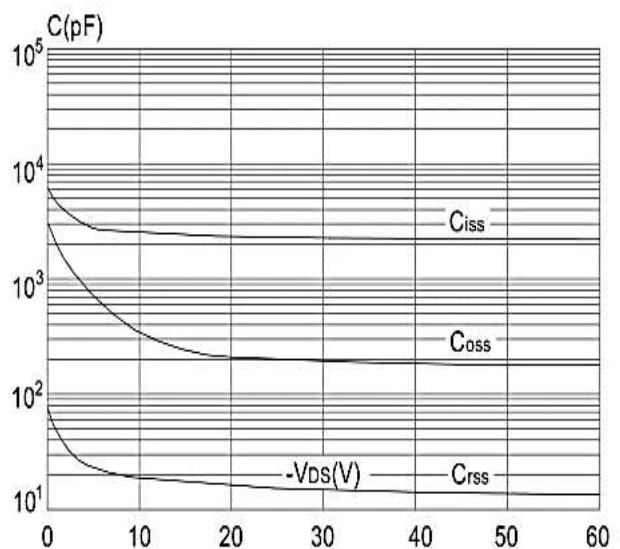
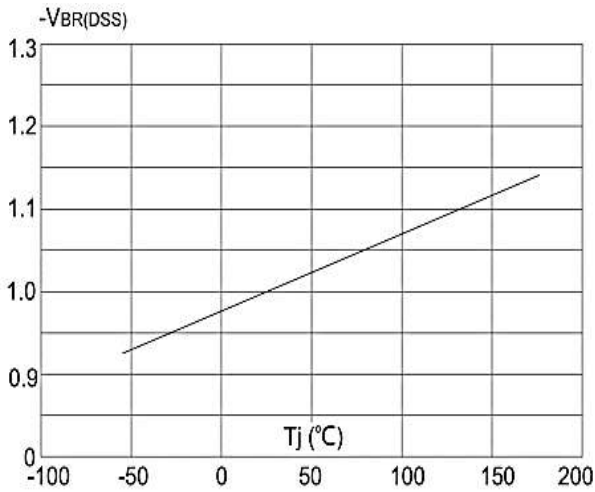
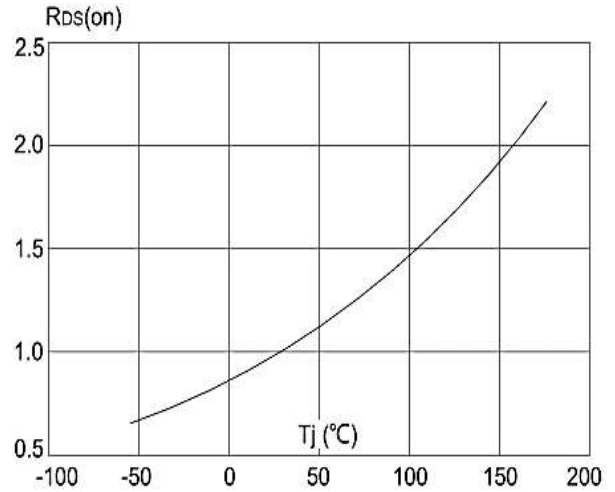


Figure 6: Capacitance Characteristics

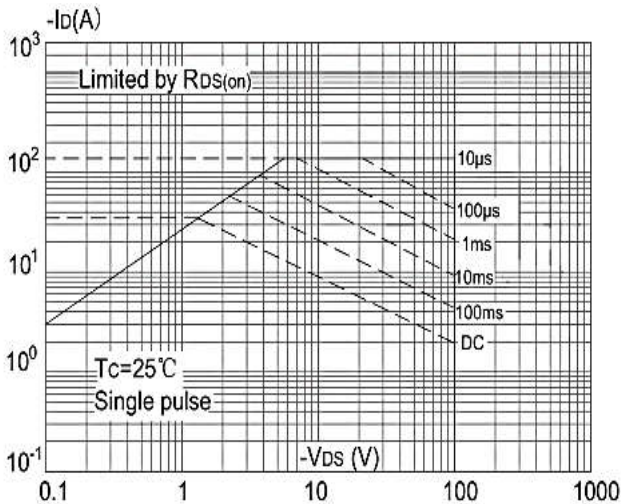
**Ratings and Characteristic Curves**



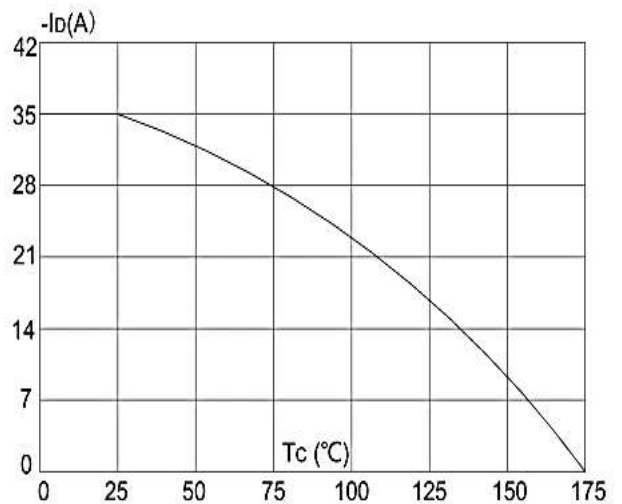
**Figure 7: Normalized Breakdown Voltage vs Junction Temperature**



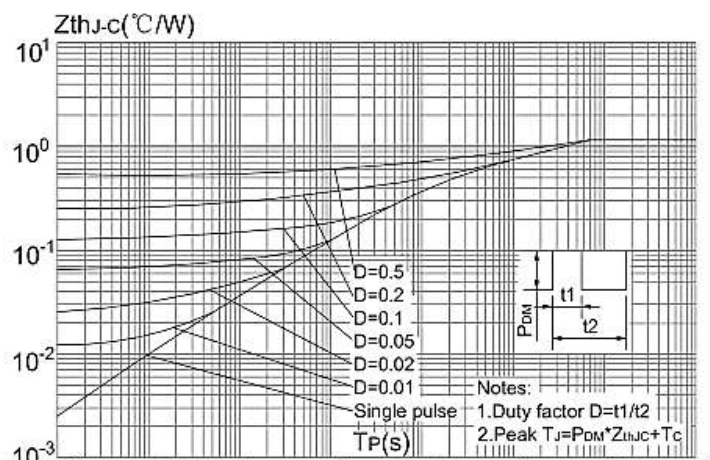
**Figure 8: Normalized on Resistance vs. Junction Temperature**



**Figure 9: Maximum Safe Operating Area**



**Figure 10: Maximum Continuous Drain Current vs. Ambient Temperature**



**Figure.11: Maximum Effective Transient Thermal Impedance, Junction-to-Ambien**

Package Outline Dimensions Millimeters

TO-252

Dim.	Min.	Typ.	Max.
A	2.10	-	2.50
A2	0	-	0.10
B	0.66	-	0.86
B2	5.18	-	5.48
C	0.40	-	0.60
C2	0.44	-	0.58
D	5.90	-	6.30
D1	5.30REF		
E	6.40	-	6.80
E1	4.63	-	-
G	4.47	-	4.67
H	9.50	-	10.70
L	1.09	-	1.21
L2	1.35	-	1.65
V1	-	7°	-
V2	0°	-	6°
All Dimensions in millimeter			

