

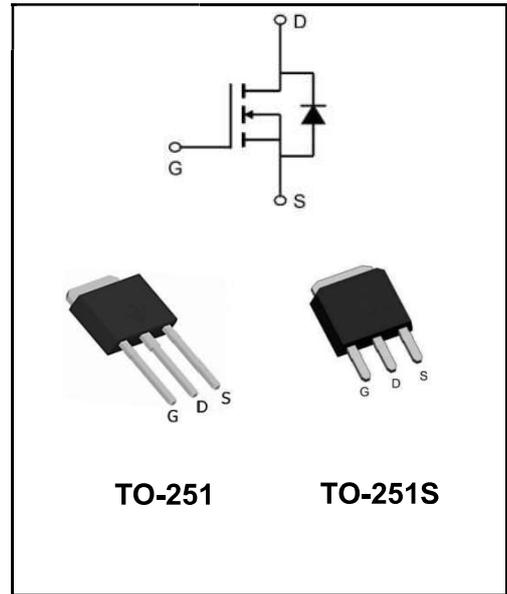
**200V N-CHANNEL ENHANCEMENT MODE MOSFET**

**MAIN CHARACTERISTICS**

<b>I<sub>D</sub></b>	9A
<b>V<sub>DSS</sub></b>	200V
<b>R<sub>DS(on)-typ(@V<sub>GS</sub>=10V)</sub></b>	< 300mΩ ( <b>Type:230mΩ</b> )

**Application**

- ◆Uninterruptible Power Supply(UPS)
- ◆Power Factor Correction (PFC)



**Product Specification Classification**

Part Number	Package	Marking	Pack
YFW9N20AMJ	TO-251	YFW 9N20AMJ XXXXX	4000PCS/Tape
YFW9N20AMJ	TO-251S	YFW 9N20AMJ XXXXX	4000PCS/Tape

**Maximum Ratings at T<sub>c</sub>=25°C unless otherwise specified**

Characteristics	Symbols	Value	Units
Drain-Source Voltage(V <sub>GS</sub> =0V)	<b>V<sub>DS</sub></b>	200	<b>V</b>
Continuous Drain Current	<b>I<sub>D</sub></b>	9	<b>A</b>
Pulsed Drain Current(Note1)	<b>I<sub>DM</sub></b>	36	<b>A</b>
Gate - Source Voltage	<b>V<sub>GS</sub></b>	±20	<b>V</b>
Single Pulse Avalanche Energy(Note2)	<b>E<sub>AS</sub></b>	100	<b>mJ</b>
Avalanche Current(Note1)	<b>I<sub>AR</sub></b>	7.5	<b>A</b>
Repetitive Avalanche Energy(Note1)	<b>E<sub>AR</sub></b>	8.1	<b>mJ</b>
Power Dissipation (T <sub>C</sub> =25°C)	<b>P<sub>D</sub></b>	74	<b>W</b>
Operating Junction and Storage Temperature Range	<b>T<sub>J</sub> , T<sub>STG</sub></b>	-55 to +150	<b>°C</b>
Thermal Resistance, Junction-case	<b>R<sub>θJC</sub></b>	1.7	<b>°C/W</b>
Thermal Resistance, Junction ambient	<b>R<sub>θJA</sub></b>	62.5	<b>°C/W</b>

**Maximum Ratings at Tc=25°C unless otherwise specified**

Characteristics	Test Condition	Symbols	Min	Typ	Max	Units
Drain-Source Breakdown Voltage	$V_{GS}=0V, I_D=250\mu A$	<b>V(BR)DSS</b>	200	222	-	<b>V</b>
Zero Gate Voltage Drain Current	$V_{DS}=200V, V_{GS}=0V, T_J=25^\circ C$	<b>I<sub>DSS</sub></b>	-	-	5	<b>μA</b>
	$V_{DS}=160V, V_{GS}=0V, T_J=125^\circ C$		-	-	100	
Gate- Source Leakage	$V_{GS}=\pm 20V$	<b>I<sub>GSS</sub></b>	-	-	±100	<b>nA</b>
Gate Source Threshold Voltage	$V_{DS}=V_{GS}, I_D=250\mu A$	<b>V<sub>GS(th)</sub></b>	2.0	3.5	4.0	<b>V</b>
Drain-Source On-Resistance	$V_{GS}=10V, I_D=4.5A$	<b>R<sub>DS(ON)</sub></b>	-	230	300	<b>mΩ</b>
Input Capacitance	$V_{DS}=25V$ $V_{GS}=0V$ $f=1MHz$	<b>C<sub>iss</sub></b>	-	684	-	<b>pF</b>
Output Capacitance		<b>C<sub>oss</sub></b>	-	103	-	
Reverse Transfer Capacitance		<b>C<sub>rss</sub></b>	-	37	-	
Total Gate Charge	$V_{DD}=160V$ $I_D=9A$ $V_{GS}=10V$	<b>Q<sub>g</sub></b>	-	23	-	<b>nC</b>
Gate-Source Charge		<b>Q<sub>gs</sub></b>	-	2.5	-	
Gate-Drain Charge		<b>Q<sub>gd</sub></b>	-	10	-	
Turn-on delay time	$V_{DD}=100V$ $I_D=9A$ $R_G=25\Omega$	<b>t<sub>d(on)</sub></b>	-	12	-	<b>ns</b>
Turn-on Rise Time		<b>T<sub>r</sub></b>	-	22	-	
Turn-Off Delay Time		<b>t<sub>d(OFF)</sub></b>	-	50	-	
Turn-on Fall Time		<b>t<sub>f</sub></b>	-	48	-	
Continuous Body Diode Current	$T_C=25^\circ C$	<b>I<sub>S</sub></b>	-	-	9	<b>A</b>
Pulsed Diode Forward Current		<b>I<sub>SM</sub></b>	-	-	36	<b>A</b>
Body Diode Voltage	$V_{GS}=0V, I_{SD}=9A, T_J=25^\circ C$	<b>V<sub>SD</sub></b>	-	-	1.4	<b>V</b>
Reverse Recovery Time	$V_{GS}=0V, I_S=9A, di_{SD}/dt=100A/\mu s$	<b>t<sub>rr</sub></b>	-	190	-	<b>ns</b>
Reverse Recovery Charge		<b>Q<sub>rr</sub></b>	-	1.7	-	<b>nC</b>

Note :

- 1、The data tested by surface mounted on a 1 inch2 FR-4 board with 2OZ copper.
- 2、The EAS data shows Max. rating . IAS = 7.5A, VDD = 50V, RG = 25 Ω, Starting TJ = 25 °C
- 3、The test condition is Pulse Test: Pulse width ≤ 300μs, Duty Cycle ≤ 1%
- 4、The power dissipation is limited by 150°C junction temperature
- 5、The data is theoretically the same as ID and IDM , in real applications , should be limited by total power dissipation.

Typical Characteristics

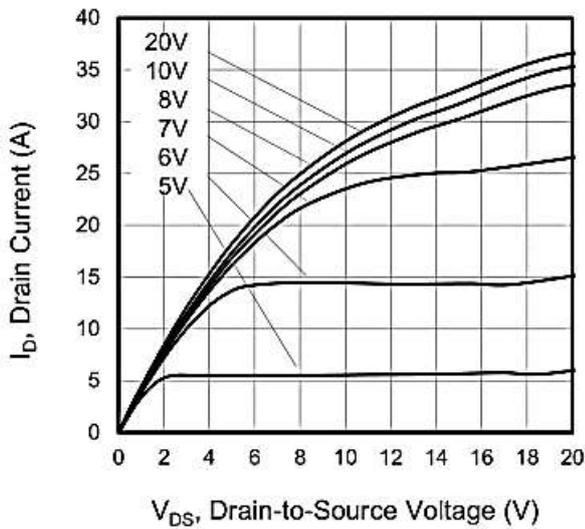


Figure 1. Output Characteristics ( $T_J = 25^\circ\text{C}$ )

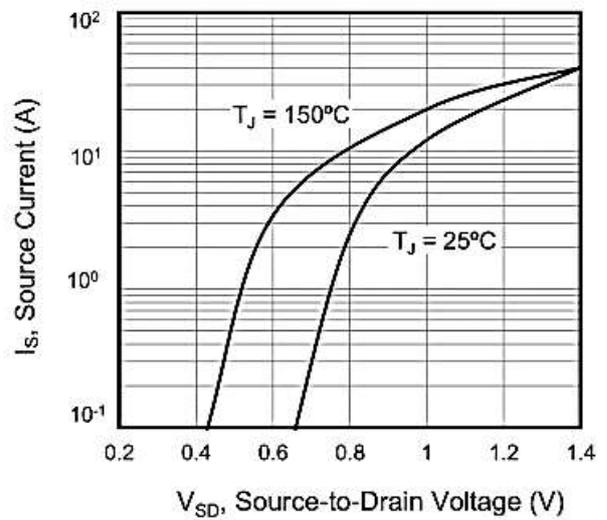


Figure 2. Body Diode Forward Voltage

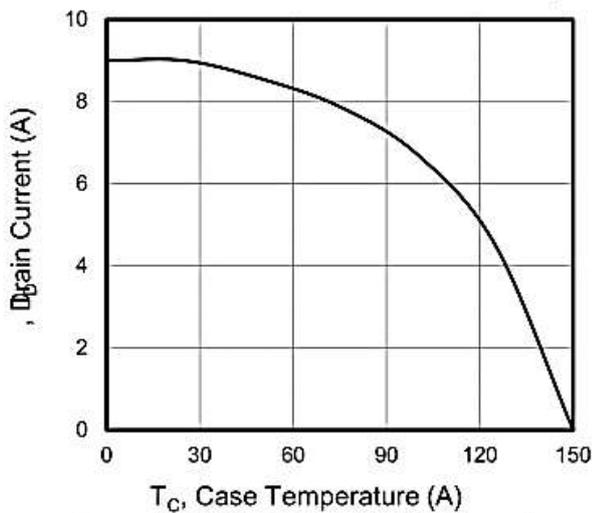


Figure 3. Drain Current vs. Temperature

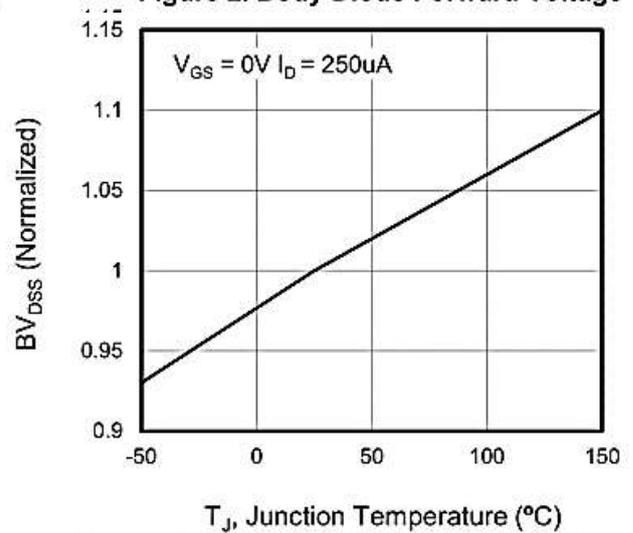


Figure 4.  $BV_{DSS}$  Variation vs. Temperature

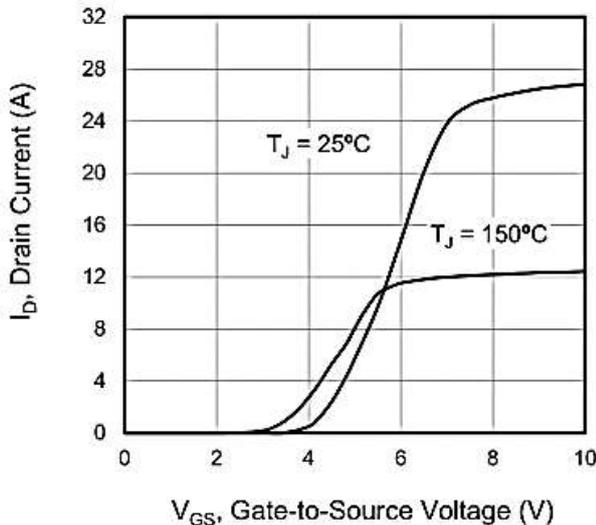


Figure 5. Transfer Characteristics

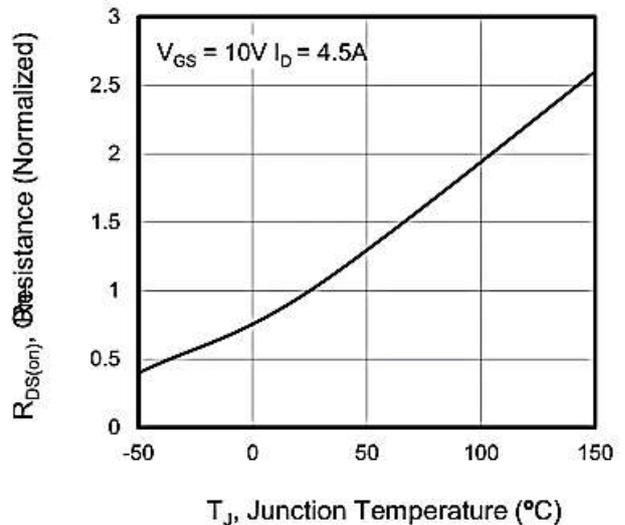


Figure 6. On-Resistance vs. Temperature

Ratings and Characteristic Curves

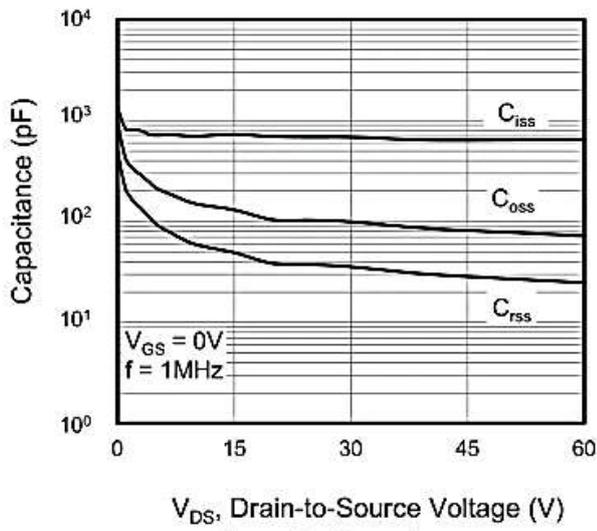


Figure 7. Capacitance

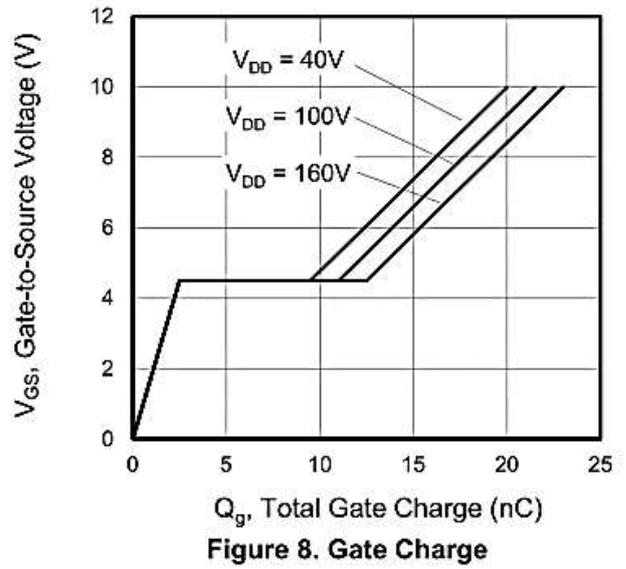


Figure 8. Gate Charge

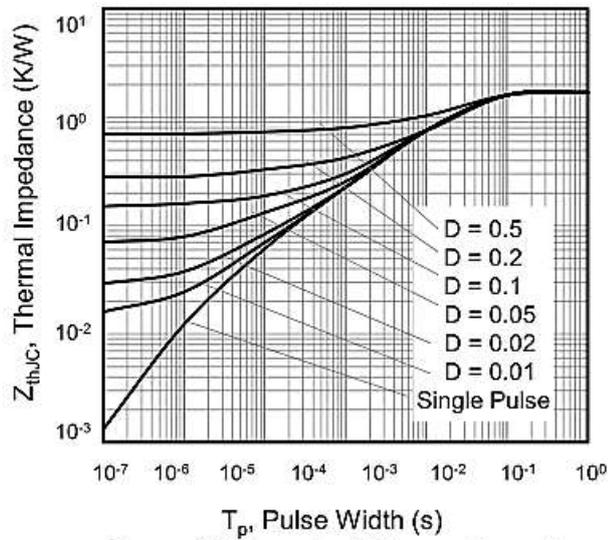


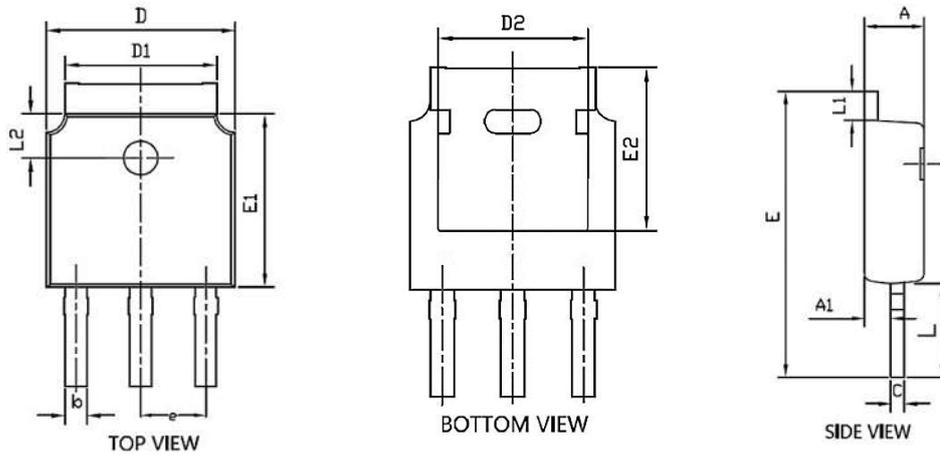
Figure 10. Transient Thermal Impedance

Package Outline Dimensions Millimeters

TO-251

	Dim.	Min.	Max.
	A	2.2	2.4
	A2	0.95	1.15
	A3	0.45	0.65
	b	0.65	0.85
	c	0.45	0.55
	D	6.45	6.75
	D2	5.2	5.4
	E	5.8	6
	E2	0.95	1.25
	e	Typ 2.3	
	e1	Typ 4.6	
	L	4	4.2
	L1	1.2	1.5
	All Dimensions in millimeter		

**TO-251S**



Symbol	Common		
	mm		
	Mim	Nom	Max
A	2.2	2.3	2.4
A1	0.9	1.0	1.1
b	0.66	0.76	0.86
C	0.46	0.52	0.58
D	6.50	6.6	6.7
D1	5.15	5.3	5.45
D2	4.6	4.8	4.95
E	10.4	---	11.5
E1	6.0	6.1	6.2
E2	5.400REF		
e	2.286BSC		
L	3.5	4.0	4.3
L1	0.9	---	1.27
L2	1.4	---	1.9