

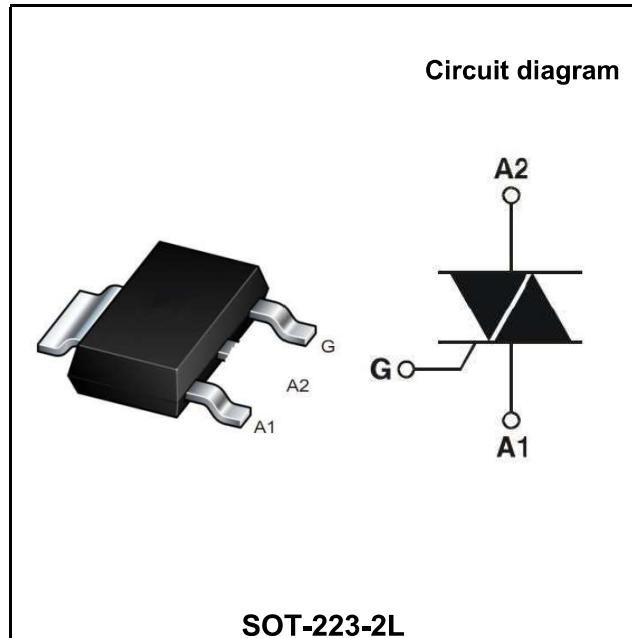
1.0A 4Quadrants TRIACs

Product Summary

Symbol	Value	Unit
$I_{T(AV)}$	1.0	A
$V_{DRM} V_{RRM}$	600/800	V
V_{TM}	1.55	V

Features

With high ability to withstand the shock loading of large current, With high commutation performances, 4 quadrants products especially recommended for use on inductive load.



Application

Washing machine, vacuums, massager, solid state relay, AC Motor speed regulation and so on.

Order Information

Part Number	Package	Marking	Delivery Form	Delivery Quantity
BT131W	SOT-223-2L	BT131 XXXX	13" T&R	2500PCS/Tape

Absolute maximum ratings (Ta=25°C unless otherwise noted)

Parameter	Symbol	Value		Unit
Repetitive peak off-state voltage	V_{DRM}	600/800		V
Repetitive peak reverse voltage	V_{RRM}	600/800		V
RMS on-state current	$I_T(RMS)$	1		A
Non repetitive surge peak on-state current (full cycle, F=50Hz)	I_{TSM}	16		A
I^2t value for fusing ($t_p=10ms$)	I^2t	1.28		A^2s
Critical rate of rise of on-state current ($ I_G = 2 \times I_{GT} $)	dI/dt	I - II - III	50	A
		IV	10	A/ps
Peak gate current	I_{GM}	2		A
Average gate power dissipation	$P_G (AV)$	0.5		W
Junction Temperature	T_J	-40~+125		°C
Storage Temperature	T_{STG}	-40 ~+150		°C

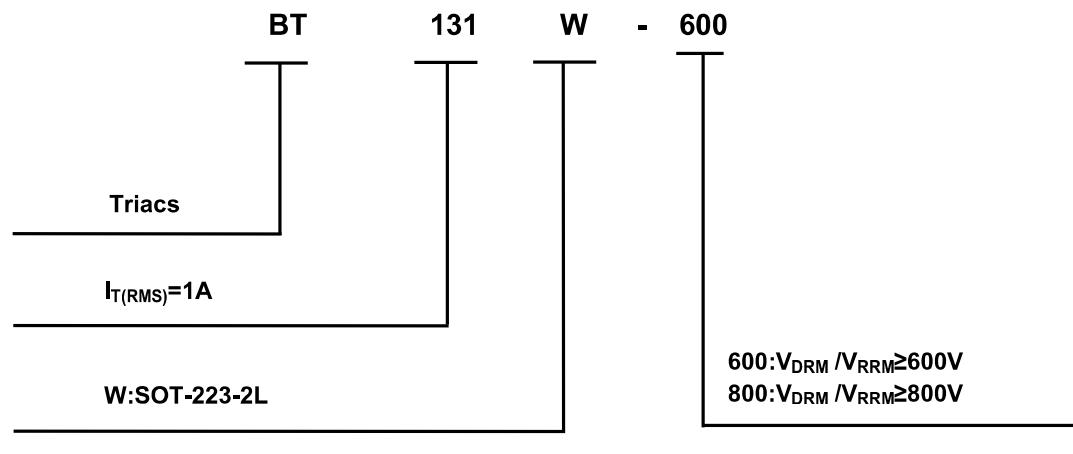
Electrical characteristics (TA=25°C, unless otherwise noted)

Parameter	Symbol	Test Condition	Value		Unit
			Min	Max	
Gate trigger current	I _{GT}	V _D =12V I _T =0.1A T _j =25°C	I - II - III	-	5
			IV	-	10
Gate trigger voltage	V _{GT}	I - II - III - IV		-	1.3
Gate non-trigger voltage	V _{GD}	V _D =V _{DRM} T _j =125°C		0.2	-
Latching current	I _L	V _D =12V I _{GT} =0.1A T _j =25°C	I - III - IV	-	10
			II	-	15
Holding current	I _H		I - II - III - IV	-	5
Critical-rate of rise of commutation voltage	dV _D /dt	V _D =2/3V _{DRM} Gate Open T _j =125°C		50	-
STATIC CHARACTERISTICS					

Forward "on" voltage	V _{TM}	I _{TM} = 1.5A tp=380ps	-	1.55	V
Repetitive Peak Off-State Current	I _{DRM}	V _D =V _{DRM} V _R =V _{RRM}	T _j =25°C	-	5
Repetitive Peak Reverse Current	I _{RRM}		T _j =125°C	-	100

Thermal resistance	R _{th(j-c)}	Junction to case(AC)	TYP.	23	°C/W
	R _{th(j-a)}	Junction to ambient	TYP.	60	°C/W

Ordering Information



Typical Characteristics

FIG.1: Maximum power dissipation versus RMS on-state current (full cycle)

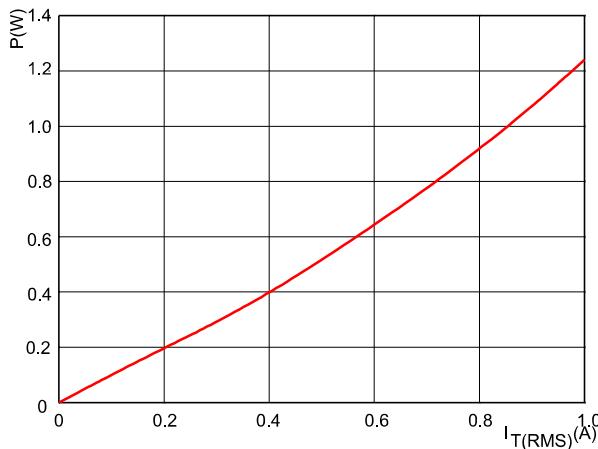


FIG.2: RMS on-state current versus case temperature (full cycle)

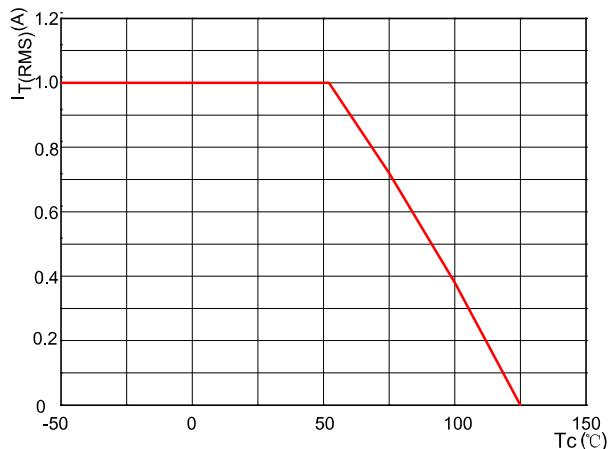


FIG.3: Surge peak on-state current versus number of cycles

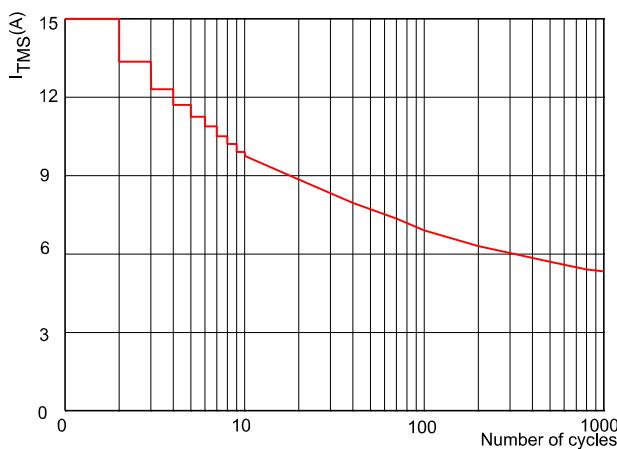


FIG.4: On-state characteristics (maximum values)

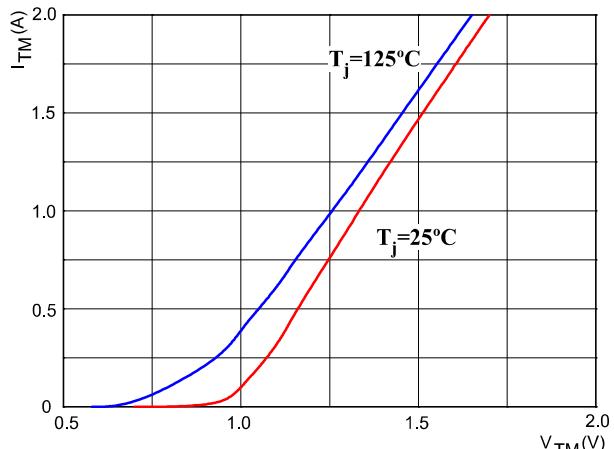


FIG.5: Non-repetitive surge peak on-state current for a sinusoidal pulse with width $t_p < 10\text{ms}$

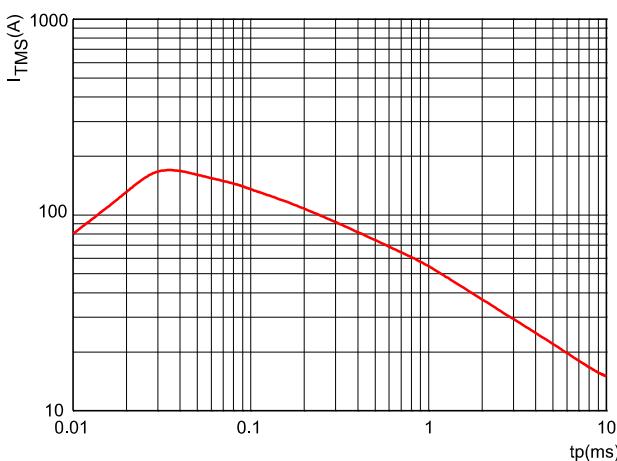
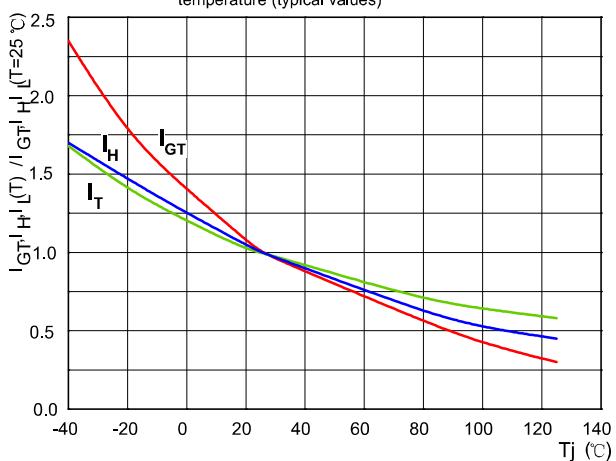
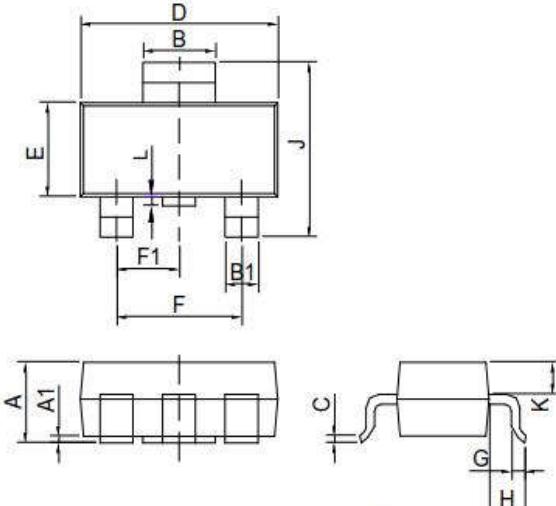


FIG.6: Relative variations of gate trigger current, holding current and latching current versus junction temperature (typical values)



Package Information

SOT-223-2L



The technical drawing illustrates the physical dimensions of the SOT-223-2L package. It includes a top view showing the overall outline, lead spacing (D), lead thickness (B), and lead height (E). A side view shows the lead profile with lead thickness (B1) and lead height (F1). A detailed lead end view shows lead width (A), lead thickness (A1), lead pitch (C), lead length (G), lead hook length (H), and lead hook width (K).

Ref.	Dimensions					
	Millimeters			Inches		
	Min.	Typ.	Max.	Min.	Typ.	Max.
A	1.5	1.6	1.8	0.059	0.063	0.071
A1	0.01	0.06	0.10	0.001	0.002	0.004
B	2.9	3.0	3.1	0.114	0.118	0.122
B1	0.6	0.7	0.8	0.024	0.028	0.031
C	0.22	0.26	0.32	0.009	0.010	0.013
D	6.3	6.5	6.7	0.248	0.256	0.264
E	3.3	3.5	3.7	0.130	0.138	0.146
F		4.6			0.181	
F1		2.3			0.091	
G	0.7	0.9	1.1	0.028	0.035	0.043
H	1.5	1.75	2	0.059	0.069	0.079
J	6.7	7.0	7.3	0.264	0.276	0.287
K		0.9			0.035	
L	0	0.1	0.2	0	0.004	0.008