

**16A 4Quadrants TRIACs**

**Product Summary**

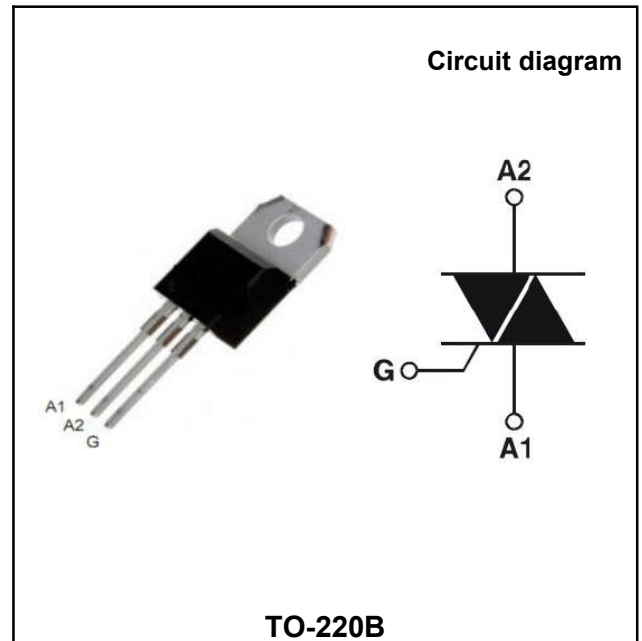
Symbol	Value	Unit
$I_{T(AV)}$	16	A
$V_{DRM} V_{RRM}$	600/800	V
$V_{TM}$	1.55	V

**Features**

With high ability to withstand the shock loading of large current, With high commutation performances, 4 quadrants products especially recommended for use on inductive load.

**Application**

Washing machine, vacuums, massager, solid state relay, AC Motor speed regulation and so on.



**Order Information**

Part Number	Package	Marking	Packing	Packing Quantity
BTB16	TO-220B	BTB16 800B XXXX	box	1000PCS/box

**Absolute maximum ratings (Ta=25°C unless otherwise noted)**

Parameter	Symbol	Value	Unit	
Repetitive peak off-state voltage	$V_{DRM}$	600/800	V	
Repetitive peak reverse voltage	$V_{RRM}$	600/800	V	
RMS on-state current	$I_{T(RMS)}$	16	A	
Non repetitive surge peak on-state current (full cycle, F=50Hz)	$I_{TSM}$	135	A	
I <sup>2</sup> t value for fusing (tp=10ms)	I <sup>2</sup> t	100	A <sup>2</sup> s	
Critical rate of rise of on-state current (I <sub>G</sub> = 2 x I <sub>GT</sub> )	di/dt	I - II - III	50	A
		IV	10	A/ps
Peak gate current	$I_{GM}$	4	A	
Average gate power dissipation	$P_G (AV)$	1	W	
Junction Temperature	$T_J$	-40 ~ +125	°C	
Storage Temperature	$T_{STG}$	-40 ~ +150	°C	

**Electrical characteristics (TA=25°C, unless otherwise noted)**

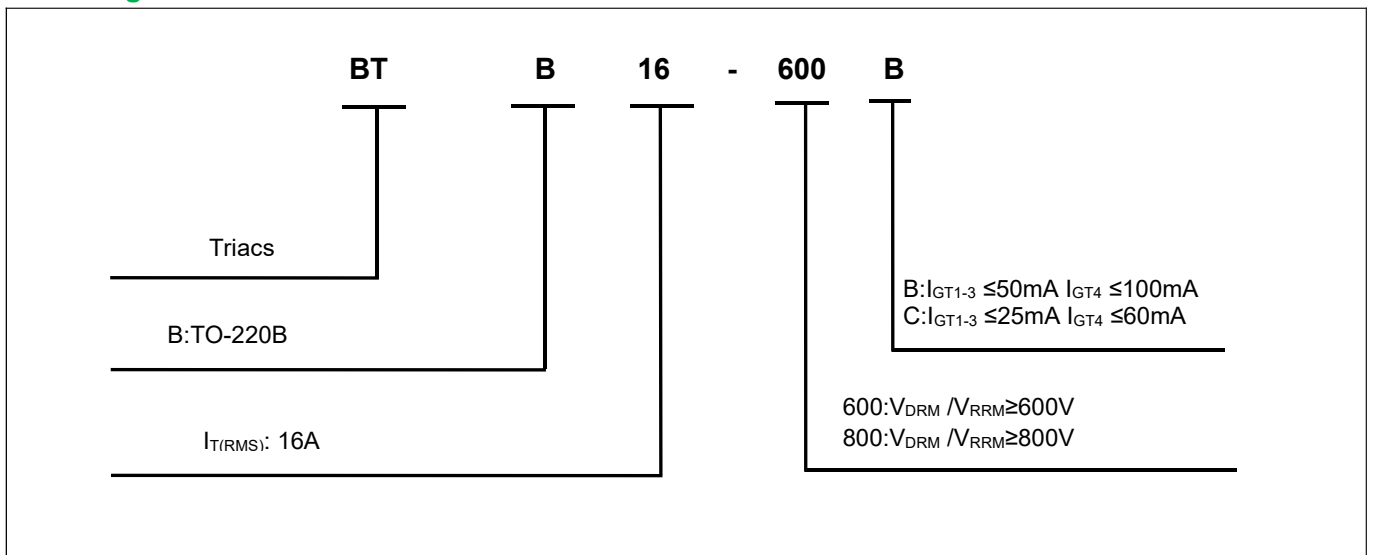
Parameter	Symbol	Test Condition	Value		Unit	
			Min	Max		
Gate trigger current	$I_{GT}$	$V_D=12V$ $RL=33\Omega$ $T_j=25^\circ C$	I-II-III	$\leq 25$	$\leq 50$	<b>mA</b>
			IV	$\leq 60$	$\leq 100$	
Gate trigger voltage	$V_{GT}$	I-II-III-IV	$\leq 1.3$		<b>V</b>	
Gate non-trigger voltage	$V_{GD}$	$V_D = V_{DRM} T_j=125^\circ C$	$\geq 0.2$		<b>V</b>	
latching current	$I_L$	$I_G=1.2I_{GT}$	I-III-IV	$\leq 40$	$\leq 60$	<b>mA</b>
			II	$\leq 80$	$\leq 120$	
Holding current	$I_H$	$I_T = 500mA$	$\leq 25$	$\leq 50$	<b>mA</b>	
Critical-rate of rise of commutation voltage	$dV_D/dt$	$V_D=2/3V_{DRM}$ Gate Open $T_j=125^\circ C$	$\geq 200$	$\geq 400$	<b>V/<math>\mu s</math></b>	

**STATIC CHARACTERISTICS**

Forward "on" voltage	$V_{TM}$	$I_{TM} = 20A$ $t_p=380\mu s$	$\leq 1.55$	<b>V</b>	
Repetitive Peak Off-State Current	$I_{DRM}$	$V_D=V_{DRM}$ $V_R=V_{RRM}$	$T_j=25^\circ C$	$\leq 10$	<b><math>\mu A</math></b>
Repetitive Peak Reverse Current	$I_{RRM}$		$T_j=125^\circ C$	$\leq 1$	<b>mA</b>

**THERMAL RESISTANCES**

Thermal resistance	$R_{th(j-c)}$	Junction to case(AC)	TYP.	1.2	<b><math>^\circ C/W</math></b>
	$R_{th(j-a)}$	Junction to ambient	TYP.	60	<b><math>^\circ C/W</math></b>

**Ordering Information**


Typical Characteristics

FIG.1: Maximum power dissipation versus RMS on-state current (full cycle)

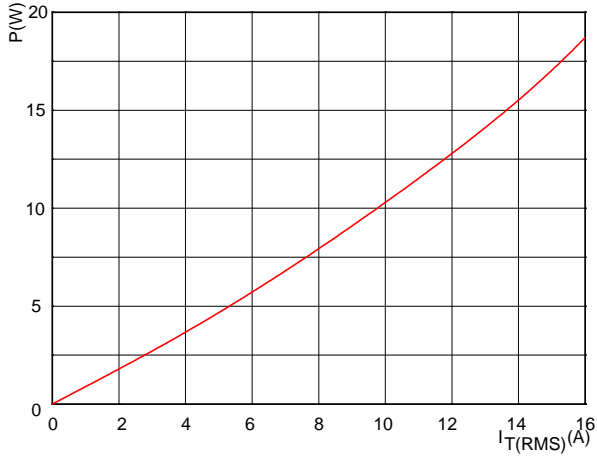


FIG.2: RMS on-state current versus case temperature (full cycle)

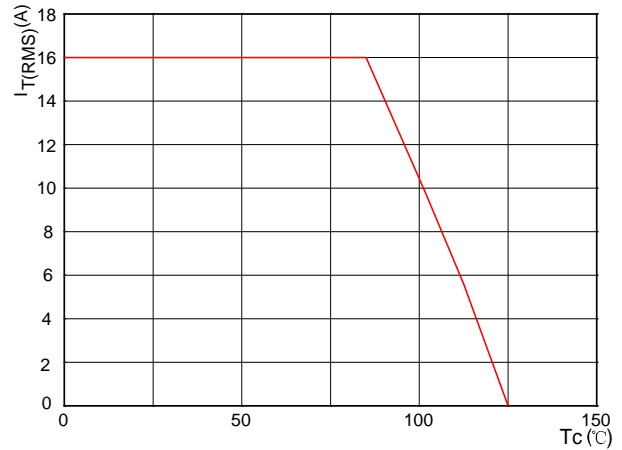


FIG.3: Surge peak on-state current versus number of cycles

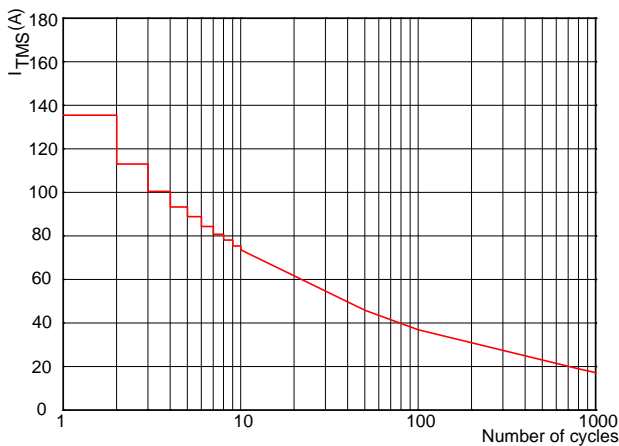


FIG.4: On-state characteristics (maximum values)

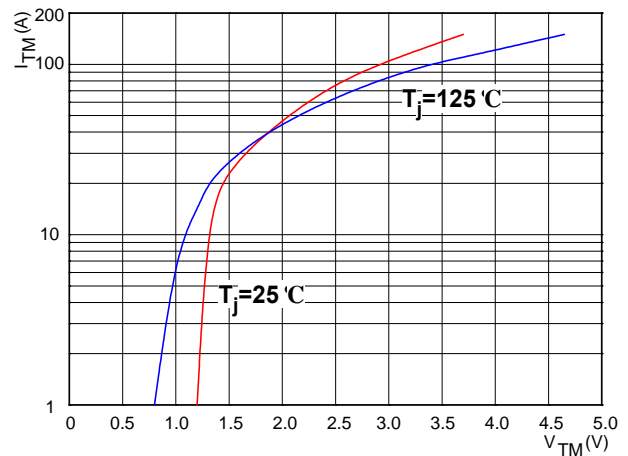


FIG.5: Non-repetitive surge peak on-state current for a sinusoidal pulse with width  $t_p < 10\text{ms}$

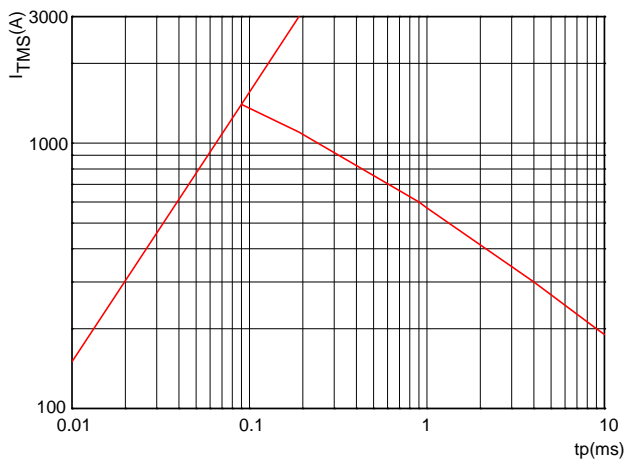
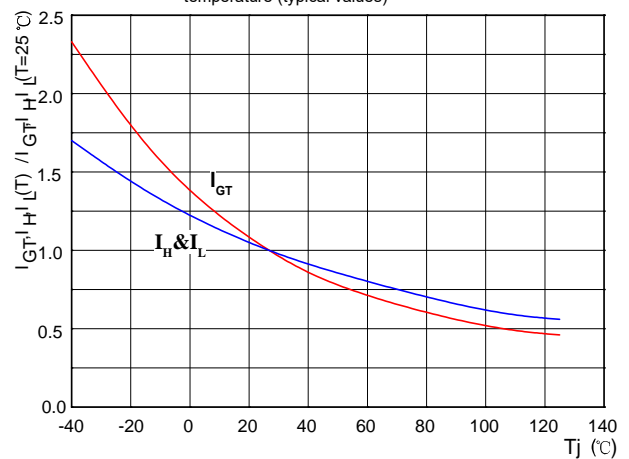
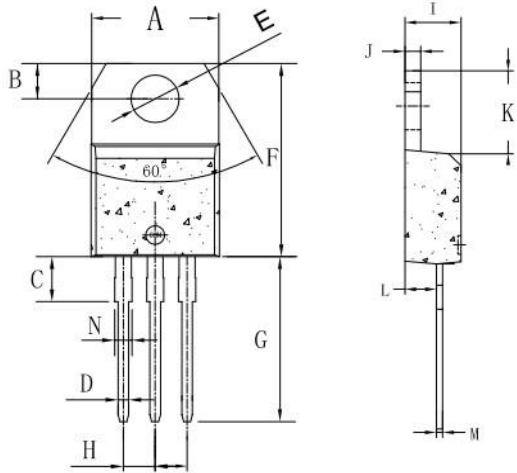


FIG.6: Relative variations of gate trigger current, holding current and latching current versus junction temperature (typical values)



Package Information

**TO-220B**



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	9.8	10.4	0.385	0.409
B	2.65	3.1	0.104	0.122
C	2.8	4.2	0.110	0.165
D	0.7	0.92	0.027	0.036
E	3.75	3.95	0.147	0.155
F	14.8	16.1	0.582	0.633
G	13.05	13.6	0.513	0.535
H	2.4	2.7	0.094	0.106
I	4.38	4.61	0.172	0.181
J	1.15	1.36	0.045	0.053
K	5.85	6.82	0.230	0.268
L	2.35	2.75	0.092	0.108
M	0.35	0.65	0.013	0.025
N	1.18	1.42	0.046	0.055