

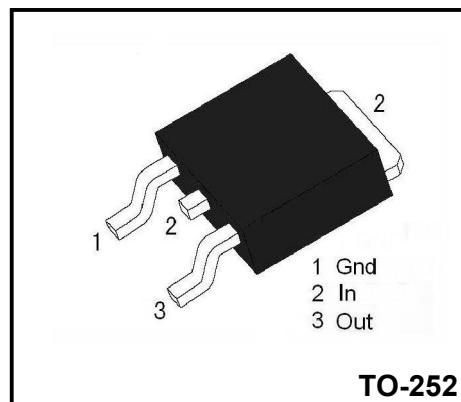
3-Terminal 1A Negative Voltage Regulator

Description

The 79M15 series of 3-Terminal medium current negative voltage regulators are monolithic integrated circuits designed as fixed voltage regulators. These regulators employ internal current limiting, thermal shutdown and safe area compensation making them essentially indestructible.

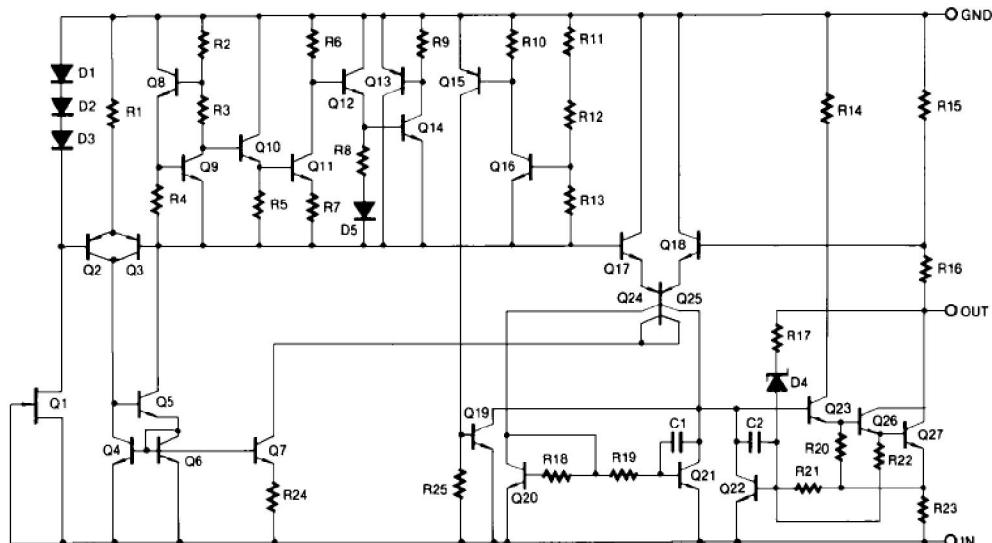
Features

- ◆ No external components required
- ◆ Output current in excess of 1A
- ◆ Internal thermal overload
- ◆ Internal short circuit current limiting
- ◆ Output transistor safe area compensation
- ◆ Output voltages of -15V



TO-252

Internal Block Diagram



Absolute Maximum Ratings

Parameter	Symbol	Value	Unit
Input voltage	V_{IN}	-35	V
Power Dissipation	P_D	1.2	W
Operating Junction Temperature Range	T_j	150	°C
Storage Temperature Range	T_{stg}	-65 ~ 150	°C
Soldering Temperature (10 seconds)	T_{sol}	260	°C

Electrical Characteristics (Ta = 25 °C)

(Refer to the test circuits, $I_O=350mA$, $V_I=-10V$, $C_I=2.2\mu F$, $C_O=1\mu F$ unless otherwise specified)

Parameter	Symbol	Conditions	Value			Unit
			Min	Typ	Max	
Output Voltage	V_O	$T_j = 25^\circ C$	-14.40	-15	-15.60	V
		$I_O = 5mA \sim 1A$ $V_I = -18V \sim -30V$	-14.25	-15	-15.75	
Line Regulation (Note)	ΔV_O	$T_j = 25^\circ C$	$V_I = -17.5V \sim -30V$		100	mV
			$V_I = -18V \sim -28V$		50	
Load Regulation (Note)	ΔV_O	$T_j = 25^\circ C$	$I_O = 5mA \sim 0.75A$		240	mV
			$I_O = 0.25A \sim 1A$		12	
Quiescent Current	I_Q	$T_j = 25^\circ C$			6	mA
Quiescent Current Change	ΔI_Q	$I_O = 5mA \sim 1A$			0.4	mA
		$V_I = -17.5V \sim -30V$			0.6	
Output Voltage Drift	$\Delta V/\Delta T$	$I_O = 5mA$		-0.4		mV/°C
Output Noise Voltage	V_N	$f = 10HZ \sim 100KHZ$		40		μV
Ripple Rejection	RR	$f = 120Hz$, $V_I = -18V \sim -30V$		60		dB
Dropout Voltage	V_D	$T_j = 25^\circ C$, $I_O = 1A$		2.0		V
Short Circuit Current	I_{SC}	$T_j = 25^\circ C$		230		mA
Peak Current	I_{PK}	$T_j = 25^\circ C$		1.8		A

Notes:

Load and line regulation are specified at constant junction temperature. Change in V_O due to heating effects must be taken into account separately. Pulse testing with low duty is used.

Typical Applications

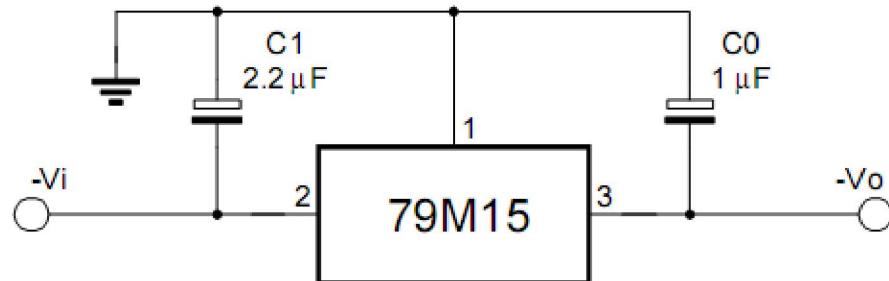


Figure.1 -15V output regulator

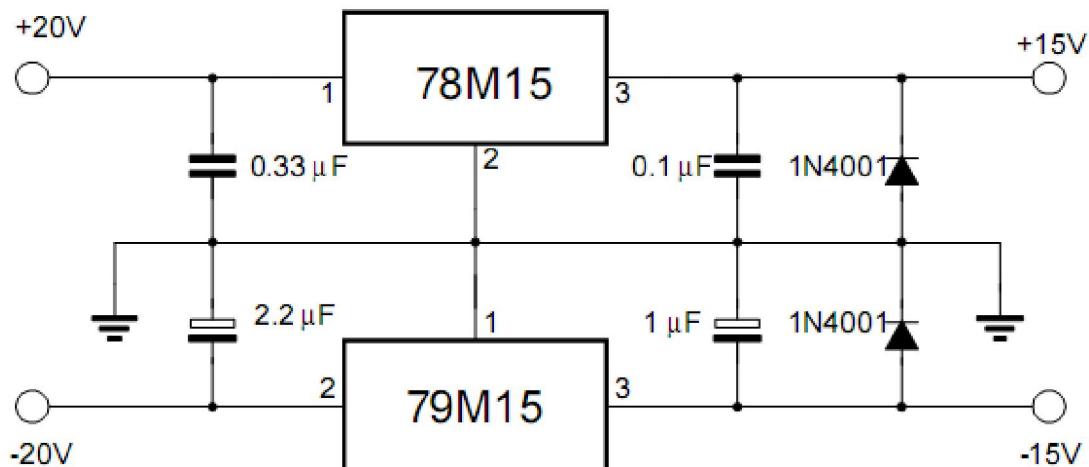


Figure.2 Split power supply($\pm 15V, 0.5A$)

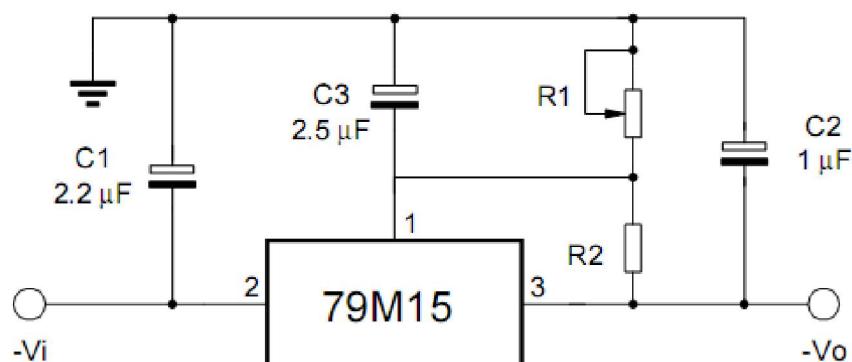


Figure.3 Circuit for increasing output voltage

Typical Characteristics

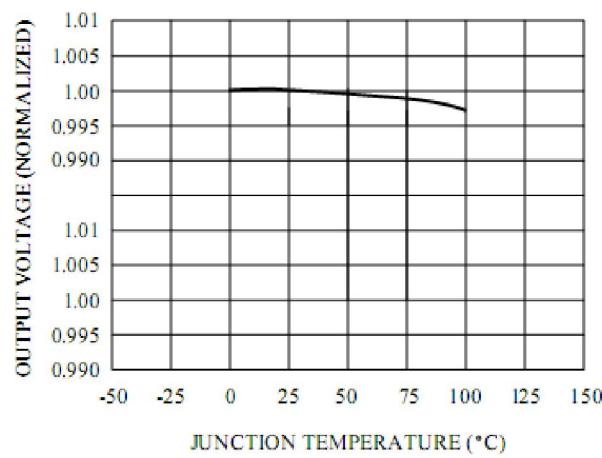


Figure 1. Output voltage vs temperature

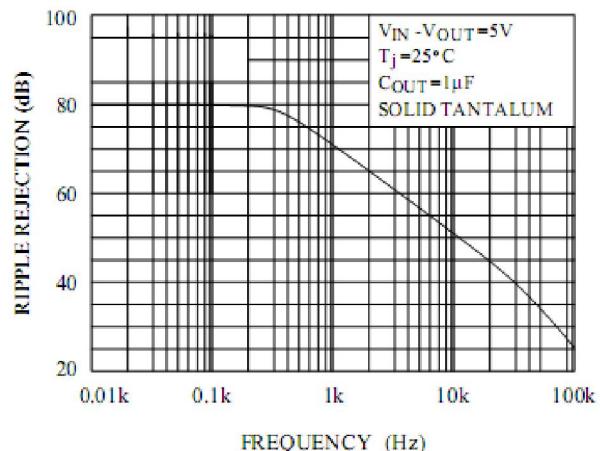


Figure 2. Ripple Rejection

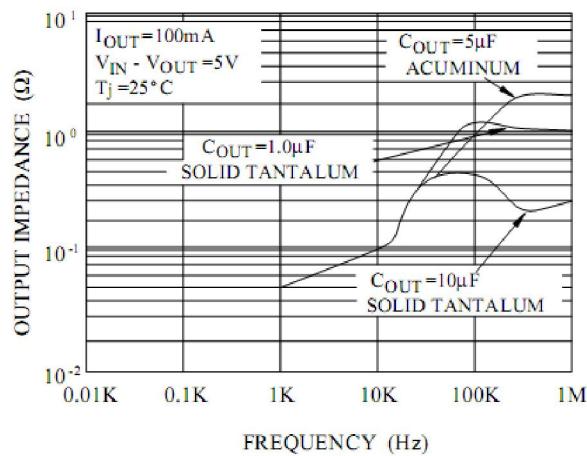


Figure 3. Output impedance

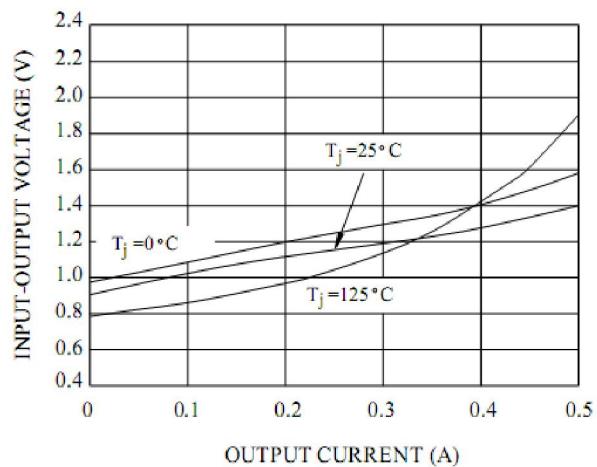


Figure 4. Minimum input-output differential

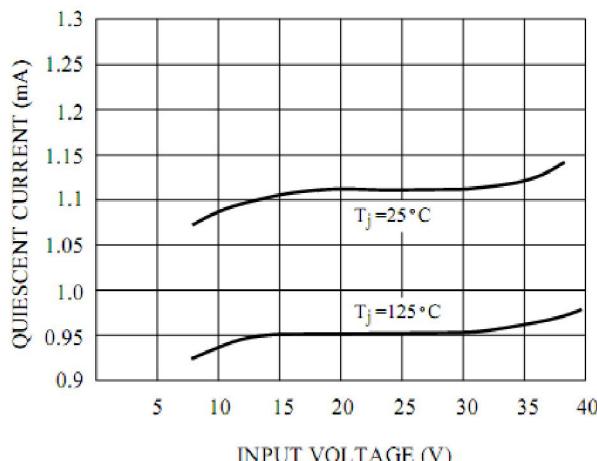


Figure 5. Input Voltage

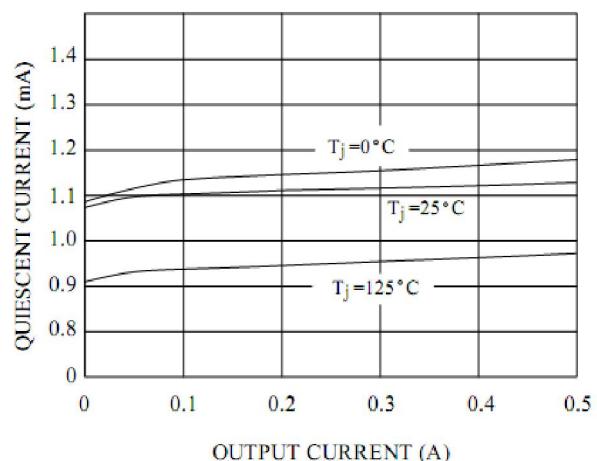


Figure 6. Quiescent current vs load current

Typical Characteristics

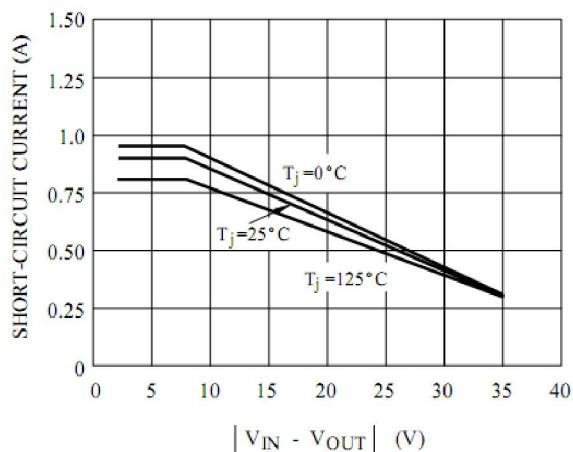


Figure 1. Short-circuit current

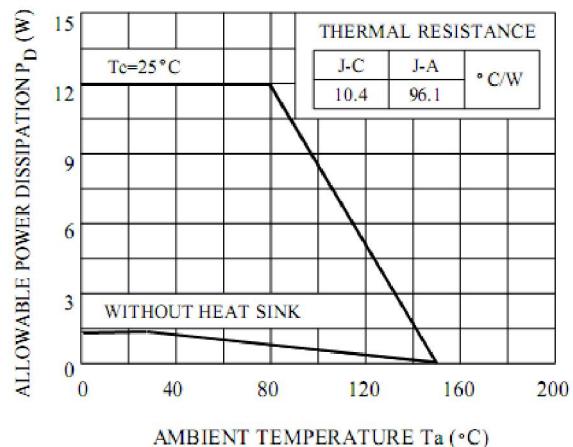
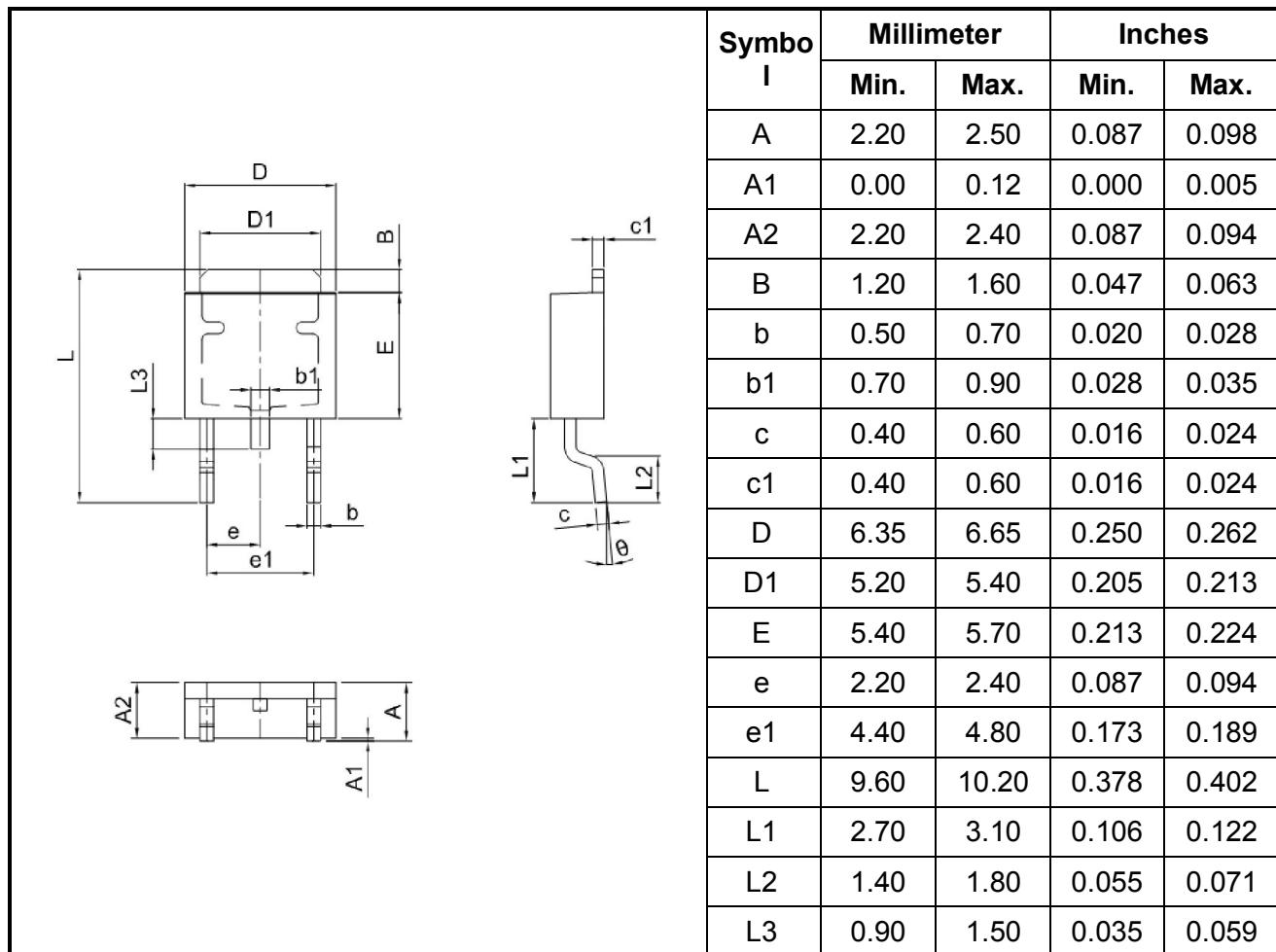


Figure 2. Power Derating Curve

Package Dimensions

TO-252



Product Specification Classification

Part Number	Package	Marking	Pack
79M15	TO-252	79M15 XXXXX	2500PCS/Tape