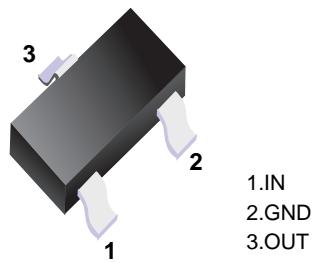


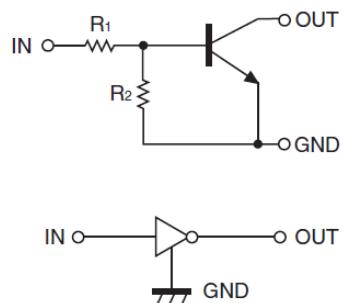
■ Digital Transistor (NPN)

■ Features

- Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors (see equivalent circuit)
- The bias resistors consist of thin-film resistors with complete isolation to allow negative biasing of the input. They also have the advantage of almost completely eliminating parasitic effects
- Only the on/off conditions need to be set for operation, making device design easy



■ Simplified outline(SOT-523)



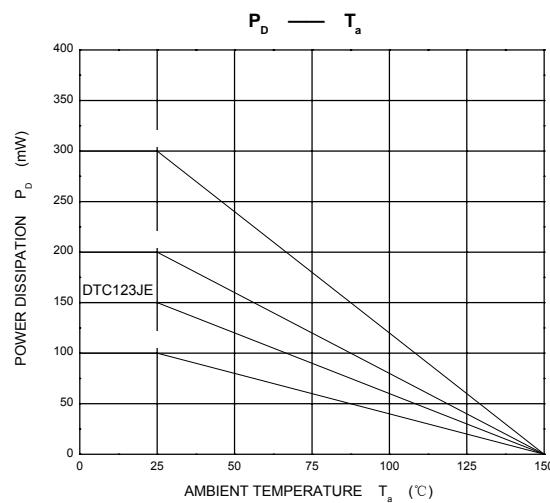
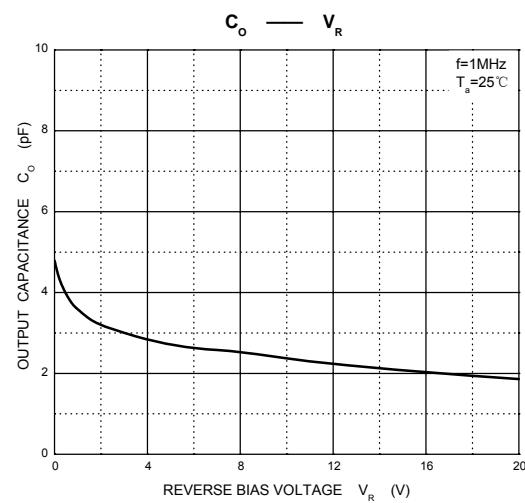
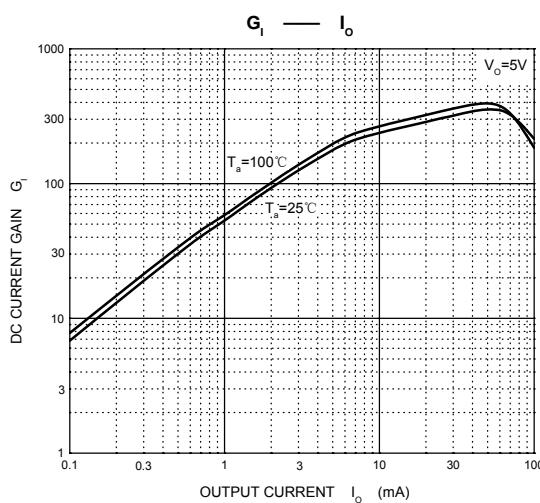
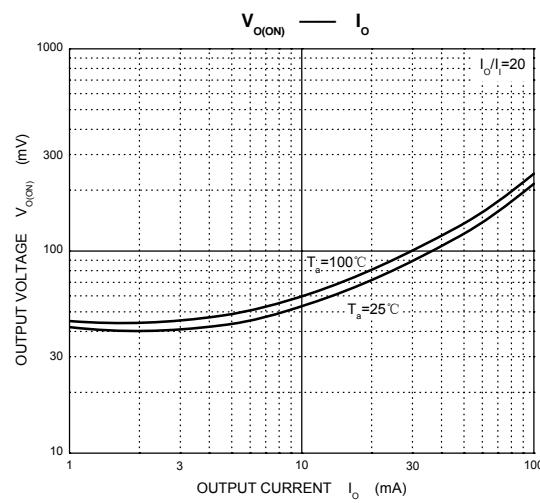
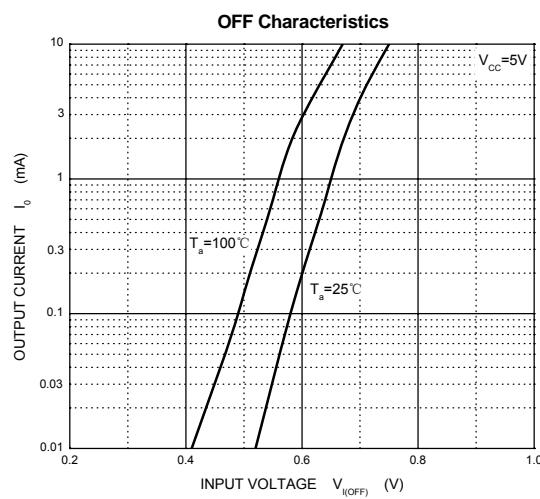
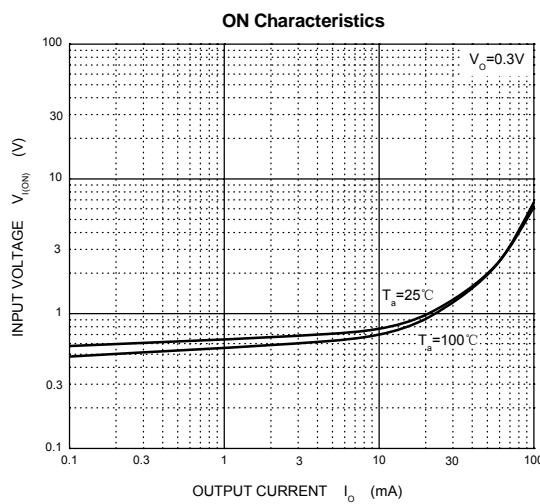
Equivalent Circuit

MAXIMUM RATINGS(Ta=25°C unless otherwise noted)

Symbol	Parameter	DTC123JE		Unit
V _{cc}	Supply Voltage	50		V
V _{IN}	Input Voltage	-5~+12		V
I _o	Output Current	100		mA
P _D	Power Dissipation	150		
T _j	Junction Temperature	150		°C
T _{stg}	Storage Temperature	-55~+150		°C

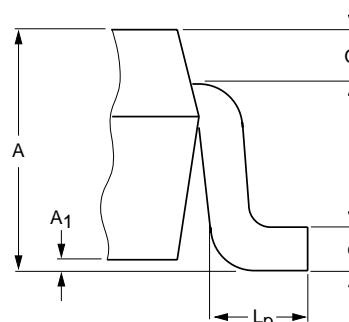
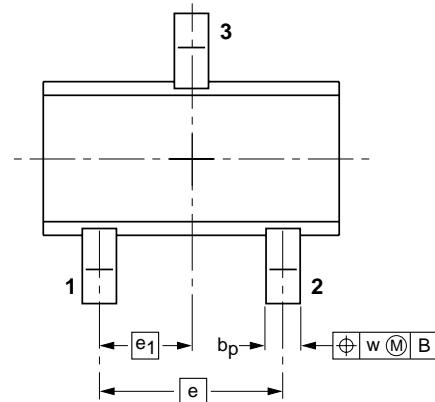
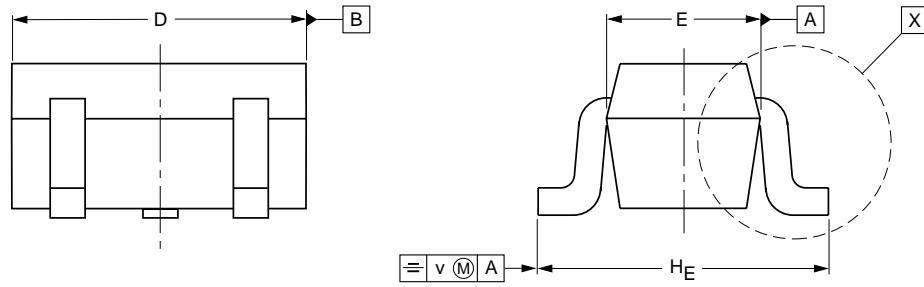
ELECTRICAL CHARACTERISTICS (Ta=25°C unless otherwise specified)

Parameter	Symbol	Conditions	Min	Typ	Max	Unit
Input voltage	V _{I(off)}	V _{cc} =5V, I _o =100μA	0.5			V
	V _{I(on)}	V _O =0.3V, I _o =5mA			1.1	V
Output voltage	V _{O(on)}	I _o /I _i =5mA/0.25mA		0.1	0.3	V
Input current	I _i	V _i =5V			3.6	mA
Output current	I _{O(off)}	V _{cc} =50V, V _i =0			0.5	μA
DC current gain	G _i	V _O =5V, I _o =10mA	80			
Input resistance	R ₁		1.54	2.2	2.86	kΩ
Resistance ratio	R ₂ /R ₁		17	21	26	
Transition frequency	f _T	V _O =10V, I _o =5mA, f=100MHz		250		MHz



Package Outline

SOT-523



detail X

0 0.5 1 mm
scale

DIMENSIONS (mm are the original dimensions)

UNIT	A	A ₁ max	b _p	c	D	E	e	e ₁	H _E	L _p	Q	v	w
mm	0.95 0.60	0.1	0.30 0.15	0.25 0.10	1.8 1.4	0.9 0.7	1	0.5	1.75 1.45	0.45 0.15	0.23 0.13	0.2	0.2

Summary of Packing Options

Package	Packing Description	Packing Quantity	Industry Standard
SOT-523	Tape/Reel,7"reel	3000	EIA-481-1