

#### SAMXON BRAND ALUMINUM ELECTROLYTIC CAPACITORS

# PRODUCT SPECIFICATION 規格書

CUSTOMER: DATE:

(客戶): 志盛翔 (日期):2020-4-2

CATEGORY (品名) : ALUMINUM ELECTROLYTIC CAPACITORS

DESCRIPTION (型号) : GF 16V680μF(φ8X16)

VERSION (版本) : 01

Customer P/N :

SUPPLIER :

SUPPLIER								
PREPARED (拟定)	CHECKED (审核)							
赵安平	刘渭清							

CUST	CUSTOMER								
APPROVAL (批准)	SIGNATURE (签名)								

#### ELECTROLYTIC CAPACITOR SPECIFICATION GF SERIES

	SPECIFICATION					ATION HIS	TORY
D.	Б.	GF SERIE					
Rev.	Date	Mark	Page	Contents	Purpose	Drafter	Approver

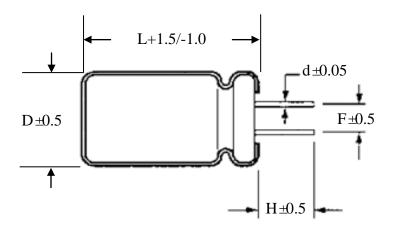
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Table 1 Product Dimensions and Characteristics

Unit: mm



Shape Code	D	8
Shape Code	L	16
	F	3.5
CB Type	Н	3.5
	d	0.5

No.	SAMXON Part No.	WV (Vdc)	Cap.	Cap tolerance	Temp. range( $^{\circ}$ C)	tan <b>δ</b> (120Hz,	Leakage Current	Max Ripple Current at 105°C 100kHz	Impedance at 20°C 100kHz	Load lifetime		ension (mm)		Sleeve
	Tart No.	(vuc)	(μ1')	tolerance	range( C)	20℃)		(mA rms)		(Hrs)	D×L	F	фd	
1	EGF687M1CF16CB**P1	16	680	-20%~+20%	-40~105	0.16	108.8	840	0.087	3000	8X16	3.5	0.5	PET

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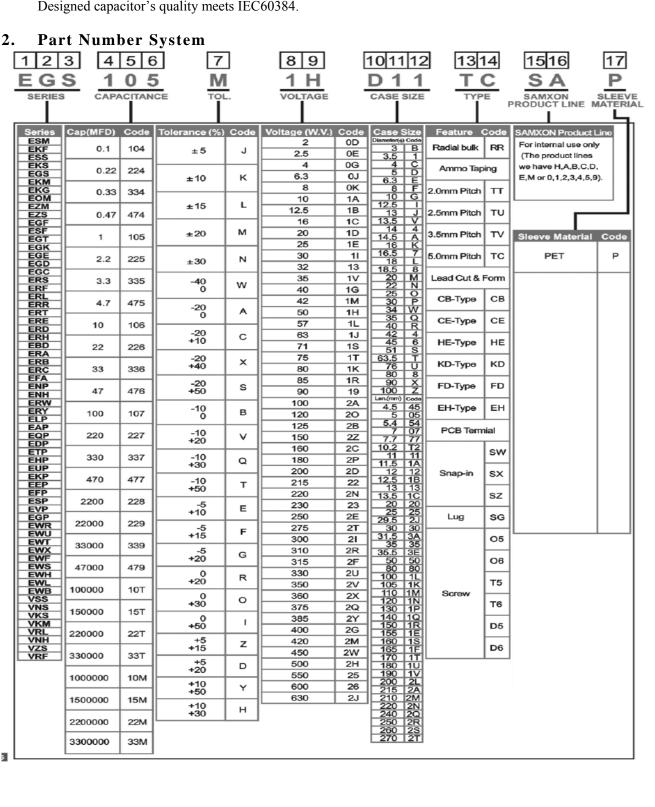
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#### 1. Application

This specification applies to polar Aluminum electrolytic capacitor (foil type) used in electronic equipment. Designed capacitor's quality meets IEC60384.



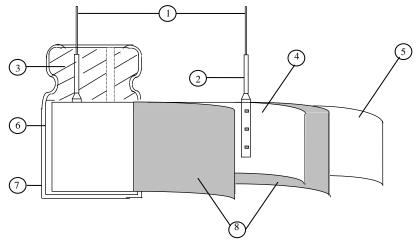
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#### 3. Construction

Single ended type to be produced to fix the terminals to anode and cathode foil, and wind together with paper, and then wound element to be impregnated with electrolyte will be enclosed in an aluminum case. Finally sealed up tightly with end seal rubber, then finished by putting on the vinyl sleeve.



No	Component	Material
1	Lead line	Tinned CP wire (Pb Free)
2	Terminal	Aluminum wire
3	Sealing Material	Rubber
4	Al-Foil (+)	Formed aluminum foil
5	Al-Foil (-)	Etched aluminum foil or formed aluminum foil
6	Case	Aluminum case
7	Sleeve	PET
8	Separator	Electrolyte paper

#### 4. Characteristics

#### Standard atmospheric conditions

Unless otherwise specified, the standard range of atmospheric conditions for making measurements and tests are as follows:

Ambient temperature :15°C to 35°C
Relative humidity : 45% to 85%
Air Pressure : 86kPa to 106kPa

If there is any doubt about the results, measurement shall be made within the following conditions:

Ambient temperature :  $20^{\circ}\text{C} \pm 2^{\circ}\text{C}$ Relative humidity : 60% to 70%Air Pressure : 86kPa to 106kPa

#### Operating temperature range

The ambient temperature range at which the capacitor can be operated continuously at rated voltage See table 1 temperature range.

As to the detailed information, please refer to table 2.

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e 2				PEDEO	D. C. N.C.	<u></u>			
ITEM				PERFO	KMANC	EE			
Rated voltage (WV)			ı				ı	T	
	WV (V.DC)	6.3	10	16	25	35	50	63	100
Surge voltage (SV)	SV (V.DC)	8	13	20	32	44	63	79	125
Nominal capacitance (Tolerance)	Measuring F Measuring V Measuring T < Criteria>	oltage emperat	: N ure : 20	ot more t 0±2°C	han 0.5V				
Leakage current	<pre><condition> Connecting the capacitor with a protective resistor <math>(1k\Omega \pm 10\Omega)</math> in series for 2 minutes, and then, measure Leakage Current. </condition></pre> <pre><criteria></criteria></pre> Refer to Table 1								
tanδ	<condition> See 4.2, Norm Capacitance, for measuring frequency, voltage and temperature.  <criteria> Refer to Table 1</criteria></condition>								
Terminal strength	Tensile Street Fixed the conseconds.  Bending Street Fixed the conseconds.  Diamet 0.5n	ength of apacitor ength of apacitor, 2~3 second er of lead	f Termina applied f applied f ands, and d wire	lls. orce to be then ben	ent the te t it for 90 ile force 1 (kgf)	rminal (1 )° to its (	l~4 mm toriginal properties of the second se	from the position vector of force N (gf) (0.25)	rubber) fo
	Voltage (WV)  Surge voltage (SV)  Nominal capacitance (Tolerance)  Leakage current  tanδ	Rated voltage (WV)  Surge voltage (SV)  Nominal capacitance (Tolerance)  Leakage current  Condition> Connecting to minutes, and c	Rated voltage (WV)  Surge voltage (SV)    VV (V.DC)   6.3     SV (V.DC)   8	Rated voltage (WV)  Surge voltage (SV)    WV (V.DC)   6.3   10     SV (V.DC)   8   13      Condition>   Measuring Frequency : 12     Measuring Voltage : Now Measuring Temperature : 20     Criteria> Shall be within the specified cannot be within the	Rated voltage (WV)  Surge voltage (SV)    WV (V.DC)   6.3   10   16     SV (V.DC)   8   13   20     SV (V.DC)   8   13   20     Nominal capacitance (Tolerance)   Measuring Frequency   120Hz ± 12     Measuring Voltage   Not more to the	Rated voltage (WV)  Surge voltage (SV)  **Condition>** Measuring Frequency : 120Hz±12Hz Measuring Voltage : Not more than 0.5V Measuring Temperature : 20±2°C  **Criteria>** Shall be within the specified capacitance tolerant connecting the capacitor with a protective resminutes, and then, measure Leakage Current.  **Condition>** Condition>** Refer to Table 1**  **Condition>** See 4.2, Norm Capacitance, for measuring frequence of the capacitor of the capacitor with a protective resminutes, and then, measure Leakage Current.  **Condition>** See 4.2, Norm Capacitance, for measuring frequence of the capacitor of the capacitor, applied force to the term seconds. Bending Strength of Terminals. Fixed the capacitor, applied force to bent the term seconds. Bending Strength of Terminals. Fixed the capacitor, applied force to bent the term seconds. Bending Strength of Terminals. Fixed the capacitor, applied force to bent the term seconds. Diameter of lead wire terminal strength of the capacitor of the seconds. Terminal strength of the capacitor of the seconds of the	Rated voltage (WV)  Surge voltage (SV)    WV (V.DC)   6.3   10   16   25   35     SV (V.DC)   8   13   20   32   44      Condition>   Measuring Frequency : 120Hz±12Hz     Measuring Voltage : Not more than 0.5Vrms     Measuring Temperature : 20±2°C     Criteria> Shall be within the specified capacitance tolerance.	Rated voltage (WV)  Surge voltage (SV)    WV (V.DC)   6.3   10   16   25   35   50     SV (V.DC)   8   13   20   32   44   63     Weasuring Frequency : 120Hz±12Hz     Measuring Voltage : Not more than 0.5Vrms     Measuring Temperature : 20±2°C     Criteria> Shall be within the specified capacitance tolerance.    Condition>	Rated voltage (WV)  Surge voltage (SV)    WV (V.DC)   6.3   10   16   25   35   50   63

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		<condition></condition>			(80)			m:		
		STEP	Testii	<u> </u>	erature(°C)			Time		
		1		$20\pm 2$			to reach t			
		2		-40(-25)			to reach t			
		3		$20\pm 2$	2	Time	to reach t	thermal (	equilibri	um
		4		$105 \pm$	2	Time	to reach t	thermal (	equilibri	um
		5		$20 \pm 2$	2	Time	to reach t	thermal	equilibri	um
		<criteria></criteria>				•				
		a. tanδ shall b	e with	in the lim	it of Item	4.4The 1	eakage cu	irrent me	easured s	hall not
		more than 8 tim	nes of i	ts specifie	ed value.					
	Temperature	b. In step 5, tar	nδ sha	all be with	hin the lin	nit of Iter	n 4.4The	leakage	current	shall not
1.0	characteristi	more than the s	pecifie	d value.						
4.6	cs	c. At-40°C (-25	5°C), iı	mpedance	(z) ratio	shall not	exceed th	e value	of the fo	llowing
		table.			, ,		1	r		1
		Working Voltage		6.3	10	16	25	35	50	63
		Z-25°C/Z+20	$^{\circ}\mathbb{C}$	4	3	2	2	2	2	2
		Z-40°C/Z+20	$^{\circ}$	8	6	4	3	3	3	3
			~ ·	100	· 1		•			•
		Working Voltage		100						
		Z-25°C/Z+20°		2						
		Z-40°C/Z+20°	$^{\circ}$ C	3						
		For capacitance value > 1000 $\mu$ F, Add 0.5 per another 1000 $\mu$ F for Z-25/Z+20 $^{\circ}$ C,								
		For capacitance	value	$> 1000 \mu$	F, Add 0.	5 per and	ther 1000	µ F for	Z-25/Z+	-20℃,
		•			Add 1.0	) per ano	ther 1000	μ F for		
		For capacitance Capacitance, tand			Add 1.0	) per ano	ther 1000	μ F for		
		Capacitance, tand			Add 1.0	) per ano	ther 1000	μ F for		
		Capacitance, tand	δ, and	d impedan	Add 1.0	per ano e measur	ther 1000 red at 120	F for the Hz.	Z-40°C/Z	Z+20℃.
		Capacitance, tand <condition> According to IE</condition>	δ, and	d impedar 34-4No.4.	Add 1.0 nce shall b	per ano be measured ls, The ca	ther 1000 red at 120 apacitor is	Hz.	Z-40°C/Z	Z+20°C.
		Capacitance, tand <b>Condition&gt;</b> According to IE $105  \text{C} \pm 2 \text{ with}$	δ, and	d impedar 34-4No.4. as voltage	Add 1.0 nce shall b	per ano be measured als, The cated ripp	red at 120 red at 120 repacitor is	Hz. s stored a	Z-40°C/Z	Z+20°C.  erature of the sum of
		Capacitance, tand <condition> According to IE  105 °C ± 2 with DC and ripple</condition>	δ, and CC6038 DC bid peak v	d impedan 34-4No.4. as voltage	Add 1.0 nce shall be 13 method e plus the hall not es	ds, The carated ripp	red at 120 apacitor is ale current erated w	F for DHz.  s stored at for Taboring vorking v	Z-40°C/Z  at a temp ble 1. (Trivoltage)	Z+20°C.  erature of the sum of then the
	Load	Capacitance, tand <b>Condition&gt;</b> According to IE $105  \text{C} \pm 2 \text{ with}$	δ, and CC6038 DC bill peak when the tester	d impedar 34-4No.4. as voltage voltage shed after 16	Add 1.0 ance shall be 13 method e plus the mall not e. 6 hours red	ds, The carated ripp	red at 120 apacitor is ale current erated w	F for DHz.  s stored at for Taboring vorking v	Z-40°C/Z  at a temp ble 1. (Trivoltage)	Z+20°C.  erature of the sum of then the
4.7	Load life	Capacitance, tand <condition> According to IE  105 °C ±2 with DC and ripple product should be</condition>	δ, and CC6038 DC bill peak when the tester	d impedar 34-4No.4. as voltage voltage shed after 16	Add 1.0 ance shall be 13 method e plus the mall not e. 6 hours red	ds, The carated ripp	red at 120 apacitor is ale current erated w	F for DHz.  s stored at for Taboring vorking v	Z-40°C/Z  at a temp ble 1. (Trivoltage)	Z+20°C.  erature of the sum of then the
4.7		Capacitance, tand <condition> According to IE  105 ℃ ±2 with  DC and ripple product should be result should me</condition>	δ, and CC6038  DC bit peak where the eet the	d impedar 34-4No.4. as voltage voltage shed after 16 following	Add 1.0 nce shall be 13 method e plus the mall not ex 6 hours red g table:	ds, The carated ripp exceed the	ther 1000 red at 120 apacitor is ale current e rated w time at at	F for DHz.  s stored at for Taboring vorking v	Z-40°C/Z  at a temp ble 1. (Trivoltage)	Z+20°C.  erature of the sum of then the
4.7	life	Capacitance, tand <condition> According to IE  105 ℃ ±2 with  DC and ripple product should be result should me  <criteria></criteria></condition>	δ, and CC6038 DC bit peak vibe teste eet the	d impedar 44-4No.4. as voltage voltage shed after 16 following	Add 1.0 nce shall be 13 method e plus the mall not explusive to hours reagge table:	ds, The carated ripp exceed the	ther 1000 red at 120 apacitor is ale current e rated w time at at	F for PHZ.  s stored at for Tabording with the store of t	Z-40°C/Z  at a temp ble 1. (Trivoltage)	Z+20°C.  erature of the sum of then the
4.7	life	Capacitance, tand  Condition> According to IE  105 °C ±2 with DC and ripple product should be result should me Criteria> The characterist	δ, and CC6038 DC bit peak who tested the current	d impedar 34-4No.4. as voltage voltage shed after 16 following	Add 1.0 ance shall be 13 method e plus the mall not e. 6 hours red g table:  e following Value in	ds, The carated ripp exceed the covering g require 4.3 shall	apacitor is erated writing at attempts.	F for DHz.  s stored at for Table orking with mospher	Z-40°C/Z  at a temp ble 1. (Trivoltage)	Z+20°C.  erature of the sum of then the
4.7	life	Capacitance, tand <condition> According to IE  105 °C ±2 with  DC and ripple product should be result should me  <criteria> The characterist  Leakage</criteria></condition>	δ, and CC6038 DC bit peak who tested the current	d impedar 34-4No.4. as voltage voltage shed after 16 following	Add 1.0 nce shall be 13 method e plus the mall not es 6 hours red g table:  e following Value in Within =	ds, The carated ripp exceed the covering eg require 4.3 shall	apacitor is a pacitor is a paci	F for DHz.  s stored at for Tab corking compospher	Z-40°C/Z at a temp ble 1. (T voltage) ric condit	erature of the sum of Then the tions. The
4.7	life	Capacitance, tand <condition> According to IE  105 ℃ ±2 with DC and ripple product should be result should me  <criteria> The characterist  Leakage  Capacitant tano</criteria></condition>	δ, and CC6038  DC bile peak who be tested eet the curren nce Ch	d impedar 34-4No.4. as voltage voltage shed after 16 following	Add 1.0 nce shall be 13 method e plus the mall not end to hours red g table:  e following Value in Within and Not more	ds, The carated ripp exceed the covering require 4.3 shall 25% of	apacitor is a pacitor in the paci	F for DHz.  s stored a t for Tab corking with the store of the store o	Z-40°C/Z  at a tempole 1. (To voltage)  ric condite	erature of the sum of Then the tions. The
4.7	life	Capacitance, tand <condition> According to IE  105 ℃ ±2 with DC and ripple product should be result should me  <criteria> The characterist  Leakage  Capacitan</criteria></condition>	δ, and CC6038  DC bile peak who be tested eet the curren nce Ch	d impedar 34-4No.4. as voltage voltage shed after 16 following	Add 1.0 nce shall be 13 method e plus the mall not end to hours red g table:  e following Value in Within and Not more	ds, The carated ripp exceed the covering require 4.3 shall 25% of	apacitor is a pacitor is a pacitor is a rated where the rated where the pacitor is a pacitor in the pacitor in	F for DHz.  s stored a t for Tab corking with the store of the store o	Z-40°C/Z  at a tempole 1. (To voltage)  ric condite	erature of the sum of Then the tions. The
4.7	life	Capacitance, tand <condition> According to IE  105 ℃ ±2 with DC and ripple product should be result should me  <criteria> The characterist  Leakage  Capacitant tano</criteria></condition>	δ, and CC6038  DC bile peak who be tested eet the curren nce Ch	d impedar 34-4No.4. as voltage voltage shed after 16 following	Add 1.0 nce shall be 13 method e plus the mall not end to hours red g table:  e following Value in Within and Not more	ds, The carated ripp exceed the covering require 4.3 shall 25% of	apacitor is a pacitor in the paci	F for DHz.  s stored a t for Tab corking with the store of the store o	Z-40°C/Z  at a tempole 1. (To voltage)  ric condite	erature of the sum of Then the tions. The
4.7	life	Capacitance, tand <condition> According to IE  105 ℃ ±2 with  DC and ripple product should be result should me  <criteria> The characterist  Leakage  Capacitant tano  Appearar</criteria></condition>	δ, and CC6038  DC bit peak vibe teste eet the curren nce Ch	d impedar 34-4No.4. as voltage voltage shed after 16 following Il meet the t	Add 1.0 mee shall be 13 method e plus the mall not e: 6 hours red g table:  e following Value in Within = Not more There sh	ds, The carated ripp exceed the covering exceed the covering 4.3 shall 25% of e than 15	apacitor is a pacitor in the paci	F for DHz.  s stored a t for Tab corking with the store	Z-40°C/Z  at a temp ble 1. (Trivoltage) ric conditions ed value.  olyte.	erature of the sum of Then the ions. The
4.7	life	Capacitance, tand <condition> According to IE  105 °C ±2 with DC and ripple product should be result should me  <criteria> The characterists  Leakage Capacitant tano Appearar</criteria></condition>	δ, and CC6038  DC bit peak who be tested eet the current nee Chance Cha	d impedar 34-4No.4. as voltage voltage shed after 16 following Il meet the t nange	Add 1.0 mee shall be 13 method e plus the mall not e. 6 hours red g table:  e following Value in Within End Not more there should be within End of the should be within En	ds, The carated ripper accept the covering description of	apacitor is apacitor is ale current erated writine at attements.  be satisficinitial various of the leakage of the declaration at a termination of the leakage of the declaration at a termination of the declaration at a termination of the declaration of the dec	F for particular parti	Z-40°C/Z  at a temp ble 1. (To voltage) ric condite  ed value.  blyte.	erature of the sum of Then the tions. The
4.7	life	Capacitance, tand <condition> According to IE  105 ℃ ±2 with DC and ripple product should be result should me  <criteria> The characterist  Leakage Capacitant tano Appearar  <condition> The capacitors are</condition></criteria></condition>	δ, and CC6038 DC bit peak who be tested eet the current nce Chance re then rs. Follows	d impedar 34-4No.4. as voltage shed after 16 following Ill meet the tange stored wire owing this	Add 1.0 mee shall be 13 method e plus the mall not e 6 hours red g table:  e following Value in Within 15 Not more the shall not voltage in the no voltage period to the shall not be 1.0 meethod in the shall not be 1.0 meee	ds, The carated rippaceed the covering 4.3 shall = 25% of all be no age appliche capac	apacitor is alle current erated writine at attribute satisfic initial various of the leakage of the end at a teritors shall	F for partial F for Table F fo	Z-40°C/Z  at a temp ble 1. (T  voltage) ric condit  ed value.  blyte.  re of 105  oved from	±2°C form the test
4.7	life	Capacitance, tand <condition> According to IE  105 ℃ ±2 with  DC and ripple product should be result should me  <criteria> The characterist  Leakage Capacitan tano Appearar  <condition> The capacitors ar 1000+48/0 hour</condition></criteria></condition>	δ, and CC6038 DC bit peak who be tested eet the current nce Chance re then rs. Follow allow	d impedar 34-4No.4. as voltage shed after 16 following Ill meet the tange stored wire owing this red to stale	Add 1.0  ance shall be the plus the phall not expluse the phall not expluse the phall not expluse table:  by the phall not expluse the phall not expluse the phall not expluse the phall not expluse the phall not more than the phall not more than the phall not expluse the phall not explu	ds, The carated rippexceed the covering description of the	apacitor is ale current e rated whime at attribute satisficial various of the leakage of the lea	F for PHz.  S stored at for Table orking with the store of the store o	at a tempole 1. (Tovoltage) ric conditions of 105 oved from the following shours.	±2°C form the test
4.7	life test	Capacitance, tand <condition> According to IE  105 ℃ ±2 with DC and ripple product should be result should me <criteria> The characterist  Leakage Capacitan tano Appearar  <condition> The capacitors and 1000+48/0 hour chamber and be</condition></criteria></condition>	δ, and CC6038 DC bit peak who be tested eet the current nice Chance re then rs. Follow et allow et ed to	d impedar 34-4No.4. as voltage shed after 16 following Ill meet the tange stored wire owing this a series 1	Add 1.0 nce shall be 13 method the plus the phall not extended to hours recognized the following table:  Within the properties of the phall not extended the pha	ds, The carated ripper accept the covering desired the covering desired the covering desired d	ther 1000 red at 120	F for DHz.  S stored at for Table orking with a specific of electron to the store of the store o	at a tempole 1. (Tovoltage) ric conditions of 105 oved from the conditions of 105 oved from th	erature of the sum of Then the tions. The the tions the test Next they d voltage
	life test	Capacitance, tand <condition> According to IE  105 ℃ ±2 with DC and ripple product should be result should me  <criteria> The characterists  Leakage Capacitant tano Appearar  <condition> The capacitors ar 1000+48/0 hour chamber and be shall be connece</condition></criteria></condition>	δ, and CC6038 DC bit peak who be tested eet the current nice Chance re then rs. Follow et allow et ed to	d impedar 34-4No.4. as voltage shed after 16 following Ill meet the tange stored wire owing this a series 1	Add 1.0 nce shall be 13 method the plus the phall not extended to hours recognized the following table:  Within the properties of the phall not extended the pha	ds, The carated ripper accept the covering desired the covering desired the covering desired d	ther 1000 red at 120	F for DHz.  S stored at for Table orking with a specific of electron to the store of the store o	at a tempole 1. (Tovoltage) ric conditions of 105 oved from the conditions of 105 oved from th	erature of the sum of Then the tions. The the tions the test Next they d voltage
	life test Shelf life	Capacitance, tand <condition> According to IE  105 ℃ ±2 with  DC and ripple product should be result should me  <criteria> The characterist  Leakage Capacitant tano Appearar  <condition> The capacitors ar 1000+48/0 hour chamber and be shall be connect applied for 30m</condition></criteria></condition>	δ, and CC6038 DC bit peak who be tested eet the current nice Chance re then rs. Follow et allow et ed to	d impedar 34-4No.4. as voltage shed after 16 following Ill meet the tange stored wire owing this a series 1	Add 1.0 nce shall be 13 method the plus the phall not extended to hours recognized the following table:  Within the properties of the phall not extended the pha	ds, The carated ripper accept the covering desired the covering desired the covering desired d	ther 1000 red at 120	F for DHz.  S stored at for Table orking with a specific of electron to the store of the store o	at a tempole 1. (Tovoltage) ric conditions of 105 oved from the conditions of 105 oved from th	erature of the sum of Then the tions. The the tions the test Next they d voltage
	life test Shelf life	Capacitance, tand <condition> According to IE  105 ℃ ±2 with  DC and ripple product should be result should me  <criteria> The characterist  Leakage Capacitant tano Appearar  <condition> The capacitors ar 1000+48/0 hour chamber and be shall be connect applied for 30m</condition></criteria></condition>	δ, and CC6038 DC bit peak who be tested eet the current nice Chance re then rs. Follow et allow et ed to	d impedar 34-4No.4. as voltage shed after 16 following Ill meet the tange stored wire owing this a series 1	Add 1.0 nce shall be 13 method the plus the phall not extended to hours recognized the following table:  Within the properties of the phall not extended the pha	ds, The carated ripper accept the covering degrequire 4.3 shall 25% of all be no age appliche capactroom teresistor(11)	ther 1000 red at 120	F for DHz.  S stored at for Table orking with a specific of electron to the store of the store o	at a tempole 1. (Tovoltage) ric conditions of 105 oved from the conditions of 105 oved from th	erature of the sum of Then the tions. The the tions the test Next they d voltage

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		< Criteria > The characteristic shall meet	the following requirements
		Leakage current	Value in 4.3 shall be satisfied
	Shelf	Capacitance Change	Within $\pm 25\%$ of initial value.
4.8	life	tanδ	Not more than 150% of the specified value.
	test	Appearance	There shall be no leakage of electrolyte.
		Remark: If the capacitors are increase. Please apply voltage	e stored more than 1 year, the leakage current may the through about 1 k $\Omega$ resistor, if necessary.
4.9	Surge test	The capacitor shall be submit followed discharge of 5 min. The test temperature shall C <sub>R</sub> : Nominal Capacitance (   Criteria>  Leakage current	be 15~35℃. µ F)  Not more than the specified value.
	test	Capacitance Change	Within $\pm 15\%$ of initial value.
		tanδ	Not more than the specified value.
		Appearance	There shall be no leakage of electrolyte.
		Attention: This test simulates over voltage as often applied	age at abnormal situation only. It is not applicable to such.
4.10	Vibration test	perpendicular directions.  Vibration frequency repeak to peak amplitude Sweep rate  Mounting method:  The capacitor with diameter in place with a bracket.	e: 1.5mm: 10Hz ~ 55Hz ~ 10Hz in about 1 minute  greater than 12.5mm or longer than 25mm must be fixed  Within 30°  To be soldered

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		<condition></condition>		
		The capacitor shall be tes	ted under the following	conditions:
		Soldering temperature	: 245±3°C	
		Dipping depth	: 2mm	
4.11	Solderability	Dipping speed	: 25±2.5mn	n/s
7.11	test	Dipping time	: 3±0.5s	
		<c<u>riteria&gt;</c<u>		
		Coating quality	A minimum immersed	m of 95% of the surface being
		0 10		
		<condition></condition>	u ahall ha immanad int	to solder both at 260±5°Cfar10±
		_		to solder bath at $260\pm5$ °C for $10\pm$
				Omm from the body of capacitor .
				temperature and normal humidity
	Resistance to	for 1~2 hours before mea	surement.	
4.12	solder heat	<criteria></criteria>		
	test	Leakage current	Not more than	the specified value.
		Capacitance Change	Within ±10%	of initial value.
		tanδ	Not more than	the specified value.
		Appearance	There shall be a	no leakage of electrolyte.
		<condition></condition>		
			rding to IEC60384-4No	.4.7methods, capacitor shall be
		placed in an oven, the cor	•	-
		_	emperature	Time
		(1)+20°C	≤3 Minutes	
		` '	ature (-40°C) (-25°C)	30±2 Minutes
	Change of	(3)Rated high temper		$30\pm2$ Minutes
4.13	temperature			30±2 Willities
	test	(1)  to  (3)=1  cycle, to	tai 5 cycle	
		<criteria></criteria>	ant the following requir	romant
		The characteristic shall m		
		Leakage current	Not more than the	=
		tanδ	Not more than the	-
		Appearance	I nere shall be no le	eakage of electrolyte.
		<condition></condition>		
		Humidity Test:		
		_	_	citor shall be exposed for $500\pm8$
				°C, the characteristic change shall
		meet the following requir	ement.	
		< <u>Criteria&gt;</u>	T	
4.14	Damp heat	Leakage current	Not more than the spe	
7.14	test	Capacitance Change	Within $\pm 20\%$ of init	
		tanδ	Not more than 120%	<u> </u>
		Appearance	There shall be no leak	tage of electrolyte.

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4.15	Vent test	Condition> The following test only apply to with vent. D.C. test The capacitor is connected wit current selected from below tak <table 3=""></table>	h its polar ble is appli rent (A)	ity reversed ed.	to a DC po	ower source	. Then a
4.16	Maximum permissible (ripple current)	Condition> The maximum permissible rip at 120Hz and can be applied Table-1 The combined value of D.C v rated voltage and shall not re  Frequency Multipliers:  Coefficient Freq. (Hz) Cap. (μ F)  ~180  220~560  680~1800  2200~3900  4700	at maximu oltage and	m operating I the peak A	g temperatu	re	ceed the

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5. It refers to the latest document of "Environment-related Substances standard" (WI-HSPM-QA-072).

	Substances			
	Cadmium and cadmium compounds			
Heavy metals	Lead and lead compounds			
Heavy metais	Mercury and mercury compounds			
	Hexavalent chromium compounds			
	Polychlorinated biphenyls (PCB)			
Chloinated	Polychlorinated naphthalenes (PCN)			
organic	Polychlorinated terphenyls (PCT)			
compounds	Short-chain chlorinated paraffins(SCCP)			
	Other chlorinated organic compounds			
D 1	Polybrominated biphenyls (PBB)			
Brominated	Polybrominated diphenylethers(PBDE) (including			
organic	decabromodiphenyl ether[DecaBDE])			
compounds	Other brominated organic compounds			
Tributyltin compo	ounds(TBT)			
Triphenyltin com	pounds(TPT)			
Asbestos				
Specific azo com	pounds			
Formaldehyde				
Beryllium oxide				
Beryllium coppe	er			
Specific phthalate	es (DEHP,DBP,BBP,DINP,DIDP,DNOP,DNHP)			
Hydrofluorocarbo	on (HFC), Perfluorocarbon (PFC)			
Perfluorooctane s	sulfonates (PFOS)			
Specific Benzotri	azole			

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#### ELECTROLYTIC CAPACITOR SPECIFICATION GF SERIES

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#### **Attachment: Application Guidelines**

#### 1.Circuit Design

#### 1.1 Operating Temperature and Frequency

Electrolytic capacitor electrical parameters are normally specified at 20°C temperature and 120Hz frequency. These parameters vary with changes in temperature and frequency. Circuit designers should take these changes into consideration.

- (1) Effects of operating temperature on electrical parameters
  - a) At higher temperatures, leakage current and capacitance increase while equivalent series resistance (ESR) decreases.
  - b) At lower temperatures, leakage current and capacitance decrease while equivalent series resistance (ESR) increases.
- (2) Effects of frequency on electrical parameters
  - a) At higher frequencies capacitance and impedance decrease while  $\tan\delta$  increases.
  - b) At lower frequencies, ripple current generated heat will rise due to an increase in equivalent series resistance (ESR).

#### 1.2 Operating Temperature and Life Expectancy

See the file: Life calculation of aluminum electrolytic capacitor

#### 1.3 Common Application Conditions to Avoid

The following misapplication load conditions will cause rapid deterioration to capacitor electrical parameters. In addition, rapid heating and gas generation within the capacitor can occur causing the pressure relief vent to operate and resultant leakage of electrolyte. Under Leaking electrolyte is combustible and electrically conductive.

#### (1) Reverse Voltage

DC capacitors have polarity. Verify correct polarity before insertion. For circuits with changing or uncertain polarity, use DC bipolar capacitors. DC bipolar capacitors are not suitable for use in AC circuits.

#### (2) Charge / Discharge Applications

Standard capacitors are not suitable for use in repeating charge / discharge applications. For charge / discharge applications consult us and advise actual conditions.

#### (3) Over voltage

Do not apply voltages exceeding the maximum specified rated voltage. Voltages up to the surge voltage rating are acceptable for short periods of time. Ensure that the sum of the DC voltage and the superimposed AC ripple voltage does not exceed the rated voltage.

#### (4) Ripple Current

Do not apply ripple currents exceeding the maximum specified value. For high ripple current applications, use a capacitor designed for high ripple currents or contact us with your requirements. Ensure that allowable ripple currents superimposed on low DC bias voltages do not cause reverse voltage conditions.

#### 1.4 Using Two or More Capacitors in Series or Parallel

#### (1) Capacitors Connected in Parallel

The circuit resistance can closely approximate the series resistance of the capacitor causing an imbalance of ripple current loads within the capacitors. Careful design of wiring methods can minimize the possibility of excessive ripple currents applied to a capacitor.

#### (2) Capacitors Connected in Series

Normal DC leakage current differences among capacitors can cause voltage imbalances. The use of voltage divider shunt resistors with consideration to leakage current can prevent capacitor voltage imbalances.

#### 1.5 Capacitor Mounting Considerations

#### (1) Double Sided Circuit Boards

Avoid wiring pattern runs, which pass between the mounted capacitor and the circuit board.

When dipping into a solder bath, excess solder may collect under the capacitor by capillary action and short circuit the anode and cathode terminals.

#### (2)Circuit Board Hole Positioning

The vinyl sleeve of the capacitor can be damaged if solder passes through a lead hole for subsequently processed parts. Special care when locating hole positions in proximity to capacitors is recommended.

#### (3)Circuit Board Hole Spacing

The circuit board holes spacing should match the capacitor lead wire spacing within the specified tolerances. Incorrect spacing can cause excessive lead wire stress during the insertion process. This may result in premature capacitor failure due to short or open circuit, increased leakage current, or electrolyte leakage.

#### (4) Clearance for Case Mounted Pressure Relief vents

Capacitors with case mounted pressure relief vents require sufficient clearance to allow for proper vent operation. The minimum clearances are dependent on capacitor diameters as proper vent operation. The minimum clearances are dependent on capacitor diameters as follows.

φ6.3~φ16mm:2mm minimum, φ18~φ35mm:3mm minimum, φ40mm or greater:5mm minimum.

#### (5) Clearance for Seal Mounted Pressure Relief Vents

A hole in the circuit board directly under the seal vent location is required to allow proper release of pressure.

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#### (6) Wiring Near the Pressure Relief Vent

Avoid locating high voltage or high current wiring or circuit board paths above the pressure relief vent. Flammable, high temperature gas exceeding 100°C may be released which could dissolve the wire insulation and ignite.

(7) Circuit Board patterns Under the Capacitor

Avoid circuit board runs under the capacitor as electrolyte leakage could cause an electrical short.

(8) Screw Terminal Capacitor Mounting

Do not orient the capacitor with the screw terminal side of the capacitor facing downwards.

Tighten the terminal and mounting bracket screws within the torque range specified in the specification.

#### 1.6 Electrical Isolation of the Capacitor

Completely isolate the capacitor as follows.

- (1) Between the cathode and the case (except for axially leaded B types) and between the anode terminal and other circuit paths
- (2) Between the extra mounting terminals (on T types) and the anode terminal, cathode terminal, and other circuit paths.
- 1.7 The Product endurance should take the sample as the standard.
- 1.8 If conduct the load or shelf life test, must be collect date code within 6 months products of sampling.

#### 1.9 Capacitor Sleeve

The vinyl sleeve or laminate coating is intended for marking and identification purposes and is not meant to electrically insulate the capacitor.

The sleeve may split or crack if immersed into solvents such as toluene or xylene, and then exposed to high temperatures.

#### CAUTION!

Always consider safety when designing equipment and circuits. Plan for worst case failure modes such as short circuits and open circuits which could occur during use.

- (1) Provide protection circuits and protection devices to allow safe failure modes.
- (2) Design redundant or secondary circuits where possible to assure continued operation in case of main circuit failure.

#### 2. Capacitor Handling Techniques

- 2.1 Considerations Before Using
- (1) Capacitors have a finite life. Do not reuse or recycle capacitors from used equipment.
- (2) Transient recovery voltage may be generated in the capacitor due to dielectric absorption. If required, this voltage can be discharged with a resistor with a value of about  $1k\Omega$ .
- (3) Capacitors stored for long periods of time may exhibit an increase in leakage current. This can be corrected by gradually applying rated voltage in series with a resistor of approximately  $1k\Omega$ .
- (4) If capacitors are dropped, they can be damaged mechanically or electrically. Avoid using dropped capacitors.
- (5) Dented or crushed capacitors should not be used. The seal integrity can be compromised and loss of electrolyte / shortened life can result

#### 2.2 Capacitor Insertion

- (1) Verify the correct capacitance and rated voltage of the capacitor.
- (2) Verify the correct polarity of the capacitor before inserting.
- (3) Verify the correct hole spacing before insertion (land pattern size on chip type) to avoid stress on the terminals.
- (4) Ensure that the auto insertion equipment lead clinching operation does not stress the capacitor leads where they enter the seal of the capacitor.

For chip type capacitors, excessive mounting pressure can cause high leakage current, short circuit, or disconnection.

#### 2.3 Manual Soldering

- (1) Observe temperature and time soldering specifications or do not exceed temperatures of 400 °C for 3 seconds or less.
- (2) If lead wires must be formed to meet terminal board hole spacing, avoid stress on the lead wire where it enters the capacitor seal.
- (3) If a soldered capacitor must be removed and reinserted, avoid excessive stress to the capacitor leads.
- (4) Avoid touching the tip of the soldering iron to the capacitor, to prevent melting of the vinyl sleeve.

#### 2.4 Flow Soldering

- (1) Do not immerse the capacitor body into the solder bath as excessive internal pressure could result.
- (2) Observe proper soldering conditions (temperature, time, etc.) Do not exceed the specified limits.
- (3) Do not allow other parts or components to touch the capacitor during soldering.

#### 2.5 Other Soldering Considerations

Rapid temperature rises during the preheat operation and resin bonding operation can cause cracking of the capacitor vinyl sleeve. For heat curing, do not exceed 150 °C for a maximum time of 2 minutes.

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#### 2.6 Capacitor Handling after Solder

- (1). Avoid movement of the capacitor after soldering to prevent excessive stress on the lead wires where they enter the seal.
- (2). Do not use capacitor as a handle when moving the circuit board assembly.
- (3). Avoid striking the capacitor after assembly to prevent failure due to excessive shock.

#### 2.7 Circuit Board Cleaning

- (1) Circuit boards can be immersed or ultrasonically cleaned using suitable cleaning solvents for up 5 minutes and up to 60°C maximum temperatures. The boards should be thoroughly rinsed and dried. The use of ozone depleting cleaning agents is not recommended in the interest of protecting the environment.
- (2) Avoid using the following solvent groups unless specifically allowed for in the specification;

Halogenated cleaning solvents: except for solvent resistant capacitor types, halogenated solvents can permeate the seal and cause internal capacitor corrosion and failure. For solvent resistant capacitors, carefully follow the temperature and time requirements of the specification. 1-1-1 trichloroethane should never be used on any aluminum electrolytic capacitor.

Alkali solvents : could attack and dissolve the aluminum case.

Petroleum based solvents: deterioration of the rubber seal could result.

Xylene : deterioration of the rubber seal could result.

Acetone : removal of the ink markings on the vinvl sleeve could result.

- (3) A thorough drying after cleaning is required to remove residual cleaning solvents which may be trapped between the capacitor and the circuit board. Avoid drying temperatures, which exceed the maximum rated temperature of the capacitor.
- (4) Monitor the contamination levels of the cleaning solvents during use by electrical conductivity, pH, specific gravity, or water content. Chlorine levels can rise with contamination and adversely affect the performance of the capacitor. Please consult us for additional information about acceptable cleaning solvents or cleaning methods.

#### 2.8 Mounting Adhesives and Coating Agents

When using mounting adhesives or coating agents to control humidity, avoid using materials containing halogenated solvents. Also, avoid the use of chloroprene based polymers. After applying adhesives or coatings, dry thoroughly to prevent residual solvents from being trapped between the capacitor and the circuit board.

#### 3. Precautions for using capacitors

3.1 Environmental Conditions

Capacitors should not be stored or used in the following environments.

- (1) Temperature exposure above the maximum rated or below the minimum rated temperature of the capacitor.
- (2) Direct contact with water, salt water, or oil.
- (3) High humidity conditions where water could condense on the capacitor.
- (4) Exposure to toxic gases such as hydrogen sulfide, sulfuric acid, nitric acid chlorine, or ammonia.
- (5) Exposure to ozone, radiation, or ultraviolet rays.
- (6) Vibration and shock conditions exceeding specified requirements.

#### 3.2 Electrical Precautions

- (1) Avoid touching the terminals of the capacitor as possible electric shock could result. The exposed aluminum case is not insulated and could also cause electric shock if touched.
- (2) Avoid short circuit the area between the capacitor terminals with conductive materials including liquids such as acids or alkaline solutions.

#### 4. Emergency Procedures

- (1) If the pressure relief vent of the capacitor operates, immediately turn off the equipment and disconnect form the power source. This will minimize additional damage caused by the vaporizing electrolyte.
- (2) Avoid contact with the escaping electrolyte gas which can exceed 100°C temperatures.

If electrolyte or gas enters the eye, immediately flush the eyes with large amounts of water.

If electrolyte or gas is ingested by month, gargle with water.

If electrolyte contacts the skin, wash with soap and water.

#### 5. Long Term Storage

Leakage current of a capacitor increases with long storage times. The aluminum oxide film deteriorates as a function of temperature and time. If used without reconditioning, an abnormally high current will be required to restore the oxide film. This current surge could cause the circuit or the capacitor to fail. After one year, a capacitor should be reconditioned by applying rated voltage in series with a  $1000\Omega$ , current limiting resistor for a time period of 30 minutes . If the expired date of products date code is over eighteen months, the products should be return to confirmation.

#### 5.1 Environmental Conditions

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The capacitor shall be not use in the following condition:

- (1) Temperature exposure above the maximum rated or below the minimum rated temperature of the capacitor.
- (2) Direct contact with water, salt water, or oil.
- (3) High humidity conditions where water could condense on the capacitor.
- (4) Exposure to toxic gases such as hydrogen sulfide, sulfuric acid, nitric acid, chlorine, or ammonia.
- (5) Exposure to ozone, radiation, or ultraviolet rays.
- (6) Vibration and shock conditions exceeding specified requirements.

#### 6. Capacitor Disposal

When disposing of capacitors, use one of the following methods.

Incinerate after crushing the capacitor or puncturing the can wall (to prevent explosion due to internal pressure rise). Capacitors should be incinerated at high temperatures to prevent the release of toxic gases such as chlorine from the polyvinyl chloride sleeve, etc.

Dispose of as solid waste.

NOTE: Local laws may have specific disposal requirements, which must be followed.

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